

Enhancing the Flexibility and Automation of Post-Quantum Anonymous Credentials: A Comparative Analysis of Zero-Knowledge Virtual Machines and SNARK Circuit Compilers

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Abstract

The advent of quantum computing demands a rapid transition to post-quantum cryptographic solutions. In digital identity, SNARK-friendly schemes like Loquat[?] underpin post-quantum anonymous credential systems such as BDEC[?]. However, BDEC's reliance on static, custom zkSNARK[?] circuits for credential verification leads to critical inflexibility, rendering it impractical for dynamic attribute management. Zero-Knowledge Virtual Machines[?] (zkVMs) promise a solution, offering to prove arbitrary programs and transform complex circuits into high-level code updates. This research will investigate the specific zero-knowledge properties of different zkVMs through comparative analysis of zkVMs and alternative SNARK circuit compilers[?], implementing and benchmarking the BDEC verifier within both approaches. This quantitative and qualitative analysis will determine which approach offers a more viable and agile foundation for the next generation of digital identity systems, specifically addressing the trade-offs between flexibility, performance with concrete metrics such as prover time, verification time, and memory usage.

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Notation

Let $\lambda \in \mathbb{N}$ be the security parameter, $\text{negl}(\lambda)$ denote a negligible function, and PPT stand for probabilistic polynomial-time algorithms.

We use pp to denote public parameters, and sk, pk for secret and public keys, respectively. The symbol crs represents a common reference string typically used in zero-knowledge proof systems, and pk, vk refer to the proving and verifying keys of zkSNARKs. The public input to a zkSNARK is denoted x , the private witness as ω , and the proof as π . For digital signatures, m is the message, and σ is its signature. The relation or circuit verified by zkSNARKs is expressed as $C(x, \omega)$.

Regarding anonymous credentials, \mathcal{A} denotes the universal set of attributes, with attr and subattr as subsets of \mathcal{A} . A credential is represented by cred , and a shown credential by show . The logical statement or predicate proved on attributes is stmt , and auxiliary descriptions by aux .

For the Loquat post-quantum signature scheme, $L\text{-pp}$ denotes its specific public parameters, and H the collision-resistant hash functions it uses. We denote $R1CS$ as the Rank-1 Constraint System representation of arithmetic circuits. The prime field used for the Legendre PRF is \mathbb{F}_p , and $\mathcal{L}(\cdot)$ the Legendre symbol pseudorandom function.

2.1 Cryptographic Primitives

2.1.1 zkSNARKs

A zk-SNARK (more commonly referred to as Zero-Knowledge Succinct Non-Interactive Argument of Knowledge) [?, ?, ?] is a cryptographic proof system that enables a *prover* to convince a *verifier* that they know a secret witness ω satisfying a publicly-known statement $C(x, \omega) = \text{true}$, without revealing ω , in a succinct and non-interactive format. Concretely, a zk-SNARK consists of the following algorithms,

1. $\text{Setup}(1^\lambda) \rightarrow (\text{pk}, \text{vk})$,
2. $\text{Prove}(\text{pk}, x, \omega) \rightarrow \pi$,
3. $\text{Verify}(\text{vk}, x, \pi) \in \{0, 1\}$,

where λ is the security parameter, x is the public input and ω the private witness.

The zk-SNARK system should satisfy the following properties:

Completeness. If $C(x, \omega) = \text{true}$, then for an honest *prover*

$$\Pr \left[\text{Verify}(\text{vk}, x, \pi) = 1 \mid \pi \leftarrow \text{Prove}(\text{pk}, x, \omega) \right] \geq 1 - \text{negl}(\lambda).$$

Soundness. For any probabilistic polynomial-time adversary Adv ,

$$\Pr \left[\text{Verify}(\text{vk}, x, \pi) = 1 \wedge \neg \exists \omega' : C(x, \omega') = \text{true} \mid \begin{array}{l} (\text{pk}, \text{vk}) \leftarrow \text{Setup}(1^\lambda), \\ (x, \pi) \leftarrow \text{Adv}(\text{pk}) \end{array} \right] \leq \text{negl}(\lambda).$$

Moreover, there exists an extractor \mathcal{E} such that if Adv outputs an accepting proof (x, π) with non-negligible probability, then $\mathcal{E}(\text{Adv's state})$ outputs a valid ω' satisfying $C(x, \omega') = \text{true}$.

Zero-Knowledge. There exists a simulator Sim that, given only the verification key vk and a public input x with $C(x, \cdot)$ satisfiable, produces a proof π^* such that the distribution

$$(x, \pi) = (x, \pi \leftarrow \text{Prove}(\text{pk}, x, \omega))$$

is computationally indistinguishable from $(x, \pi^*) = (x, \pi^* \leftarrow \text{Sim}(\text{vk}, x))$.

Succinctness. The size of the proof π is short, typically $O(\text{polylog}(|C|))$ or otherwise "sub-linear" in the size of the circuit representing C ; the *verifier's* running time is similarly efficient (e.g., $O(|x| + \text{polylog}(|C|))$).

2.1.2 Digital Signatures

A digital signature scheme is composed of the following tuple of PPT algorithms $\text{Setup}(1^\lambda)$, $\text{KeyGen}(\text{pp})$, $\text{Sign}(sk, m)$, and $\text{Verify}(pk, m, \sigma)$.

Definition 2.1 (EUF-CMA Security). A digital signature scheme Σ is existentially unforgeable under chosen-message attacks (EUF-CMA) if for all PPT adversaries \mathcal{A} with access to a signing oracle $\mathcal{O}_{\text{Sign}}$, the probability that \mathcal{A} outputs a pair (m^*, σ^*) such that $\text{Verify}(\text{pk}, m^*, \sigma^*) = 1$ and m^* was never queried to $\mathcal{O}_{\text{Sign}}$ is negligible in λ .

In this work, we instantiate the above notion with the *Loquat* post-quantum signature scheme.

Definition 2.2 (Loquat: A SNARK-Friendly Post-Quantum Signature). Loquat [?] is a digital signature scheme post-quantum secure under collision-resistant hashes and Legendre PRF, where

$$\begin{aligned} (\text{L-pp}) &\leftarrow \text{L-Setup}(1^\lambda), \\ (sk, pk) &\leftarrow \text{L-KeyGen}(\text{L-pp}), \\ \sigma &\leftarrow \text{L-Sign}(\text{L-pp}, sk, m), \\ \{0, 1\} &\leftarrow \text{L-Verify}(pk, m, \sigma, \text{L-pp}). \end{aligned}$$

Security. Loquat is proven EUF-CMA secure in the random-oracle model under the hardness of breaking the underlying Legendre PRF and the collision resistance of H [?].

A crucial property for this work is that the Loquat verification algorithm admits an efficient rank-1 constraint system (R1CS) representation.

SNARK-friendliness. For the Loquat-128 parameter set instantiated with the Griffin hash function, the verification circuit can be represented using approximately 1.49×10^5 R1CS constraints [?]. This is significantly smaller than known SNARK encodings of lattice-based post-quantum signature schemes such as CRYSTALS-Dilithium at comparable security levels [?], and thus makes Loquat particularly suitable for use inside zkSNARK circuits.

2.2 Anonymous Credentials

Definition 2.3 (Anonymous Credential System). An anonymous credential system over an attribute universe \mathcal{A} is a tuple of PPT algorithms $\text{AC.Setup}(1^\lambda)$, $\text{AC.KeyGen}(\text{pp})$, $\text{AC.Issue}(\text{isk}, \text{ipk}, \text{attr} \subseteq \mathcal{A}, \text{aux})$, $\text{AC.Show}(\text{cred}, \text{subattr} \subseteq \text{attr}, \text{stmt})$, and $\text{AC.Verify}(\text{pk}_I, \text{show}, \pi, \text{subattr}, \text{stmt}, \text{aux})$.

Definition 2.4 (BDEC: Post-Quantum Blockchain-based Digital Education Credential). BDEC is a post-quantum anonymous credential system designed to securely and privately verify educational achievements on a blockchain. It builds upon generic anonymous credentials and ensures the following properties:

- **Unforgeability**: No adversary can forge valid credentials.
- **Anonymity**: Credentials hide the user's identity.
- **Unlinkability**: Different proofs by the same user cannot be linked.
- **Conditional Linkability**: Selective linking enables managing fragmented learning records.
- **Revocation**: Credentials can be revoked if compromised.

2.2.1 Static Circuit Limitation

BDEC fixes circuit size to maximum attributes $|A| \leq N$ (e.g., $N = 32$). Dynamic $|A|$ requires new $\text{Setup}(N')$ per update, breaking efficiency.

2.3 Dynamic Approaches

2.3.1 Zero-Knowledge Virtual Machines

2.3.2 SNARK Circuit Compilers

3 Dynamism

Definition 3.1 (Dynamic Zero-Knowledge Proof System for Credentials). Let $\lambda \in \mathbb{N}$ be the security parameter. Let $\{\mathcal{W}_n\}_{n \in \mathbb{N}}$ be a family of witness spaces, where $\mathcal{W}_n \subseteq \{0, 1\}^*$ collects all admissible witnesses of "size" n (e.g., number of attributes or program state size). Let $\mathcal{X} \subseteq \{0, 1\}^*$ be the statement space, and let $\{R_n\}_{n \in \mathbb{N}}$ be a family of polynomial-time decidable relations

$$R_n \subseteq \mathcal{X} \times \mathcal{W}_n$$

such that $(x, w) \in R_n$ formalises "credential program P is correct on input w of size n for statement x ".

A dynamic zero-knowledge proof system for $\{R_n\}_{n \in \mathbb{N}}$ is a tuple of probabilistic polynomial-time algorithms

$$\Pi = (\text{Setup}, \text{Prove}, \text{Verify})$$

with the following syntax.

- Common reference string: $\text{crs} \leftarrow \text{Setup}(1^\lambda)$.
- Proof generation: $\pi \leftarrow \text{Prove}(\text{crs}, x, w)$ for $(x, w) \in R_n$.
- Verification: $b \leftarrow \text{Verify}(\text{crs}, x, \pi)$, where $b \in \{0, 1\}$.

The system Π is dynamic if, for all $n \in \mathbb{N}$, all $x \in \mathcal{X}$, and all $w \in \mathcal{W}_n$, the following properties hold.

1. **Completeness.** For every PPT algorithm that honestly samples (x, w) with $(x, w) \in R_n$,

$$\Pr [\text{Verify}(\text{crs}, x, \pi) = 1 : \text{crs} \leftarrow \text{Setup}(1^\lambda), \pi \leftarrow \text{Prove}(\text{crs}, x, w)] = 1 - \text{negl}(\lambda).$$

2. **Soundness.** For every PPT adversary \mathcal{A} there exists a PPT extractor \mathcal{E} such that

$$\Pr [\text{Verify}(\text{crs}, x, \pi) = 1 \wedge (x, w) \notin R_n]$$

is negligible in λ , where the probability is over $\text{crs} \leftarrow \text{Setup}(1^\lambda)$, $(x, \pi) \leftarrow \mathcal{A}(\text{crs})$, and the internal randomness of all algorithms. Moreover, whenever $\text{Verify}(\text{crs}, x, \pi) = 1$, the extractor $\mathcal{E}^{\mathcal{A}}$ outputs $w \in \mathcal{W}_n$ with $(x, w) \in R_n$ except with negligible probability.

3. **Zero-knowledge.** There exists a PPT simulator Sim such that, for every PPT distinguisher \mathcal{D} , every n , every polynomial-time samplable distribution over (x, w) with $(x, w) \in R_n$,

$$\left| \Pr [\mathcal{D}(x, \pi) = 1] - \Pr [\mathcal{D}(x, \pi') = 1] \right|$$

is negligible in λ , where in the first probability $\pi \leftarrow \text{Prove}(\text{crs}, x, w)$ and in the second $\pi' \leftarrow \text{Sim}(\text{crs}, x)$, both under $\text{crs} \leftarrow \text{Setup}(1^\lambda)$.

4. **Unlinkability across dimensions.** Consider any PPT adversary \mathcal{A} interacting with an oracle that, on input a size parameter n and an access pattern (e.g., which subset of attributes is disclosed), returns a simulated proof for some distribution of (x, w) over R_n . The view of \mathcal{A} when interacting with this oracle for size n is computationally indistinguishable from its view when interacting with the same oracle for size n' , for any n, n' of the same polynomial order in λ , even if the underlying credential belongs to the same user. Informally, the distribution of proofs for a given credential is independent (up to negligible terms) of the chosen n and of which attribute subsets are disclosed.

5. **Dynamic support for varying witness size.** The size of the common reference string $|\text{crs}|$, the verifier's running time $T_{\text{Verify}}(\lambda, n)$, and the proof length $|\pi(\lambda, n)|$ are bounded by polynomials in λ and n , and the setup algorithm Setup does not depend on n . That is, a single set of public parameters supports all relations R_n without re-running setup or re-issuing credentials.

In the context of BDEC instantiated with Loquat and a zkSNARK (or zkVM), the family $\{R_n\}_{n \in \mathbb{N}}$ represents verification of a credential with n attributes (and associated system state), and a proof system is dynamic if it satisfies the above properties simultaneously for all n using a single setup and without re-signing or recompiling verification circuits.

We now present two compilation strategies for BDEC verification. Algorithm ?? leverages zkVMs to achieve true dynamism per Definition ??, while Algorithms ?? and ?? use circuit compilers to create a family of SNARKs—one per attribute count n .

[H] Compilation of BDEC Verification into a RISC Zero zkVM

[1]

BDEC = (Setup, PriGen, NymKey, CreGen, CreVer, ShowCre, ShowVer, RevCre), Loquat = (Loquat.Setup, Loquat.

Guest program P_{BDEC} , NP relations $\{R_n\}_{n \in \mathbb{N}}$, Dynamic proof system $\Pi = (\text{Setup}, \text{Prove}, \text{Verify})$

Witness and Statement Formats Fix maximum attribute count N , with $n \leq N$. $W_n \leftarrow (a_1, \dots, a_n, \text{mt_path}_1, \dots, \text{mt_path}_n)$
 $x \leftarrow (\text{pk}_U, \text{pp}, \text{cred}, \sigma, \text{policy}, n)$

Guest Verifier $P_{\text{BDEC}}(x, w)$ $\text{root}' \leftarrow \text{MerkleRoot}(a_i, \text{mt_path}_i)$ $\text{root}' \neq \text{cred.root} \leftarrow \text{FAIL}$ $\text{Loquat.Verify}(\text{pk}_U, \text{root}', \sigma) = 0 \leftarrow \text{FAIL}$ $\text{policy}(a_1, \dots, a_n) = \text{false} \leftarrow \text{FAIL}$ $\text{REVOCATIONCHECK}(x.\text{revdata}, \text{cred}) \text{ fails} \leftarrow \text{FAIL}$
 $\leftarrow \text{OK}$

Compilation to zkVM Method $\text{mid}_{\text{BDEC}} \leftarrow \text{compile}(P_{\text{BDEC}})$

NP Relations $(x, w) \in R_n \iff \text{running } P_{\text{BDEC}} \text{ inside the zkVM with method ID } \text{mid}_{\text{BDEC}} \text{ commits OK.}$

Dynamic Proof System Π Setup : $\text{crs} \leftarrow \text{zkVM parameters (STARK params, hash, method ID)}$

Prove : $\pi \leftarrow \text{zkVM receipt of execution on } (x, w)$ Verify : check π , mid_{BDEC} , journal output

Dynamicity and Security n only changes witness shape; crs and mid_{BDEC} remain fixed. Security inherits zkVM STARK soundness and zero-knowledge. Journal leaks only OK/FAIL (unlinkable). Algorithm ?? achieves true dynamism per Definition ?. The STARK parameters crs and method ID mid_{BDEC} remain fixed regardless of n , satisfying property 5. The journal output mechanism ensures unlinkability across dimensions (property 4).

[H] Compilation of BDEC Verification into a SNARK Circuit Compiler (Part 1: Circuit Synthesis)

[1]

BDEC, Loquat, SNARK circuit compiler (e.g. zkLLVM or Circom) Circuit family $\{C_n\}_{n \in \mathbb{N}}$, relations $\{R_n\}_{n \in \mathbb{N}}$, proof system Π_{circ}

Witness and Statement Formats Fix maximum attribute count N , with $n \leq N$. $W_n \leftarrow (a_1, \dots, a_n, \text{mt_path}_1, \dots, \text{mt_path}_n)$
 $x \leftarrow (\text{pk}_U, \text{pp}, \text{cred}, \sigma, \text{policy}, n)$

High-Level Verifier Logic $P_{\text{BDEC}}(x, w)$ $\text{root}' \leftarrow \text{MerkleRoot}(a_i, \text{mt_path}_i)$ $\text{root}' \neq \text{cred.root} \leftarrow \text{FAIL}$ $\text{Loquat.Verify}(\text{pk}_U, \text{root}', \sigma) = 0 \leftarrow \text{FAIL}$ $\text{policy}(a_1, \dots, a_n) = \text{false} \leftarrow \text{FAIL}$ $\text{REVOCATIONCHECK}(x.\text{revdata}, \text{cred}) \text{ fails} \leftarrow \text{FAIL}$
 $\leftarrow \text{OK}$

Circuit Synthesis per Attribute Bound $n = 1$ to N Specialise P_{BDEC} to fixed arity n (unroll loops over a_1, \dots, a_n) Use the circuit compiler to generate an arithmetic circuit C_n for: Input wires for public x and private w Subcircuits for Merkle root recomputation Subcircuit for Loquat.Verify

Subcircuit for policy evaluation Optional revocation checks Obtain constraint system (e.g. R1CS) for C_n

[H] Compilation of BDEC Verification into a SNARK Circuit Compiler (Part 2: NP Relations & Security) [1]

NP Relations $n = 1$ to N $(x, w) \in R_n \iff C_n(x, w)$ outputs OK (the circuit's output bit is 1)

SNARK Setup and Proof System $\Pi_{\text{circ}} \ n = 1$ to N $\text{crs}_n \leftarrow \text{SNARK.Setup}(1^\lambda, C_n)$ Define: $\text{Setup}_{\text{circ}} : \text{output } \{\text{crs}_n\}_{n=1}^N$ $\text{Prove}_{\text{circ}}(\text{crs}_n, x, w) : \pi \leftarrow \text{SNARK.Prove}(\text{crs}_n, C_n, x, w)$ $\text{Verify}_{\text{circ}}(\text{crs}_n, x, \pi) : b \leftarrow \text{SNARK.Verify}(\text{crs}_n, C_n, x, \pi)$

Dynamicity and Security Characteristics Each n has its own circuit C_n and CRS crs_n (setup is circuit-specific). Changing n generally requires switching to a different (C_n, crs_n) pair. Security (completeness, soundness, ZK) inherits from the underlying SNARK and Loquat/BDEC proofs.

Algorithms ?? and ?? produce a proof system family, but not a single dynamic system. Each n requires its own circuit C_n and CRS crs_n , violating property 5 of Definition ?. This approach trades dynamism for per-instance efficiency: smaller proofs and faster verification per fixed n .

3.1 Logic Updates and Benchmarking

[H] Logic Update Workflow for BDEC Verification

[1]

Original verification logic \mathcal{L} , updated logic \mathcal{L}' , zkVM method P_{BDEC} , circuit family $\{C_n\}$ Updated zkVM method and/or circuit(s), recorded update cost $C_{\Delta\text{logic}}$

Update in zkVM Architecture Modify high-level Rust code from \mathcal{L} to \mathcal{L}' (e.g., change policy, add attributes) Recompile guest program to obtain new method binary P'_{BDEC} $\text{mid}'_{\text{BDEC}} \leftarrow \text{compile}(P'_{\text{BDEC}})$ Record engineering steps (code changes, compile time) as part of $C_{\Delta\text{logic}, \text{zkVM}}(\mathcal{L} \rightarrow \mathcal{L}')$ Note: underlying zkVM proof parameters (STARK params, hash) remain unchanged

Update in SNARK Compiler Architecture each n in the supported range Modify verifier description for C_n to reflect \mathcal{L}' (e.g., new policy, new checks) Re-run circuit compilation to obtain updated circuit C'_n SNARK is preprocessing $\text{crs}'_n \leftarrow \text{SNARK.Setup}(1^\lambda, C'_n)$ Record engineering steps (circuit changes, compilation time, setup time) as $C_{\Delta\text{logic}, \text{circ}}(\mathcal{L} \rightarrow \mathcal{L}')$

Output Return updated zkVM method ID $\text{mid}'_{\text{BDEC}}$ and updated circuit family $\{C'_n\}$ together with measured $C_{\Delta\text{logic}}$ for both architectures

Algorithm ?? quantifies the engineering cost $C_{\Delta\text{logic}}$ of updating verification logic $\mathcal{L} \rightarrow \mathcal{L}'$. For zkVM, this requires $O(1)$ re-compilation to obtain $\text{mid}'_{\text{BDEC}}$ with no change to crs . For circuit-SNARK with preprocessing, all N circuits must be re-compiled and N new trusted setups executed.

[H] Benchmark and Metric Collection for Dynamic BDEC Verification

[1]

zkVM construction (Alg. ??), circuit construction (Alg. ??), test set of (n, policy) pairs Measured metrics: $T_{\text{prove}}, T_{\text{verify}}, |\pi|$, constraint counts, ρ , Δ -values

Select Test Instances Choose a finite set $\mathcal{T} = \{(n_i, \text{policy}_i)\}$ of attribute sizes and policies each $(n_i, \text{policy}_i) \in \mathcal{T}$ Sample or construct representative witnesses w and statements x for BDEC+Loquat with n_i attributes

Run zkVM Benchmarks Start timer; run $\pi_{\text{zkVM}} \leftarrow \text{Prove}_{\text{zkVM}}(\text{crs}, x, w)$ Stop timer; record $T_{\text{prove}, \text{zkVM}}(n_i)$ Measure proof size $|\pi_{\text{zkVM}}(n_i)|$ Start timer; run $\text{Verify}_{\text{zkVM}}(\text{crs}, x, \pi_{\text{zkVM}})$ Stop timer; record $T_{\text{verify}, \text{zkVM}}(n_i)$

Extract or estimate constraint count $C_{\text{zkVM}}(\mathcal{L}, n_i)$ from zkVM tooling or documentation Compute $\rho_{\text{zkVM}}(\mathcal{L}, n_i) = \frac{C_{\text{zkVM}}(\mathcal{L}, n_i)}{C_{\text{min}}(\mathcal{L}, n_i)}$

Run Circuit-SNARK Benchmarks Select corresponding circuit C_{n_i} and CRS crs_{n_i} Start timer; run $\pi_{\text{circ}} \leftarrow \text{Prove}_{\text{circ}}(\text{crs}_{n_i}, x, w)$ Stop timer; record $T_{\text{prove}, \text{circ}}(n_i)$ Measure proof size $|\pi_{\text{circ}}(n_i)|$ Start timer; run $\text{Verify}_{\text{circ}}(\text{crs}_{n_i}, x, \pi_{\text{circ}})$ Stop timer; record $T_{\text{verify}, \text{circ}}(n_i)$ Obtain constraint count $C_{\text{circ}}(\mathcal{L}, n_i)$ from the compiler (e.g. R1CS size) Compute $\rho_{\text{circ}}(\mathcal{L}, n_i) = \frac{C_{\text{circ}}(\mathcal{L}, n_i)}{C_{\text{min}}(\mathcal{L}, n_i)}$

Compute Upgrade / Dynamism Metrics For successive n_i, n_j in \mathcal{T} , compute: $\Delta T_{\text{prove}}, \Delta T_{\text{verify}}, \Delta|\pi|, \Delta C$ for zkVM and circuit paths Use these to instantiate dynamism and agility measures (e.g. $C_{\Delta \text{logic}}$, scaling in n)

Output Return all recorded metrics for use in tables, plots, and analysis

Algorithm ?? provides empirical validation of dynamism properties. The measured metrics $\rho_{\text{zkVM}}(L, n)$ and $\rho_{\text{circ}}(L, n)$ quantify circuit overhead, while $\Delta T_{\text{prove}}, \Delta T_{\text{verify}}$, and $\Delta|\pi|$ measure scaling costs.

4 Evaluation

5 Discussion

6 Conclusion

Acknowledgement

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A Proof of Theorem