module h\_counter(clk,h\_count,trig\_v);

input clk;

output [9:0] h\_count;

output trig\_v;

reg trig\_v;

reg [9:0]h\_count;

initial h\_count=0;

initial trig\_v = 0;

always @ (posedge clk)

begin

if (h\_count<=798)

begin

h\_count = h\_count + 1;

trig\_v =0;

end

else

begin

trig\_v=1;

h\_count=0;

end

end

endmodule