```
{
00000045   PUSH R1          Push register on stack
00000046   PUSH R0          Push register on stack
00000047   IN R0,0x3F          In from I/O location
00000048   PUSH R0          Push register on stack
00000049   CLR R1          Clear Register
0000004A   PUSH R24          Push register on stack
0000004B   PUSH R25          Push register on stack
    TCNT0 = -30;
0000004C   LDI R24,0xE2       Load immediate
0000004D   OUT 0x32,R24       Out to I/O location
    PORTB ^= 0X40;
0000004E   IN R25,0x18        In from I/O location
0000004F   LDI R24,0x40       Load immediate
00000050   EOR R24,R25        Exclusive OR
00000051   OUT 0x18,R24       Out to I/O location
}
00000052   POP R25          Pop register from stack
00000053   POP R24          Pop register from stack
00000054   POP R0          Pop register from stack
00000055   OUT 0x3F,R0          Out to I/O location
00000056   POP R0          Pop register from stack
00000057   POP R1          Pop register from stack
00000058   RETI       Interrupt return
{
```