

```

{
00000059  PUSH R1          Push register on stack
0000005A  PUSH R0          Push register on stack
0000005B  IN R0,0x3F       In from I/O location
0000005C  PUSH R0          Push register on stack
0000005D  CLR R1           Clear Register
0000005E  PUSH R24         Push register on stack
0000005F  PUSH R25         Push register on stack

    TCNT2 = -60;

00000060  LDI R24,0xC4     Load immediate
00000061  OUT 0x24,R24     Out to I/O location

    PORTB ^= 0X20;

00000062  IN R25,0x18      In from I/O location
00000063  LDI R24,0x20     Load immediate
00000064  EOR R24,R25      Exclusive OR
00000065  OUT 0x18,R24     Out to I/O location
}

00000066  POP R25          Pop register from stack
00000067  POP R24          Pop register from stack
00000068  POP R0           Pop register from stack
00000069  OUT 0x3F,R0      Out to I/O location
0000006A  POP R0           Pop register from stack
0000006B  POP R1           Pop register from stack
0000006C  RETI             Interrupt return

```