



# **EGCO334: Microprocessor and Interfacing**

**AVR C Programming** 





### **Outline**

- AVR C Programming
- AVR GCC Inline Assembly





### **High Level Languages**

Easy to develop and update

#### **C** Language

- Acceptable performance
- Easy to develop and update
- Portable

### **Low Level Languages**

- High performance
- Not portable



High Level Languages (like VB)

C Language

Machine Language







**AVR Libs** 

; store SREG value

; restore SREG value (I-bi

; disable interrupts during timed sec

sbi EECR, EEMPE ; start EEPROM write

Assembly Code Example

sbi EECR, EEPE

out SREG, r16

C++ Libs

Libs

C++ Libs

C/C++ (Readable Code)

Assembly (Readable Code)

Machine Language (Binary Code)

```
#define F_CPU 1000000
#include <avr/io.h>
#include <util/delay.h>
#include "../leah_library/pin_macros.h"
int main (void)
       b00utput();
       for(;;)
               b0High();
                _delay_ms(1000);
               b0Low();
               _delay_ms(1000);
       return 0;
```

:100020000C947E000C947E000C947E000C947E0058



### Regular C programming

Write a program that calculate the sum of {1,3,...,13,15}

```
int main ()
{
    unsigned int sum;

for (int i = 1; i <= 15; i+=2)
    sum += i;

while (1);
    return 0;
}</pre>
```



### AVR C program to send value 0xAA to PORTD

```
#include <avr/io.h>
int main ()
{
   DDRD = 0xFF;
   PORTD = 0xAA;

   while (1);
   return 0;
}
```



### **Data Types**

Table 7-1: Some Data Types Widely Used by C compilers					
Data Type	Size in Bits	Data Range/Usage			
unsigned char	8-bit	0 to 255			
char	8-bit	-128 to +127			
unsigned int	16-bit	0 to 65,535			
int	16-bit	-32,768 to +32,767			
unsigned long	32-bit	0 to 4,294,967,295			
long	32-bit -2,14	47,483,648 to +2,147,483,648			
float	32-bit	$\pm 1.175e-38$ to $\pm 3.402e38$			
double	32-bit	$\pm 1.175$ e-38 to $\pm 3.402$ e38			



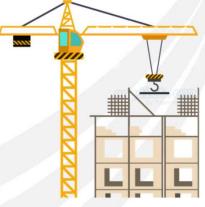
### **Data Types**

Traditional name	Portable name	# Bytes	Min	Max
signed char	int8_t	1	-128	+127
unsigned char	uint8_t	1	0	255
signed int	int16_t	2	-32768	32767
unsigned int	uint16_t	2	0	65535
signed long	int32_t	4	-2147483648	2147483647
unsigned long	uint32_t	4	0	4294967295



### **AVR C Programming**

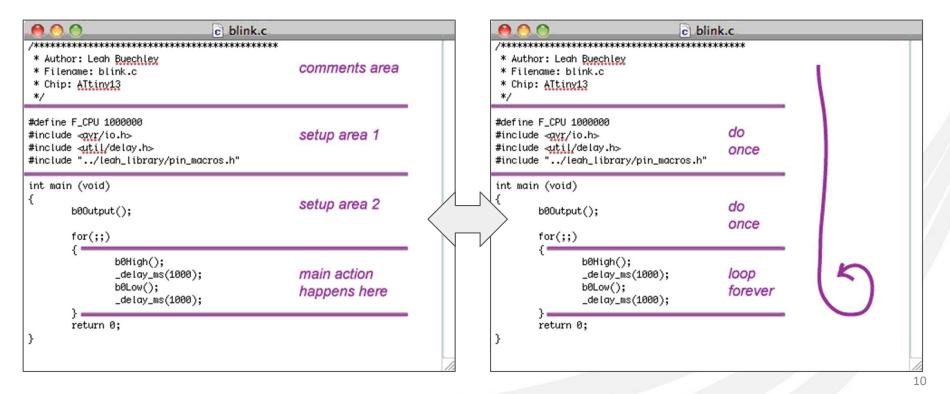
# Everything is same as regular C programming style. Except the coding structure







### **Coding Structure**

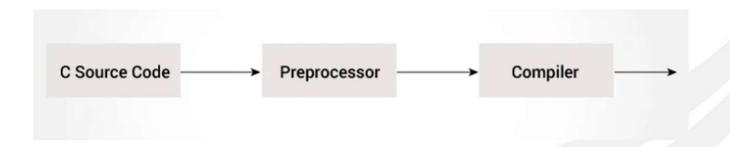




### **Preprocessor**

The C **preprocessor** is a macro processor that is used automatically by the C compiler to transform your program <u>before actual compilation</u>.

It is called a macro processor because it allows you to define macros, which are brief abbreviations for longer constructs.





### **AVR Preprocessor**

#assert Assertions

#cpu Assertions

#define Macros with Arguments

#elif The `#elif' Directive

#else The `#else' Directive

#error The `#error' and `#warning' Directives

#ident Miscellaneous Preprocessing Directives

<sup>\*</sup>Assertions are a more systematic alternative to macros in writing conditionals to test what sort of computer or system the compiled program will run on



### **AVR Preprocessor**

#if Syntax of Conditionals

#ifdef Conditionals and Macros

#ifndef Conditionals and Macros

#import Once-Only Include Files

#include The `#include' Directive

#include\_next Inheritance and Header Files

#line Combining Source Files



### **AVR Preprocessor**

#machine Assertions

#pragma Miscellaneous Preprocessing

**Directives** 

#pragma once Once-Only Include Files

#system Assertions

#unassert Assertions

#warning The `#error' and `#warning'

**Directives** 



### **AVR Preprocessor**

**#define:** is a macro that requires arguments, you write a `#define' directive with a list of argument names in parentheses after the name of the macro.

### **Example**

- #define PI 3.14
- #define circleArea(r) (3.1415\*(r)\*(r))
- #define min(X, Y) ((X) < (Y) ? (X) : (Y))</li>



### **AVR Preprocessor**

#if, #else, #elif: expresses the condition for preprocessing (logically similar as using in C programming).

Kindly note, #endif is not required to be used for ending #if condition

#### **Syntax**

```
#if expression
Text if true
#elif expression
Text if true
#else
Text if true
#endif
```



### **AVR Preprocessor**

```
#if, #else, #elif

Example

#if X == 1
...

#elif X == 2
...

#else /* X != 2 and X != 1*/
...

#endif /* X != 2 and X != 1*/
```



### **AVR Preprocessor**

#### #ifdef, #ifndef:

- #ifdef name → is equivalent to `#if defined (name)'.
- #ifndef name → is equivalent to `#if! defined (name)'.



### **AVR Preprocessor**

#### #include

- #include <file>: searches for a file named file in a list of directories specified by you, then in a standard list of system directories
- #include "file": searches for a file named file first in the current directory



### **AVR Preprocessor**

#### #include

- #include <file>: searches for a file named file in a list of directories specified by you, then in a standard list of <u>system directories</u>
- #include "file": searches for a file named file first in the <u>current</u> <u>directory</u>



#### **Libraries**

- avr/ioXXXX.h
   This header file includes the appropriate IO definitions for the device
- util/delay.h
   The convenience (busy wait) functions where actual time values can be specified rather than a number of cycles to wait for
- avr/interrupt.h
   Interrupt handling functions
- regular C library
  - o string.h o math.h
  - o string.h o stlib.h





#### **Libraries**

```
#define DDRB SFR IO8(0x04)
                                                                        #define PORTB SFR IO8(0x05)
#define PINB SFR IO8(0x03)
                                    #define DDB0 \overline{0}
                                                                        #define PORTB0 0
#define PINB0 0
                                    #define DDB1 1
                                                                        #define PORTB1 1
#define PINB1 1
                                    #define DDB2 2
#define PINB2 2
                                                                        #define PORTB2 2
#define PINB3 3
                                    #define DDB3 3
                                                                        #define PORTB3 3
#define PINB4 4
                                    #define DDB4 4
                                                                        #define PORTB4 4
#define PINB5 5
                                    #define DDB5 5
                                                                        #define PORTB5 5
                                    #define DDB6 6
#define PINB6 6
                                                                        #define PORTB6 6
                                    #define DDB7 7
#define PINB7 7
                                                                        #define PORTB7 7
```

PINB	0x23	PINB7	PINB6	PINB5	PINB4	PINB3	PINB2	PINB1	PINB0
DDRB	0x24	DDRB7	DDRB6	DDRB5	DDRB4	DDRB3	DDRB2	DDRB1	DDRB0
PORTB	0x25	PORTB7	PORTB6	PORTB5	PORTB4	PORTB3	PORTB2	PORTB1	PORTB0





#### **Libraries**

```
#define PORTB _SFR_IO8(0x05)
#define PORTB0 0
                                      #define DDRB SFR IO8 (0x04)
#define PINB _SFR_IO8(0x03
                                      #define DDB0 \overline{0}
#define PINB0 0
                                      #define DDB1 1
                                                                            #define PORTB1 1
#define PINB1 1
                                      #define DDB2 2
                                                                            #define PORTB2 2
#define PINB2 2
#define PINB3 3
                                      #define DDB3 3
                                                                            #define PORTB3 3
#define PINB4 4
                                      #define DDB4 4
                                                                            #define PORTB4 4
#define PINB5 5
                                      #define DDB5 5
                                                                            #define PORTB5 5
                                      #define DDB6 6
#define PINB6 6
                                                                            #define PORTB6 6
                                      #define DDB7 7
#define PINB7 7
                                                                            #define PORTB7 7
```

PINB	0x23	PINB7	PINB6	PINB5	PINB4	PINB3	PINB2	PINB1	PINB0
DDRB	0x24	DDRB7	DDRB6	DDRB5	DDRB4	DDRB3	DDRB2	DDRB1	DDRB0
PORTB	0x25	PORTB7	PORTB6	PORTB5	PORTB4	PORTB3	PORTB2	PORTB1	PORTB0



#### **Libraries**

```
#define PINB SFR IO8(0x03
                                      #define DDRB SFR IO8(0x04)
                                                                            #define PORTB SFR IO8(0x05)
                                      #define DDB0 \overline{0}
                                                                            #define PORTBO 0
#define PINBO
                                      #define DDB1 1
#define PINB1 1
                                                                            #define PORTB1 1
                                      #define DDB2 2
#define PINB2 2
                                                                            #define PORTB2 2
                                      #define DDB3 3
                                                                            #define PORTB3 3
#define PINB3 3
                                      #define DDB4 4
#define PINB4 4
                                                                            #define PORTB4 4
#define PINB5 5
                                      #define DDB5 5
                                                                            #define PORTB5 5
                                      #acfine DDB6 6
#define PINB6 6
                                                                            #define PORTB6 6
                                      #define DDB7 7
#define PINB7 7
                                                                            #define PORTB7 7
#define SFR IO8(io addr) MMIO BYTE((io addr)
                                                          SFR OFFSET
                                               if _AVR_ARCH__ >= 100

define __SFR_OFFSET 0x00
                                                        SFR OFFSET 0x20
                                            # endif
                                            #endif
```



#### **Libraries**

```
#define DDRB SFR IO8(0x04)
                                                                        #define PORTB SFR IO8(0x05)
#define PINB SFR IO8(0x03)
                                    #define DDB0 \overline{0}
                                                                        #define PORTBO 0
#define PINB0 0
                                    #define DDB1 1
                                                                        #define PORTB1 1
#define PINB1 1
                                    #define DDB2 2
#define PINB2 2
                                                                        #define PORTB2 2
                                    #define DDB3 3
                                                                        #define PORTB3 3
#define PINB3 3
                                    #define DDB4 4
#define PINB4 4
                                                                        #define PORTB4 4
#define PINB5 5
                                    #define DDB5 5
                                                                        #define PORTB5 5
                                    #define DDB6 6
#define PINB6 6
                                                                        #define PORTB6 6
                                    #define DDB7 7
#define PINB7 7
                                                                        #define PORTB7 7
#define _SFR_IO8(io_addr) _MMIO_BYTE((io_addr) +
           #define MMIO BYTE(mem addr) (*(volatile uint8 t *)(mem addr))
```



### **Libraries**

avr/ioXXXX.h

Therefore, if we want to set bit 5 high, we can now just say

PORTB = PORTB | 0x20; // or more typically: PORTB |= 0x20;



#### Libraries

#### util/delay.h

The functions available allow the specification of microsecond, and millisecond delays directly, using the application-supplied macro F\_CPU as the CPU clock frequency (in Hertz).

```
#define F_CPU 1000000UL
frequency to be considered by the delay macros

void _delay_ms (double __ms) //Perform a delay of milliseconds (The maximal possible delay is 262.14 ms / F_CPU in MHz)

void _delay_us (double __us) //Perform a delay of microseconds (The maximal possible delay is 768 us / F CPU in MHz)
```



### **Libraries**

util/delay.h

#### **Example**





#### **Question 1**

Write AVR C program read pins 1 and 0 of PORTB and send ASCII character to PORTD according to the following table

pin1	pin0	send
0	0	11
0	1	<b>'2'</b>
1	0	<b>'3'</b>
1	1	<b>'4'</b>





#### **Question 2**

Write AVR C program that check the value of PORTB.7 every 100ms. If it is 1, make bit 4 of PORTB input, otherwise, change pin 4 of PORTB to output