National Institute of Technology Jamshedpur

Session : NOV-2012

Faculty : AMIT PRAKASH

Scheme : M.TECH FIRST SEMESTER VLSI DESIGN & EMBEDDED SYSTEM (ELECTRONICS & COMM. ENGG.)

Course : DIGITAL VLSI SYSTEM DESIGN (Theory)

Mid : 30   Teach. Asset 20   End Sem: 50   Total Marks : 100   Credits : 4

Sr. No.	Roll No.	Student Name	MidSem Marks	Teacher Ast.	EndSem Marks	Total Marks	Grades
1.	007/12	SOORAJ M N	26	16	40	82	A
2.	008/12	SANTOSH KUMAR	22	16	34	72	B
3.	009/12	ANANT ANAND SINGH	20	18	32	70	B
4.	010/12	SIDDHARTH SRIVASTAVA	27	19	44	90	EX
5.	011/12	PRASANTA KUMAR GORAI	19	16	36	71	B
6.	012/12	SATYA PRAKSH JAISWAL	23	17	32	72	B
7.	013/12	PINNINTI SIVA PRASADA RAO	26	17	42	85	A
8.	014/12	PRATAP KUMAR MEHRA	20	16	28	64	C
9.	015/12	GUGULOTH SREEKANTH	26	16	31	73	B
10.	074/12	ABHISHEK KUMAR	24	16	40	80	A
11.	075/12	AMRENDRA PRATAP SINGH	22	16	36	74	B
12.	076/12	ADITYA KUMAR JHA	19	16	29	64	C
13.	077/12	NISHANT KUMAR MISHRA	23	17	35	75	B
14.	078/12	RAJ KUMAR MADDHESHIYA	23	16	32	71	B
15.	079/12	JAYANT HANSDAH	12	16	36	64	C

Date : 20-Dec-2012