National Institute of Technology Jamshedpur

Session :   NOV-2012   Faculty :   JYOTI ATHIYA (ECE)

Scheme :   M.TECH FIRST SEMESTER VLSI DESIGN & EMBEDDED   Course :   ANALOG VLSI SYSTEM DESIGN (Theory)
SYSTEM (ELECTRONICS & COMM. ENGG.)

Mid : 30   Teach. 20   End Sem: 50   Total Marks :   100   Credits :   3
Asset

Sr. No.	Roll No.	Student Name	MidSem Marks	Teacher Ast.	EndSem Marks	Total Marks	Grades
1.	007/12	SOORAJ M N	26	19	45	90	EX
2.	008/12	SANTOSH KUMAR	26	17	37	80	A
3.	009/12	ANANT ANAND SINGH	21	19	40	80	A
4.	010/12	SIDDHARTH SRIVASTAVA	26	19	45	90	EX
5.	011/12	PRASANTA KUMAR GORAI	25	18	42	85	A
6.	012/12	SATYA PRAKSH JAISWAL	25	17	41	83	A
7.	013/12	PINNINTI SIVA PRASADA RAO	21	18	41	80	A
8.	014/12	PRATAP KUMAR MEHRA	21	18	37	76	B
9.	015/12	GUGULOTH SREEKANTH	23	19	38	80	A
10.	074/12	ABHISHEK KUMAR	25	18	40	83	A
11.	075/12	AMRENDRA PRATAP SINGH	21	19	40	80	A
12.	076/12	ADITYA KUMAR JHA	24	17	39	80	A
13.	077/12	NISHANT KUMAR MISHRA	19	18	36	73	B
14.	078/12	RAJ KUMAR MADDHESHIYA	22	19	39	80	A
15.	079/12	JAYANT HANSDAH	18	18	34	70	B

Date : 20-Dec-2012