

National Institute of Technology Jamshedpur

Session : NOV-2012

Faculty : A. CHOUBEY

Scheme : M.TECH FIRST SEMESTER VLSI DESIGN & EMBEDDED SYSTEM (ELECTRONICS & COMM. ENGG.) Course : DSP LAB. (Practical)

Mid : 0

Teach. 0
Asset

End Sem: 100

Total Marks : 100

Credits : 2

Sr. No.	Roll No.	Student Name	MidSem Marks	Teacher Ast.	EndSem Marks	Total Marks	Grades
1.	007/12	SOORAJ M N	.00	.00	75	75	B
2.	008/12	SANTOSH KUMAR	.00	.00	65	65	C
3.	009/12	ANANT ANAND SINGH	.00	.00	65	65	C
4.	010/12	SIDDHARTH SRIVASTAVA	.00	.00	65	65	C
5.	011/12	PRASANTA KUMAR GORAI	.00	.00	75	75	B
6.	012/12	SATYA PRAKSH JAISWAL	.00	.00	65	65	C
7.	013/12	PINNINTI SIVA PRASADA RAO	.00	.00	65	65	C
8.	014/12	PRATAP KUMAR MEHRA	.00	.00	65	65	C
9.	015/12	GUGULOTH SREEKANTH	.00	.00	65	65	C
10.	074/12	ABHISHEK KUMAR	.00	.00	65	65	C
11.	075/12	AMRENDRA PRATAP SINGH	.00	.00	65	65	C
12.	076/12	ADITYA KUMAR JHA	.00	.00	65	65	C
13.	077/12	NISHANT KUMAR MISHRA	.00	.00	65	65	C
14.	078/12	RAJ KUMAR MADDHESHIYA	.00	.00	65	65	C
15.	079/12	JAYANT HANSDAH	.00	.00	75	75	B