National Institute of Technology Jamshedpur

Session :   NOV-2012   Faculty :   S.N. SINGH

Scheme :   M.TECH FIRST SEMESTER VLSI DESIGN & EMBEDDED   Course :   EMBEDDED SYSTEM DESIGN (Theory)
              SYSTEM (ELECTRONICS & COMM. ENGG.)

Mid : 30   Teach. 20   End Sem: 50   Total Marks :   100   Credits :   3
              Asset

Sr. No.	Roll No.	Student Name	MidSem Marks	Teacher Ast.	EndSem Marks	Total Marks	Grades
1.	007/12	SOORAJ M N	21	18	35	74	B
2.	008/12	SANTOSH KUMAR	22	18	39	79	B
3.	009/12	ANANT ANAND SINGH	24	19	44	87	A
4.	010/12	SIDDHARTH SRIVASTAVA	24	19	40	83	A
5.	011/12	PRASANTA KUMAR GORAI	21	16	34	71	B
6.	012/12	SATYA PRAKSH JAISWAL	21	16	35	72	B
7.	013/12	PINNINTI SIVA PRASADA RAO	27	19	44	90	EX
8.	014/12	PRATAP KUMAR MEHRA	20	15	34	69	C
9.	015/12	GUGULOTH SREEKANTH	22	16	34	72	B
10.	074/12	ABHISHEK KUMAR	27	18	42	87	A
11.	075/12	AMRENDRA PRATAP SINGH	24	18	40	82	A
12.	076/12	ADITYA KUMAR JHA	21	17	34	72	B
13.	077/12	NISHANT KUMAR MISHRA	26	17	37	80	A
14.	078/12	RAJ KUMAR MADDHESHIYA	24	17	39	80	A
15.	079/12	JAYANT HANSDAH	24	16	36	76	B