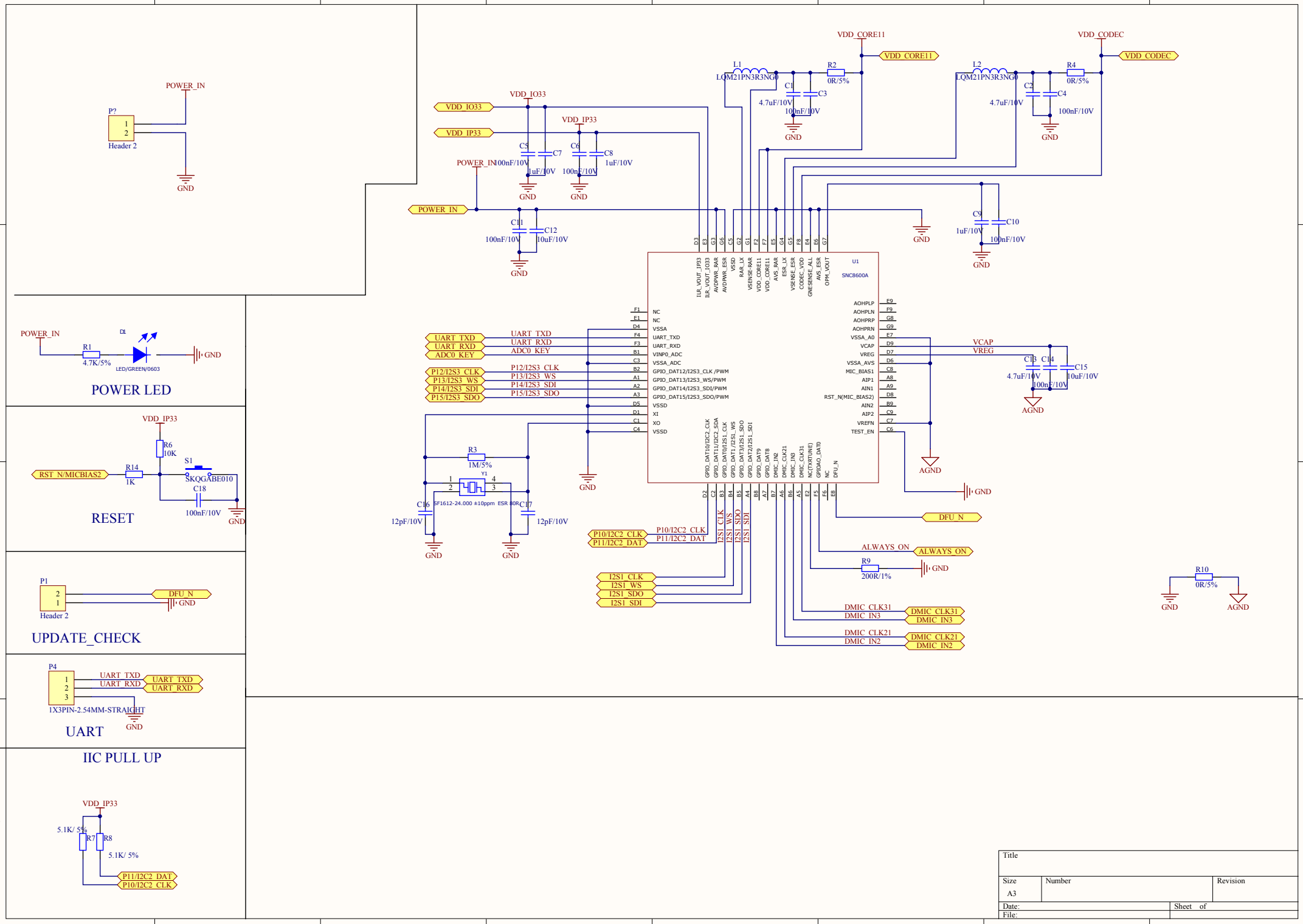
POWER_IN
P?
Header 2
GND
VDD_CORE11
VDD_CODEC
L1
LQM21PN3R3NG0
R2
0R/5%
L2
LQM21PN3R3NG0
R4
0R/5%
C1
C3
4.7uF/10V
100nF/10V
C2
C4
4.7uF/10V
100nF/10V
VDD_IO33
VDD_IP33
C5
C7
C6
C8
100nF/10V
1uF/10V
C11
C12
100nF/10V
10uF/10V
C9
C10
1uF/10V
100nF/10V
U1
SNC8600A
UART_TXD
UART_RXD
ADC0_KEY
P12/I2S3_CLK
P13/I2S3_WS
P14/I2S3_SDI
P15/I2S3_SDO
VCAP
VREG
C13
C14
C15
4.7uF/10V
100nF/10V
10uF/10V
AGND
R3
1M/5%
Y1
SF1612-24.000 ±10ppm ESR 80R
C16
C17
12pF/10V
P10/I2C2_CLK
P11/I2C2_DAT
DFU_N
ALWAYS_ON
R9
200R/1%
I2S1_CLK
I2S1_WS
I2S1_SDO
I2S1_SDI
DMIC_CLK31
DMIC_IN3
DMIC_CLK21
DMIC_IN2
R10
0R/5%
POWER LED
POWER_IN
R1
4.7K/5%
D1
LED/GREEN/0603
RESET
VDD_IP33
R6
10K
RST_N/MICBIAS2
R14
1K
S1
SKQGABE010
C18
100nF/10V
UPDATE_CHECK
P1
Header 2
UART
P4
1X3PIN-2.54MM-STRAIGHT
IIC PULL UP
VDD_IP33
5.1K/ 5%
R7
R8
5.1K/ 5%
P11/I2C2_DAT
P10/I2C2_CLK
Title
Size
A3
Number
Revision
Date:
Sheet of
File: