

TWS206 Datasheet

Bluetooth 5.2 HD Audio AI SOC

Version: 0.2

May 10th, 2021



Revision History

| Version | Date | Description | |
|---------|---------------|--------------------------------|--|
| 0.1 | December 2020 | Initial release. | |
| 0.11 | January 2021 | Updated application schematic. | |
| 0.2 | May, 2021 | Updated spec. | |





Table of Contents

| Kev | ision H | istory | 1 |
|-----|----------|----------------------------------|----|
| Tab | le of Co | ontents | 2 |
| 1 | Gener | al description | 3 |
| 2 | Featur | es | 3 |
| 3 | Block | Diagram | 5 |
| 4 | Pin As | signment | 6 |
| 5 | Pin De | scription | 6 |
| 6 | Electri | cal Characteristics | 9 |
| | 6.1 | Absolute Maximum Ratings | 9 |
| | 6.2 | Recommended Operating Conditions | 9 |
| | 6.3 | DC Electrical Characteristics | 9 |
| 7 | Packa | ge Information | 10 |
| | 7.1 | QFN32L (4mm x 4mm 0.4pitch) | 10 |
| | 7.2 | RoHS compliance | 10 |
| 8 | Examp | ole Application Schematic | 11 |
| | 8.1 | TWS Hybrid ANC | 11 |
| ABC | OUT US. | | |

1 General description

TWS206 is a Bluetooth 5.2 dual-mode audio SOC mainly for TWS hybrid ANC and voice AI applications. It includes high performance RISC processor, BT/BLE dual-mode radio, hybrid ANC processor, low-power VAD, 24-bit high-performance audio codecs, advanced PMU, Li-ion battery charger, embedded flash, and flexible interfaces including USB, I2S, I2C, UART, PWM and programmable input/output.

TWS206 is a high performance & low-power solution for TWS hybrid ANC and voice AI products.

2 Features

Processor

- 32-bit RISC Processor
- Up to 336MHz
- Supports DSP instructions
- Supports floating point instructions

Bluetooth

- Bluetooth V5.2 dual mode
- Supports BR/EDR, BLE 1/2M
- Supports LE isochronous channel
- TX power up to 10dBm
- RX sensitivity:
 - BR@-97dBm;
 - EDR@-96dBm;
- Multi-link up to 7 active ACL links
- Supports A2DP/AVRCP/HFP/ HSP/SPP/Etc. profiles

Audio/Voice Processing

- FF/FB/Hybrid ANC
- Context adaptive ANC
- Multi-MIC AI AENC
- Low power VAD
- Key word spotting and voice command
- HW EQ and ASRC

Audio Codec

- High performance 24-bit audio codecs, up to 192KHz sample rate
 - ADC x 3: 95dB SNR; -85dB THD+N
 - DAC x 1: 100dB SNR; -93dB THD+N
- Supports triple analog mic inputs
- Integrated headphone drivers

Peripherals

- Full speed USB2.0 OTG
- 1/2/4 SPI Flash CTL with cache
- I2S/UART/I2C/PWM/SPI
- GPIOs

PMU

- Integrated Li-ion Battery charger
- Integrated DCDC and LDOs
- Supports brown-out protection
- Supports over-charge protection

System

- Internal RC OSCs
- RTC/Timer/TRNG/DMA
- Key-ADC/DKC
- 128bit E-fuse
- 8Mb serial Flash embedded



Package:

■ QFN32L 4x4 with 0.4 pin pitch

■ Green (RoHS compliant and no antimony or halogenated flame retardants)

Temperature:

■ Operating: -30°C ~ 85°C
 ■ Storage: -40°C ~125°C



3 Block Diagram

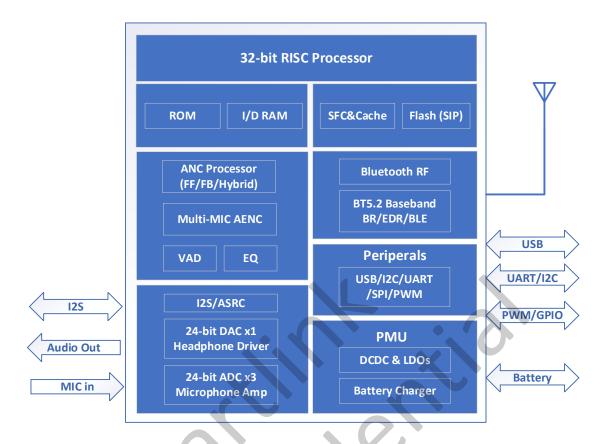


Figure 3-1 TWS206 Block Diagram

4 Pin Assignment

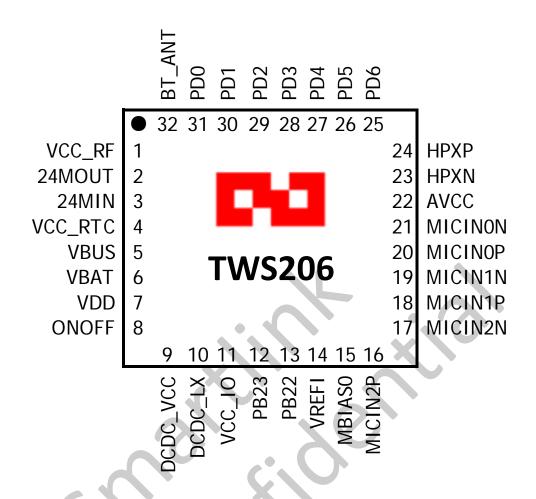


Figure 4-1 TWS206 Pinout

5 Pin Description

| PIN# | PIN NAME | TYPE | Description | Power |
|------|----------|------|-----------------------------|--------|
| | | | | Domain |
| 1 | VCC_RF | Р | RF power | / |
| 2 | 24MOUT | Α | 24MHz Crystal Output | / |
| 3 | 24MIN | Α | 24MHz Crystal Input | / |
| 4 | VCC_RTC | Р | RTC power | / |
| 5 | VBUS | Р | Charge input | / |
| 6 | VBAT | Р | Battery input/charge output | / |
| 7 | VDD | Р | Digital core power | / |
| 8 | ONOFF | - 1 | Power on/off key | VBAT |
| 9 | DCDC_VCC | Р | DCDC power input | / |



| PIN# | PIN NAME | TYPE | Description | Power |
|------|----------|------|---|----------|
| | | | | Domain |
| 10 | DCDC_LX | Р | DCDC switch mode output | / |
| 11 | VCC_IO | Р | IO Power | / |
| 12 | PB23 | 10 | PB23/SWDIO_0/UART1_TX/DMIC_DATA1/ | VCC-IO |
| | | | PWM3/UART0_RX/I2C0_SDA/I2CS_SDA/ | |
| | | | SWDIO_1/UART2_RX/I2S0_D0/USB0-DM | |
| 13 | PB22 | 10 | PB22/SWCLK_0/UART1_RX/DMIC_CLK/ | VCC-IO |
| | | | PWM2/UARTO_TX/I2CO_SCK/I2CS_SCK/ | |
| | | | SWCLK_1/UART2_TX/I2S0_BCLK/USB0-DP | |
| 14 | VREFI | Α | Decoupling of analog reference | AVCC |
| 15 | MBIAS0 | А | MIC BIAS | AVCC |
| 16 | MICIN2P | А | MIC2 input positive pin | / |
| 17 | MICIN2N | А | MIC2 input negative pin | AVCC |
| 18 | MICIN1P | Α | MIC1 input positive pin. | AVCC |
| 19 | MICIN1N | Α | MIC1 input negative pin | AVCC |
| 20 | MICINOP | Α | MICO input positive pin. | AVCC |
| 21 | MICINON | Α | MICO input negative pin | AVCC |
| 22 | AVCC | Р | Power for audio codec | / |
| 23 | HPXN | Α | Speaker output negative pin | AVCC |
| 24 | HPXP | Α | Speaker output positive pin | AVCC |
| 25 | PD6 | 1/0 | PD6/SPI1_MISO/I2S0_MCLK/UART1_TX/ | VCC-RTC |
| | | a' (| I2SO_LRCK/I2CO_SDA/UART2_RTS/ | |
| | | | DMIC_DATA0/I2CS_SDA/PWM3/UART0_RX/K | |
| | | | EYADC5 | |
| 26 | PD5 | I/O | PD5/SPI1_CLK/I2S0_BCLK/UART1_RX/ | VCC-RTC |
| | | | I2S0_BCLK1/I2C1_SDA/PWM2/DMIC_CLK/ | |
| | | | I2CS_SCK/UARTO_TX/KEYADC4 | |
| 27 | PD4 | 1/0 | PD4/SPI1_MOSI/I2S0_D1/SPDIF_IN_B/ | VCC-RTC |
| | | | I2SO_LRCK1/I2C1_SCK/I2SO_MCLK/ | |
| | | | DMIC_DATA1/KEYADC3 | |
| 28 | PD3 | 1/0 | PD3/SPI1_MISO/I2S0_D0/UART1_RX/ | VCC-RTC |
| | | | 12S0_BCLK1/I2C0_SCK/I2C1_SDA/ | |
| | | | DMIC_CLK/PWM4/KEYADC2 | |
| 29 | PD2 | I/O | PD2/SPI1_CS/I2S0_LRCK/UART1_TX/ | VCC-RTC |
| | | | 12SO_LRCK1/I2CO_SDA/DMIC_CLK/ | |
| | | | DMIC_DATA0/SPDIF_IN_B/PWM5/ | |
| 20 | PD1 | 1/0 | 1250_MCLK_IN | VCC DTC |
| 30 | וחא | 1/0 | PD1/SPI1_CS/I2SO_D0/UART2_TX/I2SO_BCLK/ I2CO_SCK/SPDIF_IN_A/DMIC_DATA1/ | VCC-RTC |
| | | | UART1_RX/PWM1/UART0_RX | |
| 31 | PD0 | I/O | | VCC-RTC |
| J 1 | F D0 | ',0 | PD0/SWD_SELECT/I2S0_LRCK/UART2_TX/ | VCC-INIC |

| PIN# | PIN NAME | TYPE | Description | Power |
|------|----------|------|------------------------------------|--------|
| | | | | Domain |
| | | | PWM0/SPDIF_IN_B/DMIC_CLK/DKC0/ | |
| | | | UART2_RX/I2C0_SDA/UART0_TX/KEYADC1 | |
| 32 | BT_ANT | Α | RF antenna | / |



6 Electrical Characteristics

6.1 Absolute Maximum Ratings

The Recommended Operating Conditions are as follows. Functional operation of the device at any other conditions is not implied. Exposure to absolute maximum rated conditions for extended periods may suffer irreversible damage to the device.

| SYMBOL | PARAMETER | MIN | MAX | UNIT |
|---------|-----------------------|------|-----|------|
| Tstg | Storage Temperature | -40 | 150 | °C |
| VBUS | Charge input voltage | -0.3 | 6.3 | V |
| VBAT | Battery input voltage | -0.3 | 4.6 | V |
| I_vccio | VCCIO output power | / | 200 | mA |

6.2 Recommended Operating Conditions

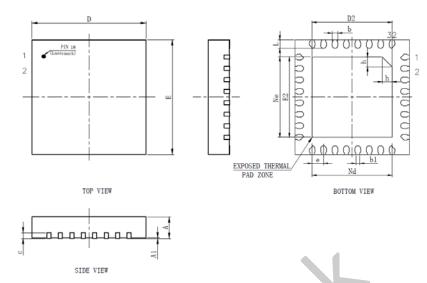
| Symbol Parameter | | Min | Тур | Max | Unit |
|------------------|--------------------------|------|-----|------|------|
| Ta | Operating Temperature | -30 | 20 | 85 | °C |
| VBUS | Power supply for charger | 4.6 | 5 | 5.8 | V |
| VBAT | Power supply for SoC | 2.7 | 3.7 | 4.4 | V |
| VCC-IO | Power supply for IO | 2.7 | 3.3 | 3.4 | V |
| VCC-RTC | Power supply for RTC IO | 1.62 | 1.8 | 1.98 | V |
| AVCC | Power supply for audio | 2.7 | 3.3 | 3.4 | V |

6.3 DC Electrical Characteristics

| Symbol | Parameter | Min | Тур | Max | Unit |
|--------|--------------------------|------------|------|------------|------|
| Vih | High-level input Voltage | 0.7*VCC-IO | / | VCC-IO+0.3 | ٧ |
| Vil | Low-level input Voltage | -0.3 | / | 0.3*VCC-IO | V |
| Rpu(a) | pull-up resistance | 50K | 100K | 150K | Ω |
| Rpd(a) | pull-down resistance | 50K | 100K | 150K | Ω |
| Rpu(b) | pull-up resistance | 1K | 2K | 3K | Ω |
| Rpd(b) | pull-down resistance | 1K | 2K | 3K | Ω |

7 Package Information

7.1 QFN32L (4mm x 4mm 0.4pitch)



| SYMBOL | MILLIMETER | | | |
|---------|------------|----------|-------|--|
| STINDOL | MIN | NOM | MAX | |
| A | 0.80 | 0.85 | 0.90 | |
| A1 | 0 | 0.02 | 0.05 | |
| ь | 0.15 | 0.20 | 0. 25 | |
| b1 | | 0. 14REF | | |
| c | 0.18 | 0.20 | 0.25 | |
| D | 3.90 | 4. 00 | 4.10 | |
| D2 | 2.70 | 2.80 | 2.90 | |
| e | 0 | . 40BSC | | |
| Ne | : | 2. 80BSC | | |
| Nd | : | 2. 80BSC | | |
| E | 3.90 | 4.00 | 4.10 | |
| E2 | 2. 70 | 2.80 | 2.90 | |
| L | 0. 25 | 0.30 | 0.35 | |
| h | 0.30 | 0.35 | 0.40 | |
| L/F载体尺寸 | 122X122 | | | |

Figure 7-1 TWS206 Package

7.2 RoHS compliance

TWS206 meets the requirements of Directive 2011/65/EU of the European

Parliament and of the Council on the Restriction of Hazardous Substance (RoHS).

8 Example Application Schematic

8.1 TWS Hybrid ANC

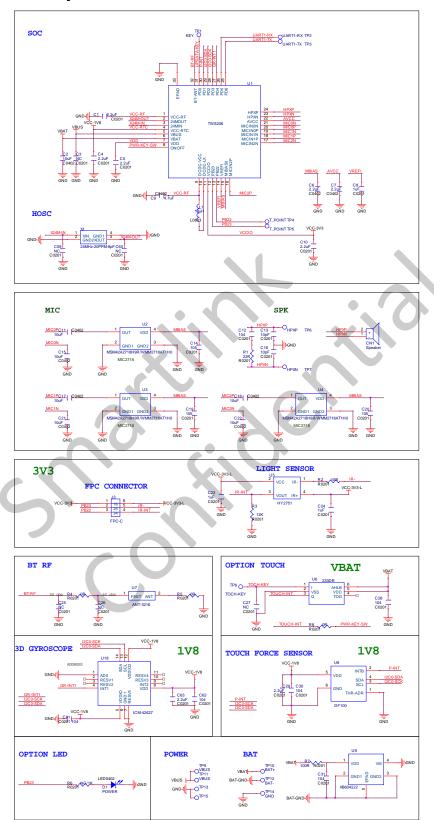


Figure 8-1 TWS example application schematic



ABOUT US

Smartlink technology Inc. is a professional intelligent wireless audio solution supplier. Founded in 2016, Smartlink technology gathered experts in audio processing, SOC design, RF design, system engineering and enthusiasts in semiconductor industry. With tacit teamwork and outstanding expertise, the core team has excellent records of full business chain operation and marketing success on numerous SOC products in years. Smartlink technology provides customers with complete solutions including SOC chips, audio and AI algorithms, and AIOT cloud service.

