

Robert Morgan Savage

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TECHNICAL SKILLS

Hardware: Embedded Microcontrollers, Analog IC Design, Digital Design, Op-Amps, FPGA

Software: OrCad Schematic Capture, SPICE simulation, Altera Quartus II Design Software, LabView, Linux, MathCad, Excel

Programming Languages: C/C++, MATLAB, Python, VHDL, Assembly

Instrumentation: Oscilloscopes, signal generators, logic analyzers

Engineering: Modern Control Theory and System Dynamics Modeling, object-oriented programming, technical writing, debugging, investigating new technologies, hardware prototyping, data collection and analysis, electrical schematics, analytical decision-making, problem-solving

EXPERIENCE

Southern Nuclear Operating Company, Waynesboro, GA

June. 2013-July. 2014

Electrical Design Engineer

- Prepared detailed design change documents, safety evaluations, engineering analysis, drawings, cost estimates and schedules for plant instrumentation, software, controls, and electrical equipment modifications and retrofits.
- Coordinated designs for construction of new plant components.

Hewlett-Packard, Autonomy, Atlanta, GA

Feb. 2012-Feb. 2013

Technical Specialist

- Generated a successful application of customized security and surveillance software with facial recognition, scene analysis, object-detection, and alarm event stream processing using AJAX, Apache Tomcat, JSP, and Java Script.
- Determined prospective customer's goals and map to software capabilities and strengths for text analytics and social media software.

Georgia Tech Research Institute, Atlanta, GA

Oct. 2010-Dec. 2011

Graduate Research Assistant

- Authored detailed, technical documentation for the historical development review and forecasting future evolution of Code Division Multiple Access (CDMA) mobile wireless technology.
- Delivered and presented findings of radar counter measure digital radio frequency memory electronic warfare research by country of origin, offering an assessment on the level of technical expertise and advancement of the research.

EDUCATION

Georgia Institute of Technology, Atlanta, GA

Aug. 2010-Dec. 2011

Master of Science Electrical and Computer Engineering

Relevant Coursework: Microelectronic Fabrication Technologies, Linear Systems and Controls, Analog Integrated Circuit Design, Non-linear Control Systems, Optimal Control and System Optimization, Digital Control, Principles of Management for Engineers, Management of Technology Resources

Georgia Institute of Technology, Atlanta, GA

Aug. 2006-Dec. 2010

Bachelor of Science Electrical Engineering, Honors Graduate

Relevant Coursework: Computer Architecture and Operating Systems, Digital Design Laboratory, Object Oriented Programming for Engineering Applications, Embedded Microcontroller Design, Electromagnetic Applications, Analog Electronics

CERTIFICATIONS

Engineer in Training, Licence Number: EIT026136

Dec. 2013

Graduate Certificate in Management of Technology

Dec. 2011

MBA level course work focusing on technology innovation and Entrepreneurship