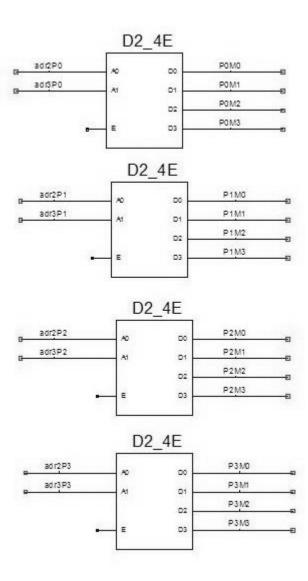
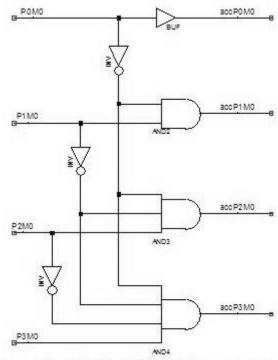
Unitatea de comanda comutator grila 4x4

Logica de decodificare cereri de la procesoare pentru module de memorie



Selectie multiplexoare modul M0



Logica de prioritati pentru modulul M0 de memorie (se repeta pentru fiecare modul)

accP0M0	accP1M0	accP2M0	ассР3М0	bM0	аМ0
1	X	X	X	0	0
0	1	X	X	0	1
0	0	1	X	1	0
0	0	0	1	1	1

Ecuatiile de comutatie pentru semnalele de selectie la multiplexoare:

$$bM0 = \overline{accP0M0} \cap \overline{accP1M0}$$

$$aM0 = (\overline{accP0M0} \cap accP1M0) \cup (\overline{accP0M0} \cap \overline{accP1M0} \cap \overline{accP2M0} \cap accP3M0)$$

Logica de achitare (acknowledge) catre procesoare

