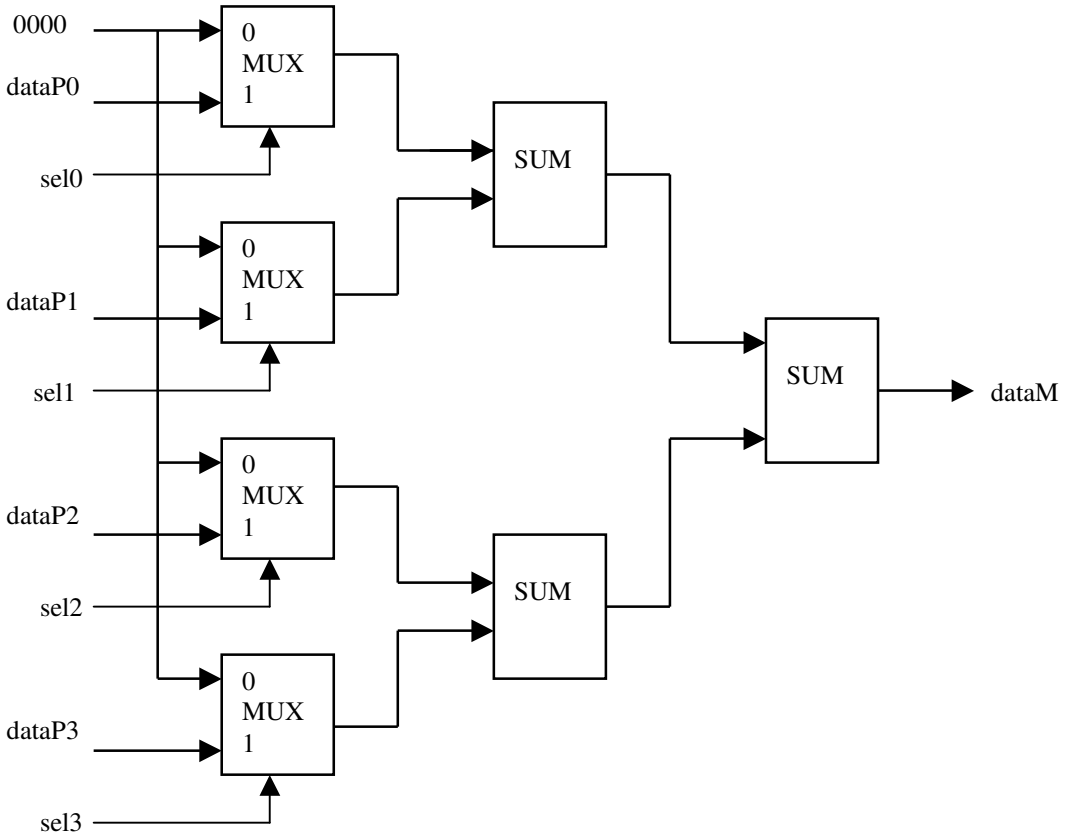
Unitatea de comanda a dispozitivului CW de acces la memorie contine urmatoarele module:

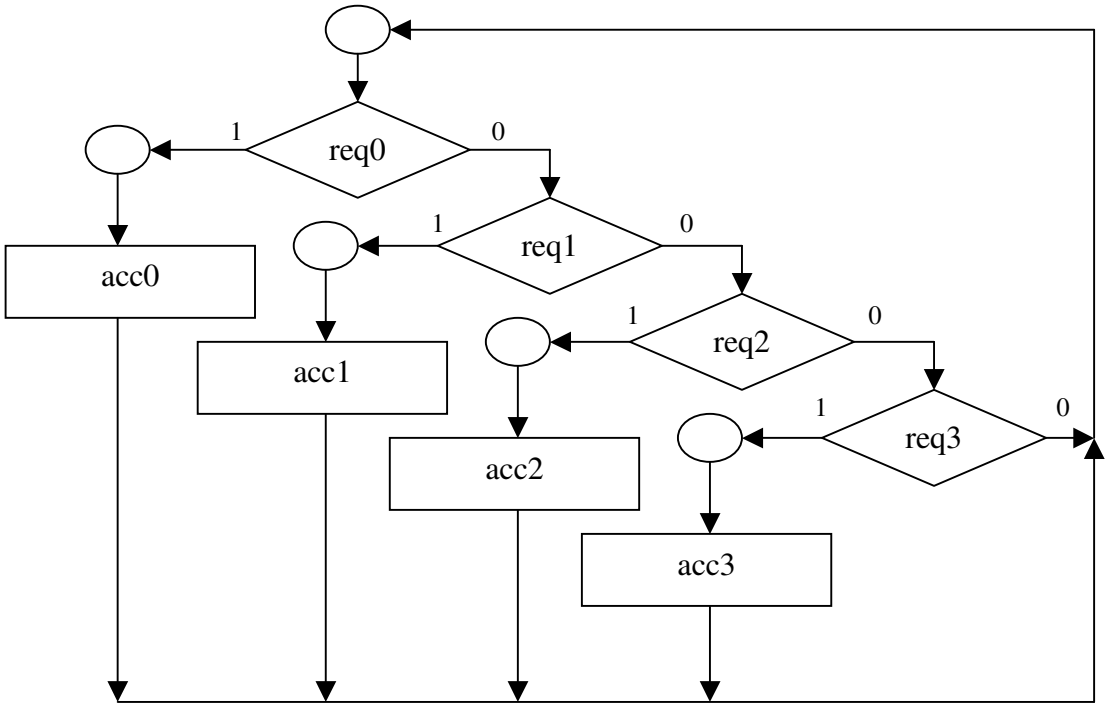
1) Comparator de adrese: genereaza semnalele *eP0P1, eP0P2, eP0P3, eP1P2, eP1P3, eP2P3* (semnifica egalitatea adreselor trimise de procesoarele Pi si Pj):

$$\overline{ePiPj} = adr3Pi \oplus adr3Pj + adr2Pi \oplus adr2Pj + adr1Pi \oplus adr1Pj + adr0Pi \oplus adr0Pj$$

2) Logica de insumare a datelor trimise de procesoare:



2) Logica secventiala a unitatii de comanda.



4) Logica de comanda a multiplexoarelor din blocul de insumare a datelor.

$sel0 = acc0$

$sel1 = acc0 \cdot req1 \cdot eP0P1 + acc1$

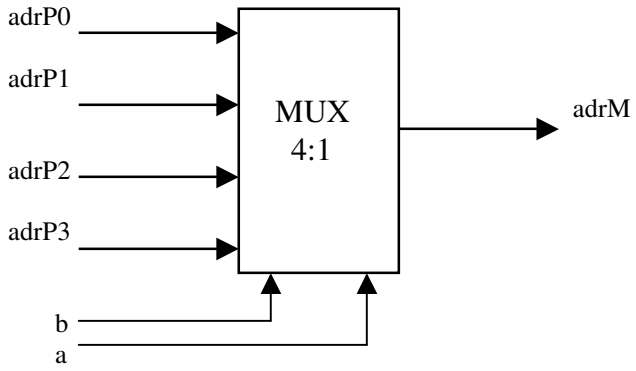
$sel2 = acc0 \cdot req2 \cdot eP0P2 + acc1 \cdot req2 \cdot eP1P2 + acc2$

$sel3 = acc0 \cdot req3 \cdot eP0P3 + acc1 \cdot req3 \cdot eP1P3 + acc2 \cdot req3 \cdot eP2P3 + acc3$

5) Logica de acknowledge pentru procesoare.

$acki = seli$

6) Logica de multiplexare a adreselor.



acc0	acc1	acc2	acc3	b	a
1	X	X	X	0	0
0	1	X	X	0	1
0	0	1	X	1	0
0	0	0	1	1	1

$$b = \overline{acc0} \cdot \overline{acc1} \cdot acc2 + \overline{acc0} \cdot \overline{acc1} \cdot \overline{acc2} \cdot acc3$$

$$a = \overline{acc0} \cdot acc1 + \overline{acc0} \cdot \overline{acc1} \cdot \overline{acc2} \cdot acc3$$