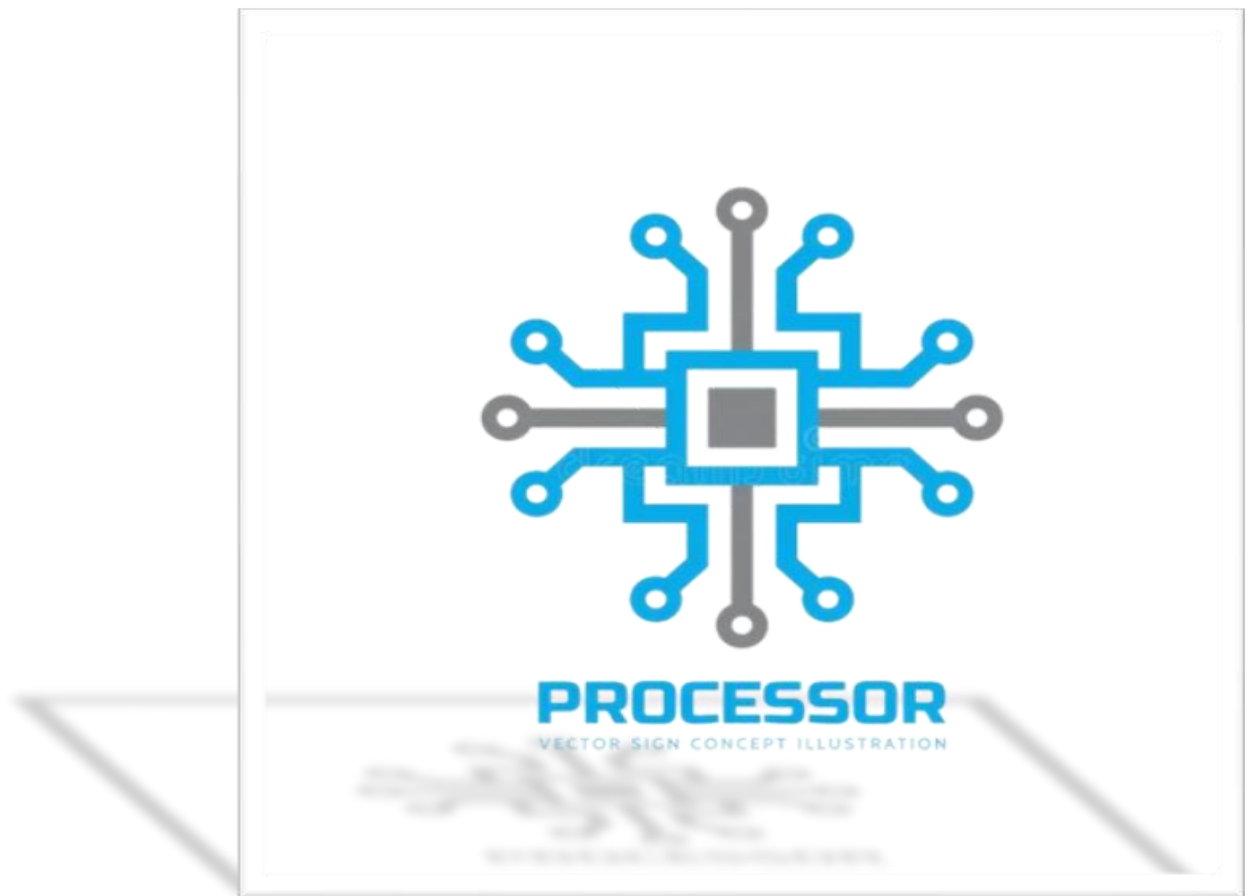


1403/3/20

Single Cycle

. Dr. Valinataj



Amirreza Zahabi 40130212089

Roshana Esmaili 40130112009

Controller:

دستورات	RegDst	ALU_Src	MemToReg	RegWrite	MemRead	MemWrite	Branch
LW	0	1	1	1	1	0	0
SW	0	1	0	0	0	1	0
Beq	0	0	0	0	0	0	1
Add	1	0	0	1	0	0	0
Sub	1	0	0	1	0	0	0
And	1	0	0	1	0	0	0
Or	1	0	0	1	0	0	0

Opcode:

	R_Type	LW	SW	beq
R_Type	000000	-----	-----	-----
I_Type	-----	000001	000010	000011

Function:

OPCODE	Function	ALU Operation
LW	----	add
SW	----	add
Beq	----	sub
Add	000000	add
Sub	000001	sub
And	000010	and
Or	000011	or

ALU Opcode:

ALU Opcode	ALU Operation
00	Add
01	Sub
10	And
11	Or

Single Cycle:

