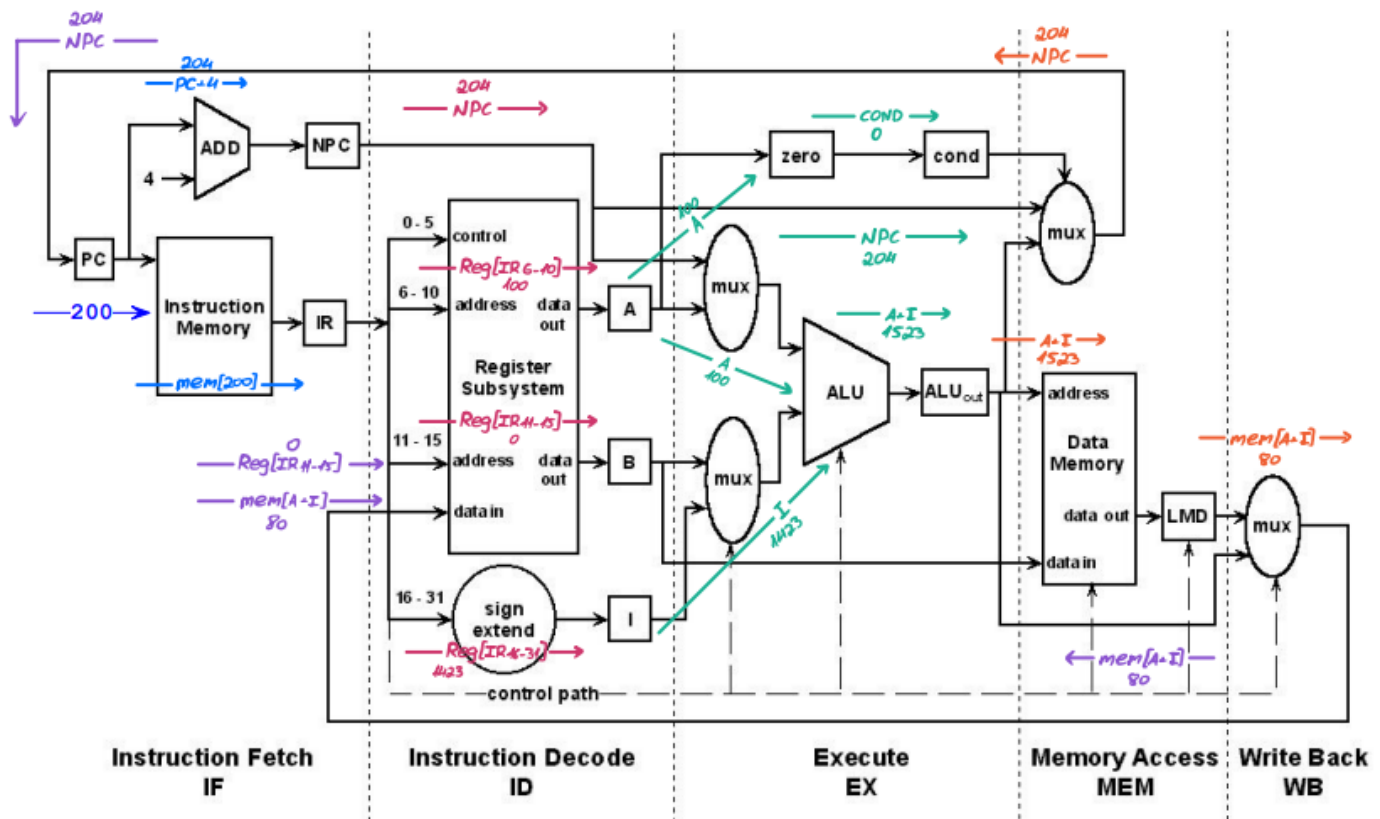


Computer Architecture Exercise 7

Consider the following drawing of version 1 of the DLX CPU:



Draw arrows to indicate the flow of information through the CPU during the execution of instruction **LW R1, 1423 (R7)**. Include the numerical values for each operation, as in the example.

The instruction is stored in address 200 in memory.

The value in register **R7** is 100.

Before the instruction executes the value in register **R1** is 0.

Before the instruction executes the value at address 1523 is 80.