

Anil Kumar G

Objective

I am a sincere and committed person, able to work in a team, and adaptable to changing requirements. Good problem-solving skills, self-learning, self-motivation and hardworking make me an ideal choice to the Industry for the growth of both organization and me.

Education

Degree	University/Board	%
M.Tech (VLSI and Embedded Systems)	IIIT, Hyderabad (June 2006)	8.5 CGPA
B.Tech. (ECE)	Jawaharlal Nehru Technological University, Hyderabad	77.4
Intermediate	Board of Intermediate Education, Andhra Pradesh	86.4
Secondary	Board of Secondary Education, Andhra Pradesh	79.3

Academic Achievements

- ❖ Valid Gate score in 2004 : 96.4
- ❖ Merit certificate for securing school second in SSC
- ❖ Class 3 rd position in B.Tech

Skill Set

Programming Languages	C, C++, Pspice, Matlab
Hardware programming languages	VHDL, Verilog, ALP of 8085, 8086, Motorola 68020, FPGA(mentor graphics)
Operating Systems	Windows 95/98/ME/2000/XP, MS-DOS, LINUX
Programming Environments	GNU/GCC, Microsoft Visual Studio
Software Tools	Microsoft Office, Latex
Networking	Gained knowledge of Networking in LAN Ability to Debug PC errors and Correct them

Areas of Interest

- Digital Design
- VLSI, Digital and Analog Communications
- Memories

Projects handled in M.Tech

VIRTUAL MACHINE OF 68020 MOTOROLA PROCESSOR USING C

This project deals with the simulation of processor 68020 instruction set. All the instructions are decoded and the free opcode list is taken and assigned to new instructions added to design.

DESIGN OF ATM SWITCH IN VHDL

In this project, I describe the design, the methodology, and the VHDL implementation 8x8 input buffered crossbar switch. The three main blocks of design are the ports, the crossbar scheduler, and the crossbar fabric.

The input ports receive Asynchronous Transfer Mode (ATM) packetized data serially, queue the packet in a FIFO. Crossbar scheduler is designed to route data at high speed with high quality performance. The crossbar fabric is responsible for physically connecting an input port to its destination output port based on the grant issued by the scheduler.

DESIGN OF LAN EMULATOR(LANE) IN VHDL

A LAN Emulation Client (LEC) is an entity in an ATM end station that performs data forwarding, address resolutions. Defined LE services are LECS, LES and BUS. LE services and LEC are modeled with appropriate memory models. LEC(client) which wants to join in ATM network is taken as entity. All possible input frames (both control frame and data frames) from LEC are taken as input from ports, Responses from LES,BUS,LECS are taken and new frames are generated. LEC memory is updated according to data from other modules. LEC module is designed to handle different Ethernet format packets and change this to ATM network acceptable cell. Initialization and registration phases related to lan emulator client is implemented.

EMBEDDED BROWSER (BYTESTREAM)

Design of Byte stream (Monologue Embedded Browser): This project is designed for the development of byte stream, an embedded browser, which differs from the conventional browser, in which transaction is through files and source code is open. The total implementation has been done in VC++ by dividing the entire project into six logically separable modules by following all the norms of software practices and a specially defined inter module format for effective communication. Project is certified to be successful by fetching desired html pages from web servers..

IMPLEMENTATION OF ALGORITHMS FOR MUL/DIV IN VHDL

Implemented booths algorithms for multiplication and restoring ,non restoring algorithm for division.

Project done in B.Tech

IMAGE COMPRESSION USING SUBBAND TRANSFORMS IN MATLAB:

Subband transforms are computed by convolving the input signal with set of band pass filters and decimating the results. Each subband encodes particular portion of frequency spectrum. Quadrature mirror filters are used for alias free reconstruction of input signal.

CASE STUDY:

Finding the derivative of the given set of theta values : This is done using moving average filter. This filter is implemented in MC 68020 Virtual machine.

Extra Curricular Activities

- Worked as a vice president for students association
- Class representative in B.Tech for 3 years
- A lab committee representative for my class.
- Secured several prizes in college, and school level
- Worked as a organizer for National level paper contest in my B.Tech

Personal Details

Fathers Name	: Anil kumar Gokavarapu
Date of Birth	: 9 th July 1983.
Permanent Address	: S/O G.R.L .Swamy Lutukurru East Godavari dt- 533247 Phone: 08862-234657

Declaration

I hereby inform that the information mentioned above is true to the best of my knowledge

Anil kumar G