1 Scope:

1.1 This specification is applicable to lead free and halogen free of RoHS directive for RTA series thick film chip resistors array。

1.2 The product is for general electronic purpose.

2 Explanation Of Part Numbers:

(EX)

RTA 02 - 4 D 100 J TH

Type	Size	Number of Circuits	Terminal Type	Nominal Resistance		Resistance Tolerance	Packaging(Refer to IE-SP-055)
Thick Film Chip Resistors Array	01(0201) 02(0402) 03(0603)	2:2circuits 4:4circuits 8:8circuits	D:Convex C:Concave	5% (3-Digit)	EX. 10Ω=100 4.7Ω=4R7 JUMPER=000	D=± 0.5% F=± 1% J=± 5%	TH：2 mm Pitch Carrier Tape 10000 pcs H2：2 mm Pitch Carrier Tape 20000 pcs H3：2 mm Pitch Carrier Tape 30000 pcs H4：2 mm Pitch Carrier Tape 40000 pcs 5H：2 mm Pitch Carrier Tape 50000 pcs TP：4 mm Pitch Carrier Tape 5000 pcs P2：4 mm Pitch Carrier Tape 10000 pcs P3：4 mm Pitch Carrier Tape 15000 pcs P4：4 mm Pitch Carrier Tape 20000 pcs
				0.5% 1% (4-Digit)	EX. 10.2Ω=10R2 10KΩ=1002 JUMPER=0000		

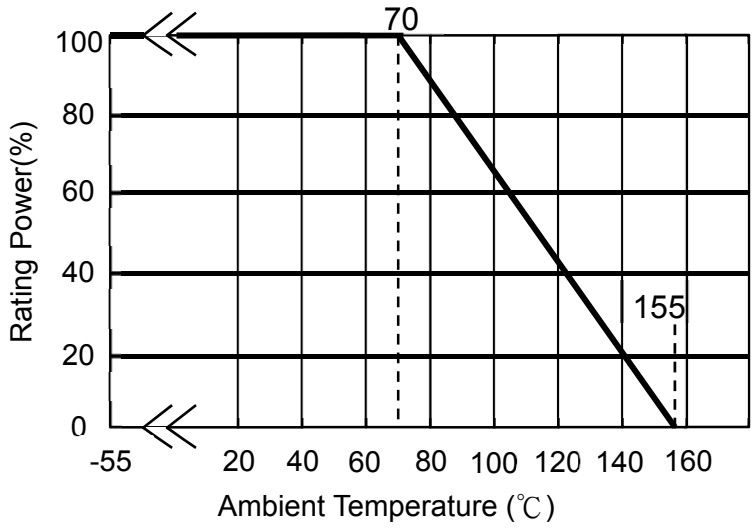
3 General Specifications:

Type	Rated Power at 70℃	Max. Working Voltage	Max. Overload Voltage	T.C.R. (ppm/℃)	Resistance Range			Number of Terminals	Number of Resistors	JUMPER (0Ω) Rated Current	JUMPER (0Ω) Resistance Value	
					D(±0.5%) E-24、E-96	F(±1%) E-24、E-96	J(±5%) E-24				F (±1%)	J (±5%)
RTA01-2D (0201)	1/32 W	12.5V	25V	±500	------	------	3Ω≦R＜10Ω	4	2	0.5A	------	50mΩ MAX.
				±300	------	------	10Ω≦R＜1K Ω					
				±200	------	------	1KΩ≦R≦1 MΩ					
RTA02-2D (0402)	1/16 W	25V	50V	±300	------	1Ω≦R＜10Ω	1Ω≦R＜10Ω	4	2	1A	25mΩ MAX.	50mΩ MAX.
				±200	------	10Ω≦R≦10MΩ	10Ω≦R≦10MΩ					
RTA03-2D (0603)	1/16 W	50V	100V	±200	------	10Ω≦R≦10MΩ	1Ω≦R≦10MΩ	4	2	1A	------	50mΩ MAX.
RTA02-4D (0402)	1/16 W	25V	50V	±300	------	1Ω≦R＜10Ω	1Ω≦R＜10Ω	8	4	1A	25mΩ MAX.	50mΩ MAX.
				±200	------	10Ω≦R≦10MΩ	10Ω≦R≦10MΩ					
RTA02-4C (0402)	1/16 W	25V	50V	±400	------	1Ω≦R＜10Ω	1Ω≦R＜10Ω	8	4	1A	------	50mΩ MAX.
				±200	------	10Ω≦R≦1MΩ	10Ω≦R≦1MΩ					
RTA03-4D (0603)	1/16 W	50V	100V	±200	22Ω≦R≦470KΩ	1Ω≦R≦10MΩ	1Ω≦R≦10MΩ	8	4	1A	25mΩ MAX	50mΩ MAX.
RTA03-4C (0603)	1/16 W	50V	100V	±200	------	1Ω≦R≦1MΩ	1Ω≦R≦10MΩ	8	4	1A	------	50mΩ MAX.
RTA02-8D (0402)	1/16 W	25V	50V	±250	------	10Ω≦R≦10MΩ	1Ω≦R≦10MΩ	16	8	1A	------	50mΩ MAX.
RTA03-8C (0603)	1/16 W	50V	100V	±200	------	1Ω≦R≦1MΩ	1Ω≦R≦10MΩ	16	8	1A	------	50mΩ MAX.
RTA03-2C (0603)	1/16 W	50V	100V	±200	------	1Ω≦R≦1MΩ	1Ω≦R≦10MΩ	4	2	1A	------	50mΩ MAX.
RTA02-2C (0402)	1/16 W	25V	50V	±650	------	3Ω≦R≦10Ω	3Ω≦R＜10Ω	4	2	1A	------	50mΩ MAX.
				±200		10Ω≦R＜1MΩ	10Ω≦R≦1MΩ					
Operating Temperature Range				−55℃ ~ +155℃								

3.1 Power Derating Curve:

Operating Temperature Range： - 55～155 ℃

For resistors operated in ambient temperatures above 70℃, power rating shall be derated in accordance with figure below。



3.2 Voltage Rating or Current Rating:

3.2.1 Resistance Range:≧1Ω

Rated Voltage: The resistor shall have a DC continuous working voltage or a rms. AC continuous working voltage at commercial-line frequency and wave form corresponding to the power rating, as determined from the following:

$$E = \sqrt{R \times P}$$

E= Rated voltage (V)
P= power rating (W)
R= Nominal resistance(Ω)

3.2.2 Resistance Range:(0Ω)

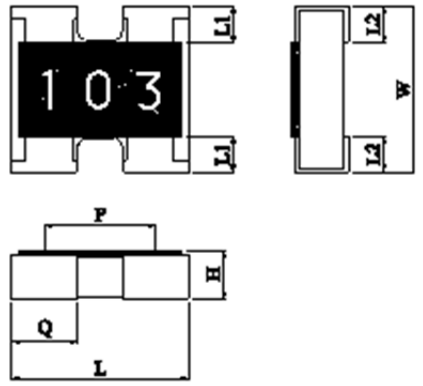
Rated Current: The resistor shall have a DC continuous working current or a rms.AC continuous working current at commercial-line frequency and wave form corresponding to the power rating, as determined from the following:

$$I = \sqrt{P/R}$$

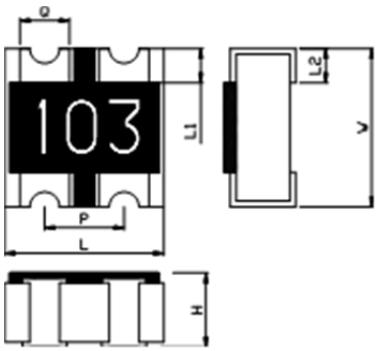
I= Rated current (A)
P= Power rating (w)
R= Nominal resistance(Ω)

4 Dimensions: (mm)

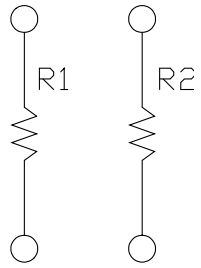
RTA03-2D



RTA03-2C

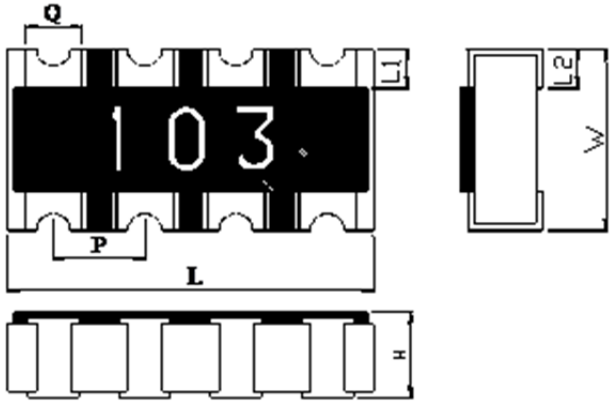


Circuits

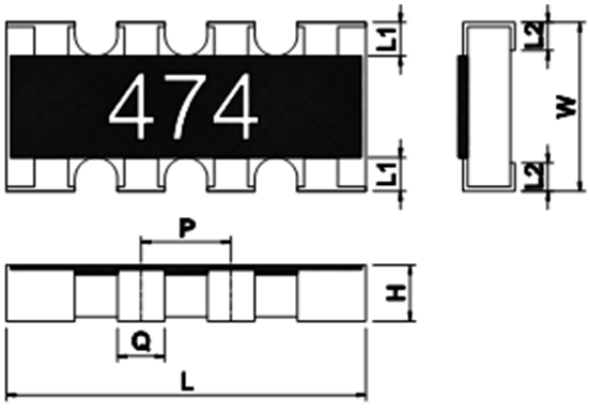


R1=R2

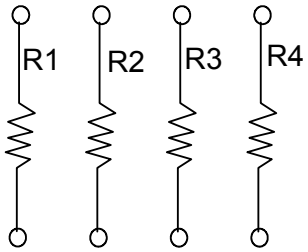
RTA02-4C / RTA03-4C



RTA02-4D / RTA03-4D

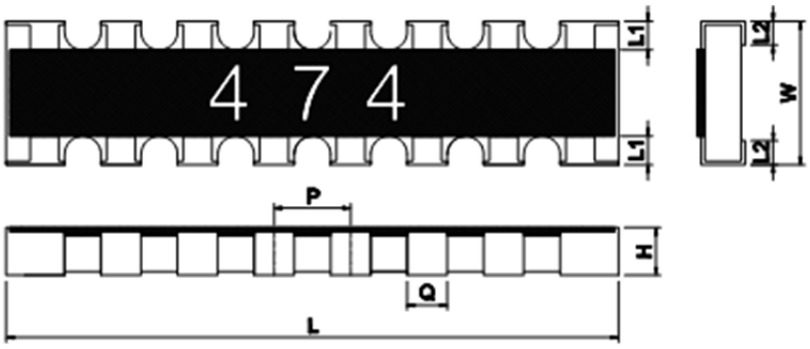


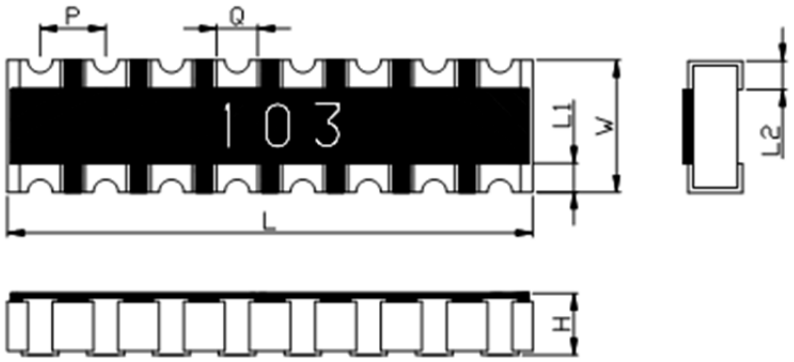
Circuits



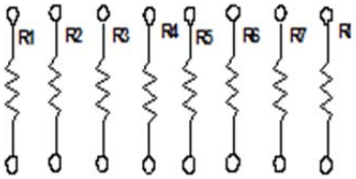
R1=R2=R3=R4

RTA02-8D / RTA03-8C



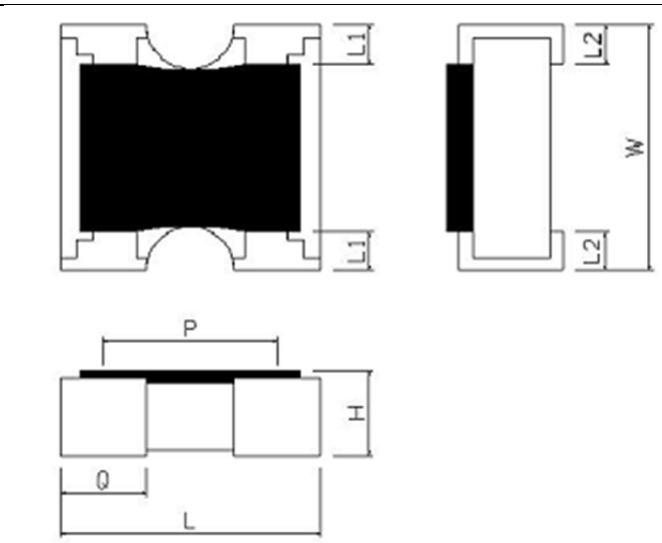


Circuits

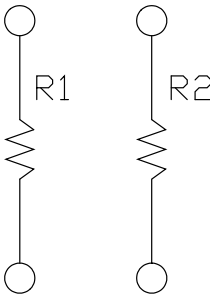


R1 =R2= R3 =R4 = R5 =R6 =R7 =R8

RTA01-2D / RTA02-2D

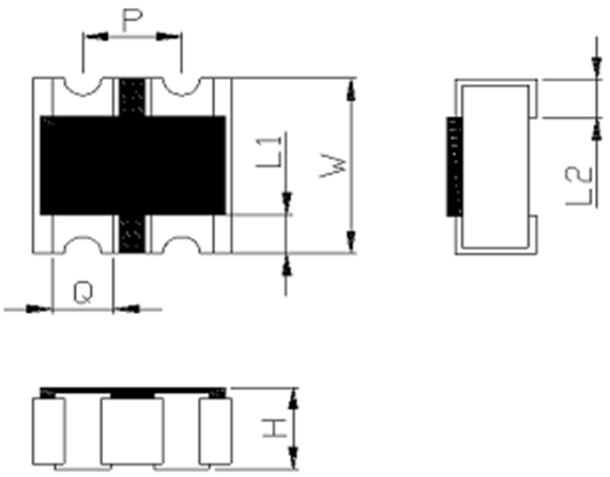


Circuits

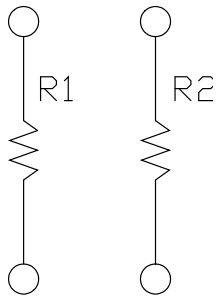


R1=R2

RTA02-2C



Circuits

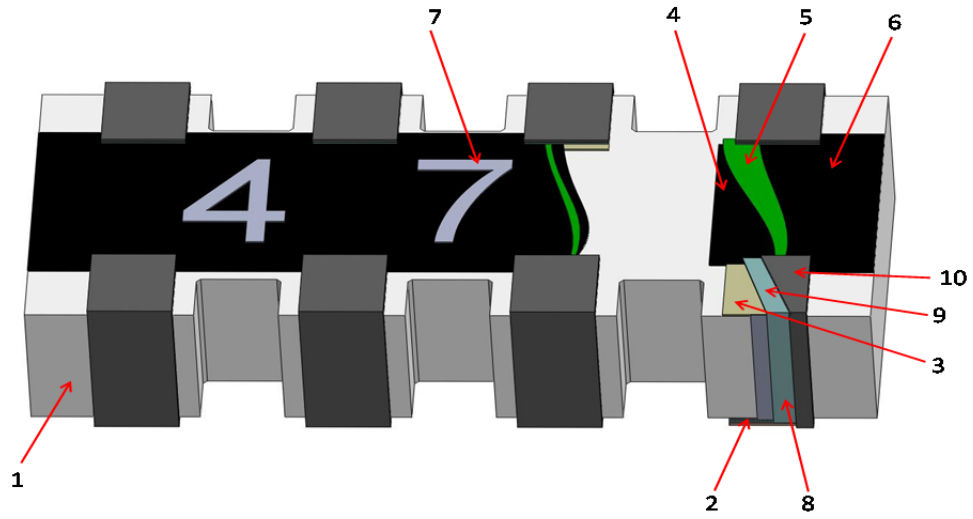


R1=R2

Type \ Dim	L	W	H	L1	L2	P	Q
RTA01-2D (0201)	0.80±0.10	0.60±0.10	0.30±0.05	0.15±0.10	0.15±0.05	(0.50)	0.35±0.10
RTA02-2D (0402)	1.00±0.10	1.00±0.10	0.30±0.05	0.15±0.10	0.25±0.10	(0.67)	0.33±0.10
RTA03-2D (0603)	1.60±0.15	1.60±0.15	0.45±0.10	0.30±0.15	0.30±0.15	(0.80)	0.60±0.10
RTA02-4D (0402)	2.00±0.10	1.00±0.10	0.40±0.10	0.20±0.10	0.25±0.10	(0.50)	0.30±0.10
RTA02-4C (0402)	2.00±0.10	1.00±0.10	0.40±0.10	0.15±0.10	0.25±0.10	(0.50)	0.30±0.10
RTA03-4D (0603)	3.20±0.20	1.60±0.15	0.50±0.10	0.30±0.15	0.30±0.15	(0.80)	0.50±0.10
RTA03-4C (0603)	3.20±0.15	1.60±0.15	0.55±0.10	0.35±0.15	0.45±0.15	(0.80)	0.50±0.10
RTA02-8D (0402)	4.00±0.20	1.60±0.10	0.40±0.10	0.30±0.15	0.30±0.10	(0.50)	0.25±0.10
RTA03-8C (0603)	6.40±0.20	1.60±0.20	0.55±0.10	0.30±0.15	0.40±0.15	(0.80)	0.50±0.10
RTA03-2C (0603)	1.60±0.15	1.60±0.15	0.55±0.10	0.30±0.15	0.40±0.15	(0.80)	0.50±0.10
RTA02-2C (0402)	1.00±0.10	1.00±0.10	0.30±0.10	0.18±0.10	0.25±0.10	(0.50)	0.30±0.10

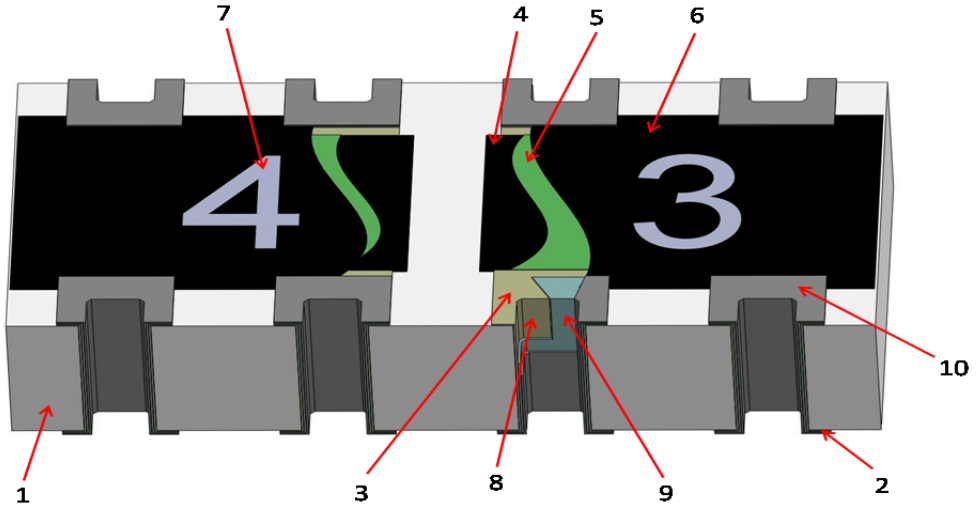
5 Structure Graph:

D(Convex) Type



1	Ceramic substrate	6	2nd Protective coating
2	Bottom inner electrode	7	Marking
3	Top inner electrode	8	Terminal inner electrode
4	Resistive layer	9	Ni plating
5	1st Protective coating	10	Sn plating

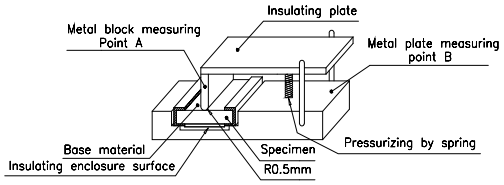
C(Concave) Type



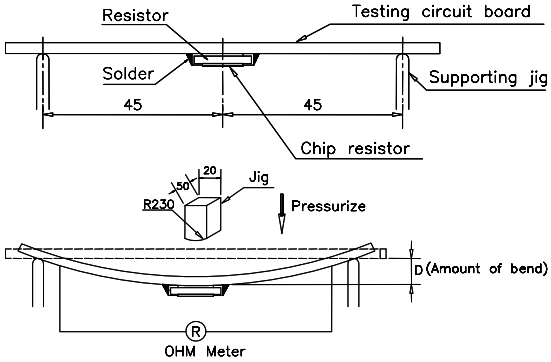
1	Ceramic substrate	6	2nd Protective coating
2	Bottom inner electrode	7	Marking
3	Top inner electrode	8	Terminal inner electrode
4	Resistive layer	9	Ni plating
5	1st Protective coating	10	Sn plating

6 Reliability Test:

6.1 Electrical Performance Test

Item	Conditions	Specifications	
		Resistors	Jumper
Temperature Coefficient of Resistance	TCR（ppm/℃）$= \frac{(R2-R1)}{R1(T2-T1)} \times 10^6$ R1: Resistance at room temperature R2: Resistance at -55℃ or +125℃ T1: Room temperature T2: Temperature -55℃ or +125℃ Refer to JIS-C5201-1 4.8	Refer item 3. General Specifications	NA.
Short Time Overload	Applied 2.5 times rated voltage for 5 seconds and release the load for about 30 minutes, then measure its resistance variance rate. (Rated voltage refer to item 3. general specifications) Refer to JIS-C5201-1 4.13	0.5%、1%:△R=±1.0% 5% : △R=±2.0%	Refer to item 3. General Specifications
Insulation Resistance	Put the resistor in the fixture, add 100 VDC in + ,- terminal for 60 sec then measured the insulation resistance between electrodes and insulating enclosure or between electrodes and base material. Refer to JIS-C5201-1 4.6  	≧$10^9\Omega$	
Dielectric Withstand Voltage	Put the resistor in the fixture, add 300 VAC in +,- terminal for 60 sec. Refer to JIS-C5201-1 4.7	No short or burned on the appearance.	
Intermittent Overload	Put the tested resistor in chamber under temperature 25±2℃ and load 2.5 times rated DC voltage for 1 sec on , 25 sec off , 10000^{+400}_{0} test cycles, then it be left at no-load for 1 hour , then measure its resistance variance rate. Refer to JIS-C5201-1 4.13	△R=±5.0%	Refer to item 3. General Specifications

6.2 Mechanical Performance Test

Item	Conditions	Specifications	
		Resistors	Jumper
Resistance to Solvent	The tested resistor be immersed into isopropyl alcohol of 20~25℃ for 5 minutes, then the resistor is left in the room for 48 hrs , then measure its resistance variance rate. Refer to JIS-C5201-1 4.29	01-2D: △R=±1.0% Other: △R=±0.5%	Refer to item 3. General Specifications
Solderability	Preconditioning: Put the tested resistor in the apparatus of PCT, at a temperature of 105℃, humidity of 100% RH, and pressure of 1.22×10^5 Pa for a duration of 4 hours. Then after left the tested resistor in room temperature for 2 hours or more. Test method: The resistor be immersed into solder pot in temperature 235±5℃ for 2 sec, then the resistor is left as placed under microscope to observed its solder area. Refer to JIS-C5201-1 4.17	Solder coverage over 95%	
Resistance to Soldering Heat	◎Test method 1 (solder pot test): The tested resistor be immersed into molten solder of 260+5/-0℃ for 10 seconds. Then the resistor is left in the room for 1 hour. ◎Test method 2 (solder pot test): The tested resistor be immersed into molten solder of 260+5/-0℃ for 30 seconds. Then the resistor is left as placed under microscope to observe its solder area. Refer to JIS-C5201-1 4.18	Test item 1: (1).Variance rate on resistance △R%=±1.0% Test item 2: (1).Solder coverage over 95%. (2).The underlying material (such as ceramic) shall not be visible at the crest corner area of the electrode.	Refer to item 3. General Specifications
Joint Strength of Solder	◎Bending Strength: Solder tested resistor on the PC board, add force in the middle down, and under load measure its resistance variance rate D=(1)01-2D=3mm (2)Other=5mm  Refer to JIS-C5201-1 4.33	△R%=±1.0%	Refer to item 3. general specifications

6.3 Environmental Test

Item	Conditions	Specifications	
		Resistors	Jumper
Resistance to Dry Heat	Put tested resistors in chamber under temperature 155±5℃ for 1,000±4 hours. Then leaving in room temperature for 60 minutes, and measure its resistance variance rate. Refer to JIS-C5201-1 4.25	0.5%、1%:△R=±1.0% 5% : △R=±2.0%	Refer to item 3. general specifications
Thermal Shock	Put the tested resistor in the thermal shock chamber under the temperature cycle which shown in the following table shall be repeated 300 times consecutively. Then leaving the tested resistor in the room temperature for 1 hours, and measure its resistance variance rate. Testing Condition: Lowest Temperature -55±5℃; Highest Temperature 125±5℃; Temperature-retaining time 15 minutes each Refer to MIL-STD 202 Method 107	△R=±1.0%	Refer to item 3. general specifications
Loading Life in Moisture	Put the tested resistor in the chamber under temperature 40±2℃, relative humidity 90~95% and load the rated voltage for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate. Refer to JIS-C5201-1 4.24	0.5%、1%:△R=±2.0% 5% : △R=±3.0%	Refer to item 3. general specifications`
Load Life	Put the tested resistor in chamber under temperature 70±2℃ and load the rated voltage for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate. Refer to JIS-C5201-1 4.25	0.5%、1%:△R=±2.0% 5% : △R=±3.0%	Refer to item 3. general specifications

7 Plating Thickness:

7.1 Ni:≧2μm

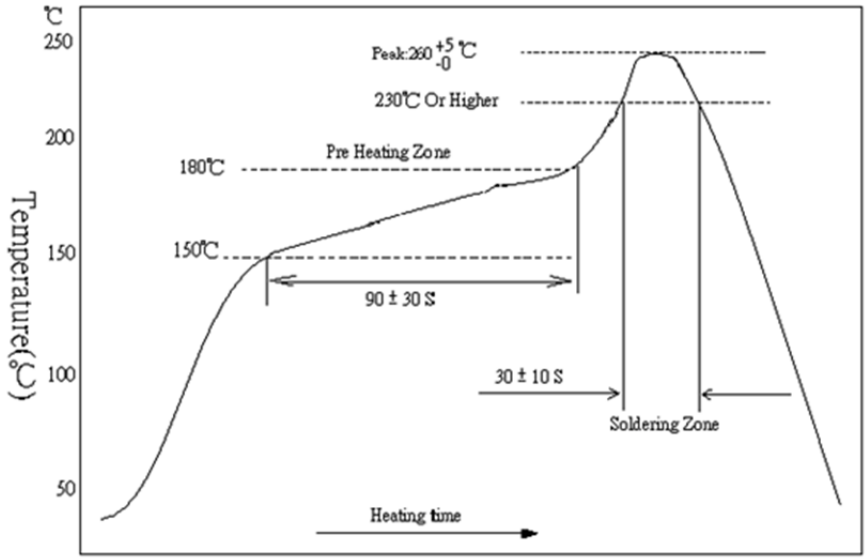
7.2 Sn(Tin):≧3μm

7.3 Sn(Tin): Matte Sn

8 Technical application notes: (This is for recommendation, please customer perform adjustment according to actual application)

8.1 Recommend Soldering Method:

8.1.1 Lead Free IR- Reflow Soldering Profile



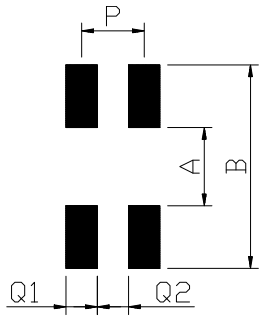
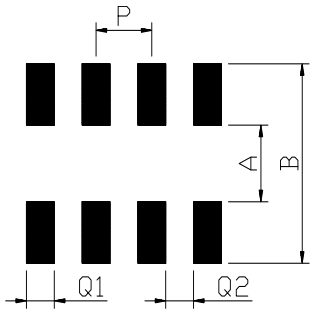
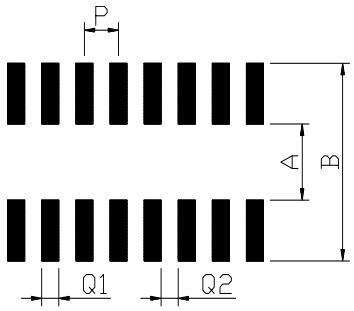
Remark: The peak temperature of soldering heat is 260 +5/-0 ℃ for 10 seconds

8.1.2 Soldering Iron: temperature 350℃±10℃ , dwell time shall be less than 3 sec.

8.2 Recommend Land Pattern Design (For Reflow Soldering)：

When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering. When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.

Unit:mm

RTA01-2D / RTA02-2D RTA02-2C / RTA03-2D / RTA03-2C	RTA02-4D / RTA02-4C RTA03-4D / RTA03-4C	RTA02-8D / RTA03-8C
		

TYPE \ DIM	A	B	P	Q1	Q2
RTA01-2D	0.30	0.90	0.50	0.30	0.20
RTA02-2D	0.50	2.00	0.67	0.33	0.34
RTA03-2D	1.00	2.60	0.80	0.40	0.40
RTA02-4D RTA02-4C	0.50	2.00	0.50	0.28	0.22
RTA03-4D RTA03-4C RTA03-2C	1.00	2.60	0.80	0.40	0.40
RTA03-8C	1.00	2.60	0.80	0.40	0.40
RTA02-8D	1.00	2.60	0.50	0.25	0.25
RTA02-2C	0.50	2.00	0.50	0.28	0.22

8.3 Environment Precautions:

This specification product is for general electronic use, RALEC will not be responsible for any damage, cost or loss caused by using this specification product in any special environment. If other applications need to confirm with RALEC.

If consumer intends to use our Company product in special environment or condition (including but not limited to those mentioned below), then will need to make individual recognition of product features and reliability accordingly.

(a) Used in high temperature and humidity environment

(b) Exposed to sea breeze or other corrosive gas, such as Cl_2、H_2S、NH_3、SO_2 and NO_2.

(c) Used in non-verified liquids including water, oil, chemical and organic solvents.

(d) Using non-verified resin or other coating material to seal or coat our Company product.

(e) After soldering, it is necessary to use water-soluble detergents to clean residual solder fluxes, even though no-clean fluxes are recommended.

8.4 Momentary Overload Precautions:

The product might be out of function when momentary overloaded. Please make sure to avoid momentary overloading while using and preserving.

8.5 Operation and Processing Precautions:

(a) Avoid damage to the edge of resistor and protective layer caused by mechanical stress.

(b) Handle with care when printing circuit board (PCB) is divided or fixed on support body, because bending of printing circuit board (PCB) mounting will make mechanical stress for resistors.

(c) Make sure the power rating is under the limit when using the resistor. When power rating is over the limit, the resister will be overloaded. There might be machinery damage due to the climbing temperature.

(d) If the resister will be exposed under massive impact load (shock wave) in a short period of time, the working environment must be set up well before use.

(e) Please make evaluation and confirmation when the product is well used in your company and have a through consideration of it's fail-safe design to ensure the system safety.

9 Storage and transportation requirement:

9.1 The temperature condition must be controlled at 25±5℃, the R.H. must be controlled at 60±15%. The stock can maintain quality level in two years。

9.2 Please avoid the mentioned harsh environment below when storing to ensure product performance and its' weldability. Places exposed to sea breeze or other corrosive gas, such as Cl2、H2S、NH3、SO2 and NO2.

9.3 When the product is moved and stored, please ensure the correct orientation of the box. Do not drop or squeeze the box. Otherwise, the electrode or the body of the product may be damaged.

10 The carton packaged for electronic-information products is made by the symbol as follows: (For china)

	
Marking for control of pollution cause by electronic-information products	Marking for package recovery

11 Attachments:

11.1 Document Revise Record Paper (QA-QR-027)

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