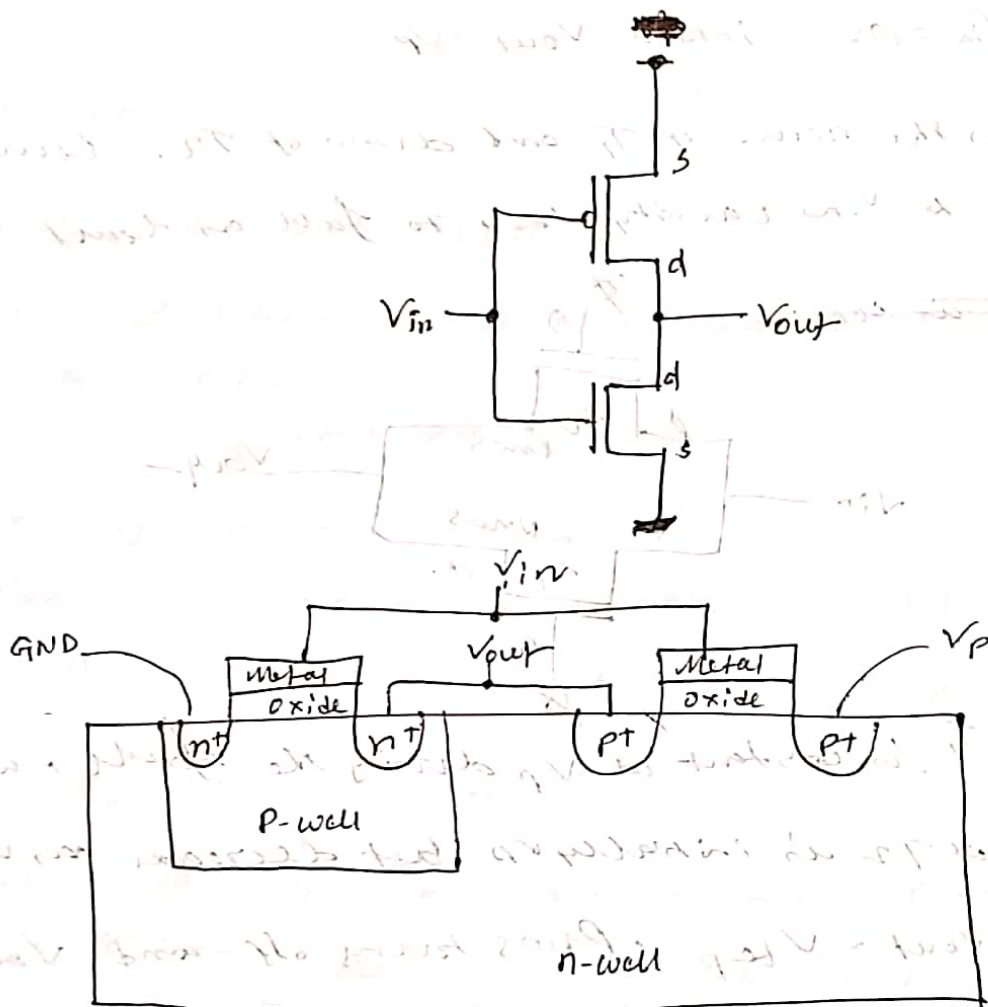


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CMOS power dissipation

1. Static
 2. Dynamic
- Short Circuit

Static Power Dissipation

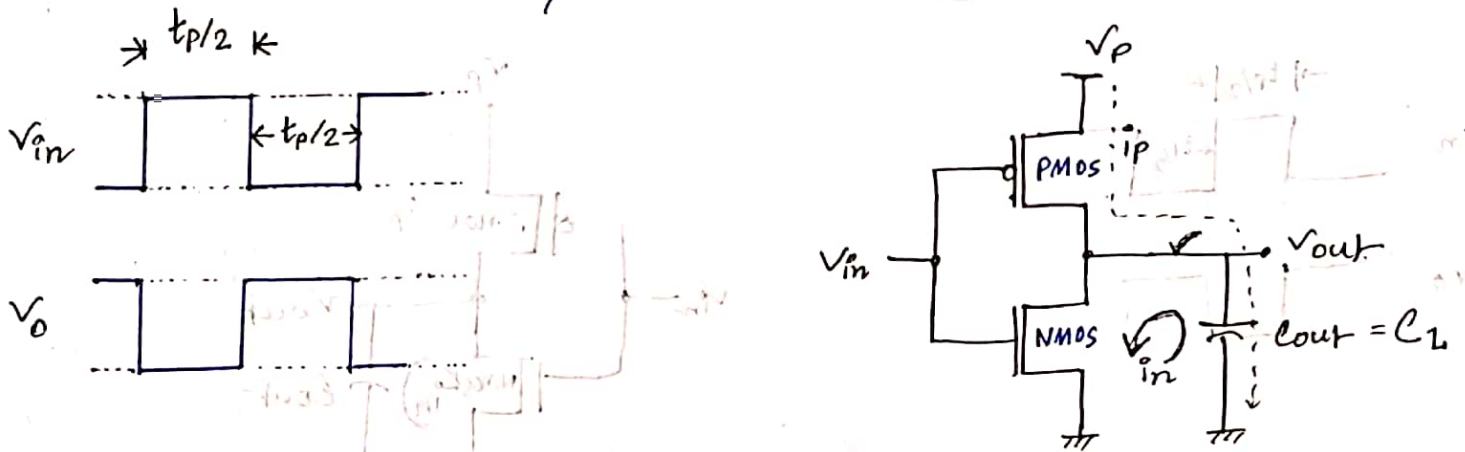


$$P_s = \sum_{i=1}^n (\text{leakage current} \times \text{supply voltage})$$

n = number of devices

leakage current = small amount of current in reverse bias

Dynamic Power Dissipation



Average Dynamic Power Dissipation, P_d

$$\begin{aligned}
 P_d &= \frac{1}{t_p} \left[\int_0^{t_p/2} i_{in}(t) V_0 dt + \int_{t_p/2}^{t_p} i_p(t) (V_p - V_0) dt \right] \\
 &= \frac{1}{t_p} \left\{ \int_0^{t_p/2} -i(t) V_0 dt + \int_{t_p/2}^{t_p} i(t) (V_p - V_0) dt \right\} \quad \left| \begin{array}{l} i_p = i \\ i_n = -i \end{array} \right. \\
 &= \frac{1}{t_p} \left\{ \int_0^{t_p/2} -(i(t) dt) V_0 + \int_{t_p/2}^{t_p} (i(t) dt) (V_p - V_0) \right\} \quad \left| \begin{array}{l} i(t) = C_L \frac{dV_0}{dt} \\ \Rightarrow i(t) dt = C_L dV_0 \end{array} \right. \\
 &= \frac{1}{t_p} \left\{ \int_{V_p}^0 -(C_L dV_0) V_0 + \int_0^{V_p} (C_L dV_0) (V_p - V_0) \right\} \quad \left| \begin{array}{l} t=0 \rightarrow V_0 = V_p \\ t=t_p/2 \rightarrow V_0 = 0 \\ t=t_p \rightarrow V_0 = V_p \end{array} \right. \\
 &= \frac{C_L}{t_p} \left\{ \int_0^{V_p} V_0 dV_0 + \int_0^{V_p} (V_p - V_0) dV_0 \right\} \\
 &= \frac{C_L}{t_p} \left\{ \int_0^{V_p} (V_0 + V_p - V_0) dV_0 \right\} \\
 &= \frac{C_L}{t_p} \left\{ \int_0^{V_p} V_p dV_0 \right\} \\
 &= \frac{C_L \cdot V_p}{t_p} \int_0^{V_p} dV_0 \\
 &= \frac{C_L \cdot V_p}{t_p} [V_0]_0^{V_p}
 \end{aligned}$$

What is the average value of the current?

$$= \frac{C_L V_P}{t_P} [V_P - 0]$$

$$= \frac{C_L \cdot V_P \cdot V_P}{t_P}$$

$$= \frac{C_L \cdot V_P^2}{t_P}$$

$$\boxed{P_d = \frac{C_L \cdot V_P^2}{t_P}}$$

$$P_d = C_L \cdot f_P \cdot V_P^2$$

$$I = \frac{C_L}{t_P} \cdot V_P \cdot V_P$$

$$\frac{1}{t_P} = f_P$$

Proof & maths are required.



$$\int_0^{t_P} I dt = \text{area under the curve}$$

$$\int_0^{t_P} I dt = \frac{I_P \cdot t_P}{2}$$

$$\int_0^{t_P} I dt = \frac{I_P \cdot t_P}{2}$$

$$\int_0^{t_P} \left(\frac{I_P}{t_P} \cdot t \right) dt = \frac{I_P}{t_P} \int_0^{t_P} t dt$$

$$\int_0^{t_P} \left(\frac{I_P}{t_P} \cdot t \right) dt = \frac{I_P}{t_P} \cdot \left(\frac{t^2}{2} \right) \Big|_0^{t_P}$$

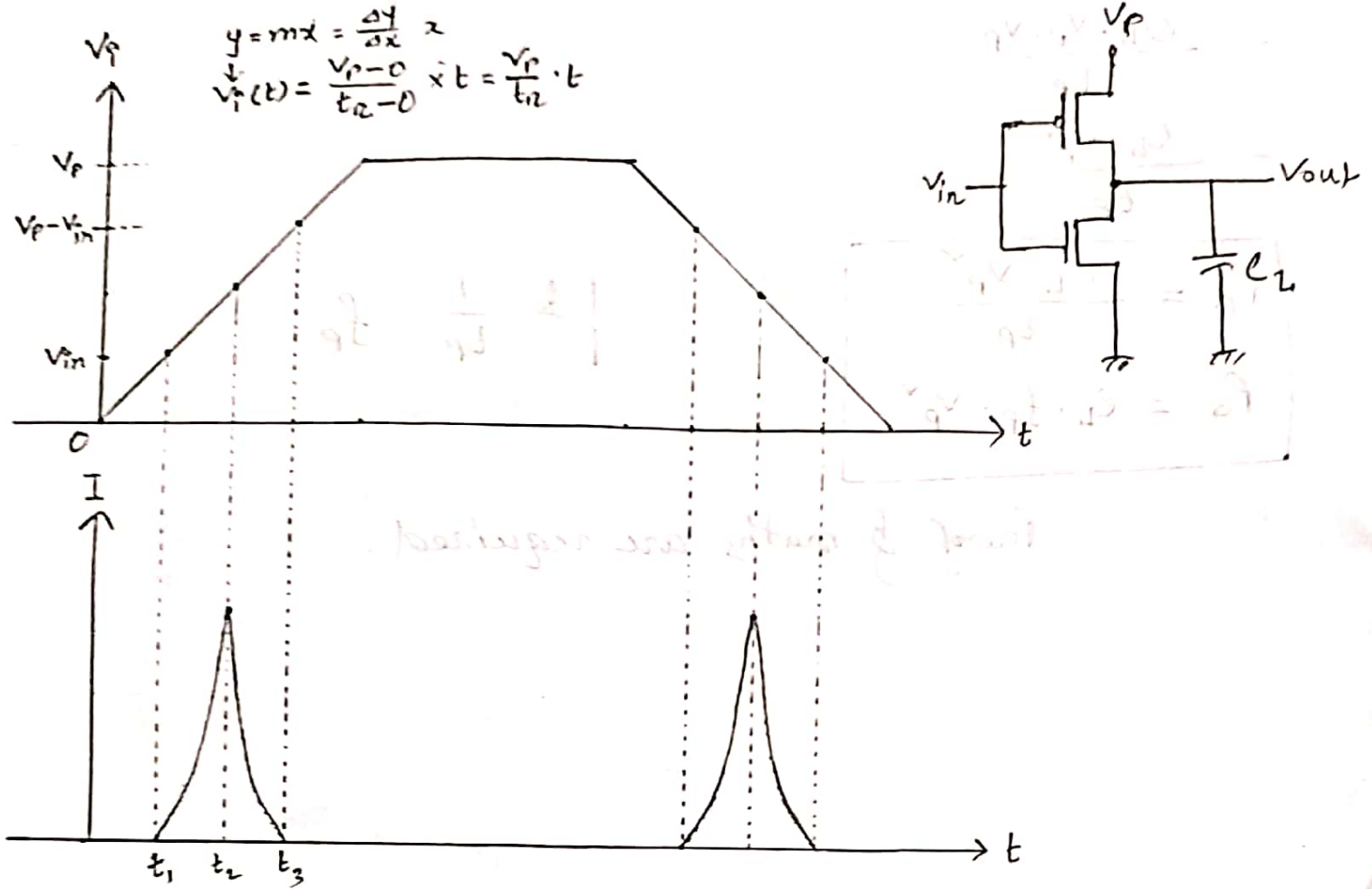
Short Circuit Power Dissipation

Q. When this happens? Ans:- When both NMOS and PMOS transistors are on.

Short circuit power dissipation = $I \times V$

$$y = mx = \frac{\Delta y}{\Delta x} x$$

$$V_p(t) = \frac{V_p - 0}{t_2 - 0} \times t = \frac{V_p}{t_2} \cdot t$$



$$I_{mean} = \frac{1}{t_p} \int_0^{t_p} I(t) dt$$

$$= \frac{2}{t_p} \int_0^{t_3} I(t) dt$$

$$= \frac{4}{t_p} \int_{t_1}^{t_2} I(t) dt$$

$$= \frac{4}{t_p} \int_{t_1}^{t_2} \left\{ \frac{\epsilon \mu_n}{D} \frac{W}{L} \frac{(V_{gs}(t) - V_{tn})^2}{2} \right\} dt$$

$$= \frac{4}{t_p} \left(\frac{\epsilon \mu_n}{D} \frac{W}{L} \right) \cdot \frac{1}{2} \int_{t_1}^{t_2} (V_{gs}(t) - V_{tn})^2 dt$$

$$I_{\text{mean}} = \frac{2}{t_p} \beta \int_{t_1}^{t_2} (V_i(t) - V_{tn})^2 dt \quad \left| \quad \frac{G_{Mn}}{D} \cdot \frac{\omega}{L} = \beta \right.$$

$$\text{Now, } V_i(t) = \frac{V_p}{t_n} \cdot t$$

$$t_2 = \frac{t_n}{2}$$

$$\text{When, } t = t_1, V_i = V_{tn}$$

$$\text{So, } V_{tn} = \frac{V_p}{t_n} \cdot t_1$$

$$\Rightarrow t_1 = \frac{V_{tn} \cdot t_n}{V_p}$$

$$\begin{aligned} \text{So, } I_{\text{mean}} &= \frac{2\beta}{t_p} \int_{\frac{V_{tn} \cdot t_n}{V_p}}^{t_n/2} \left(\frac{V_p}{t_n} \cdot t - V_{tn} \right)^2 dt \\ &= \frac{2\beta}{t_p} \left[\frac{\left(\frac{V_p}{t_n} t - V_{tn} \right)^3}{3 \cdot \frac{V_p}{t_n}} \right]_{\frac{V_{tn} \cdot t_n}{V_p}}^{t_n/2} \\ &= \frac{2\beta}{t_p} \frac{t_n}{3 \cdot V_p} \left[\left(\frac{V_p}{t_n} t - V_{tn} \right)^3 \right]_{\frac{V_{tn} \cdot t_n}{V_p}}^{t_n/2} \\ &= \frac{2\beta t_n}{3 t_p V_p} \left[\left(\frac{V_p}{t_n} \cdot \frac{t_n}{2} - V_{tn} \right)^3 - \left(\frac{V_p}{t_n} \cdot \frac{V_{tn} \cdot t_n}{V_p} - V_{tn} \right)^3 \right] \\ &= \frac{2\beta t_n}{3 t_p V_p} \left[\left(\frac{V_p}{2} - V_{tn} \right)^3 - (V_{tn} - V_{tn})^3 \right] \\ &= \frac{2\beta t_n}{3 t_p V_p} \cdot \left(\frac{V_p}{2} - V_{tn} \right)^3 \\ &= \frac{2\beta t_n}{3 t_p V_p} \cdot \frac{1}{2^3} (V_p - 2V_{tn})^3 \end{aligned}$$

$$I_{\text{mean}} = \frac{\beta t_n}{12 t_p V_p} (V_p - 2V_{tn})^3$$

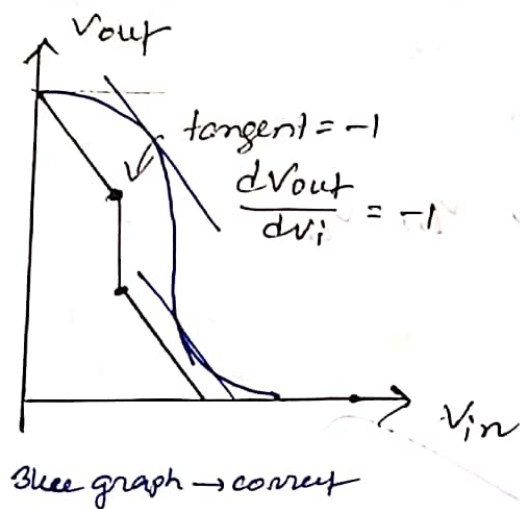
→ Short Circuit Power Dissipation
Average Current.

$$P_{sc} = I_{\text{mean}} \times V_p$$

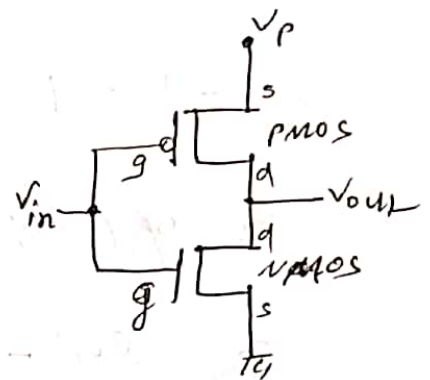
$$\Rightarrow P_{sc} = \frac{\beta t_n}{12 t_p} (V_p - 2V_{tn})^3$$

NOISE MARGIN (CMOS)

V_{IL} = Highest voltage reliably recognized as LOW.



NMOS \rightarrow Saturation
PMOS \rightarrow Resistive



$$I_P(\text{res}) = I_N(\text{sat})$$

$$\Rightarrow \frac{C_{MP}}{D} \frac{W_P}{L_P} \left[(V_{SG} - V_{TP})^2 V_{SD} - \frac{V_{SD}^3}{2} \right] = \frac{C_{MN}}{D} \frac{W_N}{L_N} \frac{(V_{GS} - V_{TN})^2}{2}$$

now,

$$\mu_P = \frac{1}{2} \mu_N$$

$$\Rightarrow \mu_P / \mu_N = \frac{1}{2}$$

let,

$$\frac{W_P}{L_P} = 2 \cdot \frac{W_N}{L_N}$$

$$\frac{W_P/L_P}{W_N/L_N} = 2$$

$$\Rightarrow \frac{1}{2} \cdot 2 \left[(V_{SG} - V_{TP}) V_{SD} - \frac{V_{SD}^3}{2} \right] = \frac{(V_{GS} - V_{TN})^2}{2}$$

$$\Rightarrow (V_P - V_G - V_{TP})(V_P - V_{out}) - \frac{(V_P - V_{out})^3}{2} = \frac{(V_G - V_{TN})^2}{2}$$

$$\Rightarrow 2(V_P - V_G - V_{TP})(V_P - V_{out}) - (V_P - V_{out})^3 = (V_G - V_{TN})^2$$

$$\Rightarrow \frac{(V_P - V_{out})^3}{x} - 2(V_P - V_G - V_{TP}) \frac{(V_P - V_{out})}{x} + (V_G - V_{TN})^2 = 0$$

$$V_P - V_{out} = \frac{2(V_P - V_G - V_{TP}) \pm \sqrt{4(V_P - V_G - V_{TP})^2 - 4(V_G - V_{TN})^2}}{2}$$

$$\Rightarrow V_P - V_{out} = \frac{2(V_P - V_G - V_{TP}) \pm 2\sqrt{(V_P - V_G - V_{TP})^2 - (V_G - V_{TN})^2}}{2}$$

$$\Rightarrow V_p - v_{out} = V_p - v_i - v_{tp} \pm \sqrt{(V_p - v_i - v_{tp} + v_{tn})(V_p - v_i - v_{tp} - v_{tn})}$$

$$\Rightarrow -v_{out} = -v_i - v_{tp} \pm \sqrt{(V_p - v_{tp} - v_{tn})(V_p - 2v_i - v_{tp} + v_{tn})}$$

$$\Rightarrow v_{out} = v_i + v_{tp} \pm \sqrt{(V_p - v_{tp} - v_{tn})} \sqrt{(V_p - 2v_i - v_{tp} + v_{tn})}$$

Differentiating w.r.t v_i ,

$$\frac{dv_{out}}{dv_i} = 1 + 0 \pm \sqrt{V_p - v_{tp} - v_{tn}} \cdot \frac{-2}{2\sqrt{V_p - 2v_i - v_{tp} + v_{tn}}}$$

$$\Rightarrow -1 = 1 \pm \frac{\sqrt{V_p - v_{tp} - v_{tn}}}{\sqrt{V_p - 2v_i - v_{tp} + v_{tn}}}$$

$$\Rightarrow 2 = \pm \frac{\sqrt{V_p - v_{tp} - v_{tn}}}{\sqrt{V_p - 2v_i - v_{tp} + v_{tn}}}$$

$$\Rightarrow 4(V_p - 2v_i - v_{tp} + v_{tn}) = V_p - v_{tp} - v_{tn}$$

$$\Rightarrow 3V_p - 8v_i - 3v_{tp} + 5v_{tn} = 0$$

$$\Rightarrow 8v_i = 3V_p - 3v_{tp} + 5v_{tn}$$

$$\Rightarrow v_i = \frac{3V_p - 3v_{tp} + 5v_{tn}}{8} = V_{IL}$$

Beyond this value, v_i can not be considered as low.

$$NM(0) = V_{IL} - 0$$

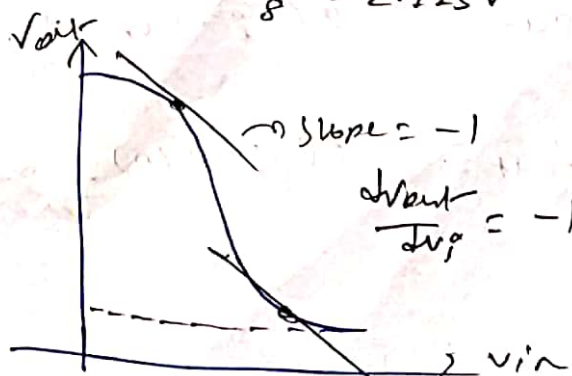
$$= 2.125 - 0$$

$$= 2.125V$$

$$NM(0) = 2.125V$$

$$V_{IL} = \frac{3 \cdot 5 - 3 \cdot 1 + 5 \cdot 1}{8} \quad \begin{cases} V_p = 5 \\ v_{tp} = 1 \\ v_{tn} = 1 \end{cases}$$

$$= \frac{17}{8} = 2.125V$$

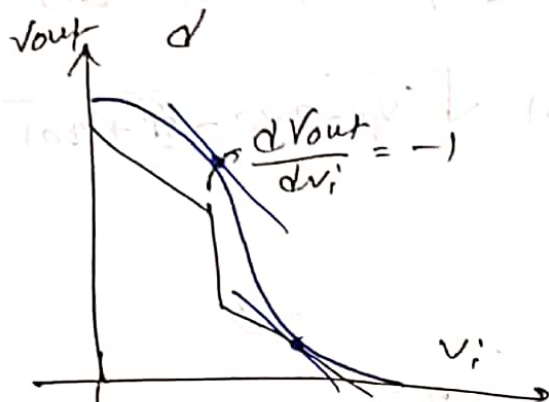


Home work

$$NM(1) : -V_{TH} = \frac{5V_P - 5V_{TP} + 3V_{TN}}{8}$$

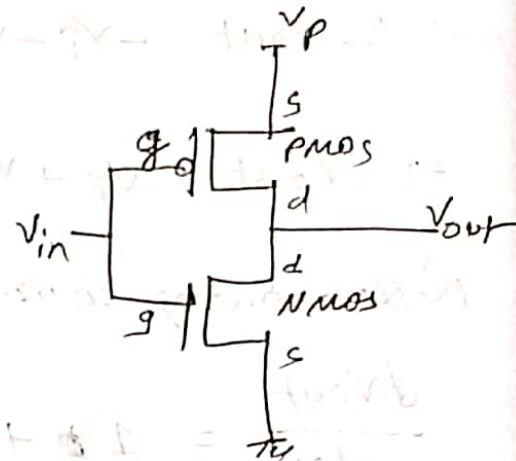
Proof:

Here,



* blue graph correct

PMOS → saturation
NMOS → resistive



$$I_N(\text{res}) = I_P(\text{sat}^n)$$

$$\Rightarrow \frac{\epsilon \mu_N}{D} \frac{W_N}{L_N} \left[(V_{GS} - V_{TN}) V_{DS} - \frac{V_{DS}^2}{2} \right] = \frac{\epsilon \mu_P}{D} \frac{W_P}{L_P} \frac{(V_{SG} - V_{TP})^2}{2}$$

we know, $\mu_N = 2\mu_P$

$$\Rightarrow \mu_N / \mu_P = 2$$

$$\text{let, } \frac{W_P}{L_P} = 2 \cdot \frac{W_N}{L_N}$$

$$\frac{W_N / L_N}{W_P / L_P} = \frac{1}{2}$$

$$\Rightarrow 2 \cdot \frac{1}{2} \left[(V_{GS} - V_{TN}) V_{DS} - \frac{V_{DS}^2}{2} \right] = \frac{(V_{SG} - V_{TP})^2}{2}$$

$$\Rightarrow \left[(V_i - V_{TN}) V_{out} - \frac{V_{out}^2}{2} \right] = \frac{(V_P - V_i - V_{TP})^2}{2}$$

$$\Rightarrow 2(V_i - V_{TN}) V_{out} - V_{out}^2 = (V_P - V_i - V_{TP})^2$$

$$\Rightarrow V_{out}^2 - 2(V_i - V_{TN}) V_{out} + (V_P - V_i - V_{TP})^2 = 0$$

$$V_{out} = \frac{2(V_i - V_{TN}) \pm \sqrt{4(V_i - V_{TN})^2 - 4(V_P - V_i - V_{TP})^2}}{2}$$

$$\Rightarrow V_{out} = \frac{2(V_i - V_{TN}) \pm 2\sqrt{(V_i - V_{TN})^2 - (V_P - V_i - V_{TP})^2}}{2}$$

$$\Rightarrow V_{out} = (V_p - V_{tn}) \pm \sqrt{(V_p - V_{tn} + V_p - V_p - V_{tp})(V_p - V_{tn} - V_p + V_p + V_{tp})}$$

$$\Rightarrow V_{out} = V_p - V_{tn} \pm \sqrt{V_p - V_{tn} - V_{tp}} \sqrt{2V_p - V_p - V_{tn} + V_{tp}}$$

Differentiating w.r.t. V_p

$$\frac{dV_{out}}{dV_p} = 1 - 0 \pm \frac{\sqrt{V_p - V_{tn} - V_{tp}}}{2\sqrt{2V_p - V_p - V_{tn} + V_{tp}}} \cdot 2$$

$$\Rightarrow -1 = 1 \pm \frac{\sqrt{V_p - V_{tn} - V_{tp}}}{\sqrt{2V_p - V_p - V_{tn} + V_{tp}}}$$

$$\Rightarrow 2 = \pm \frac{\sqrt{V_p - V_{tn} - V_{tp}}}{\sqrt{2V_p - V_p - V_{tn} + V_{tp}}}$$

$$\Rightarrow 4(2V_p - V_p - V_{tn} + V_{tp}) = V_p - V_{tn} - V_{tp}$$

$$\Rightarrow 8V_p - 5V_p - 3V_{tn} + 5V_{tp} = 0$$

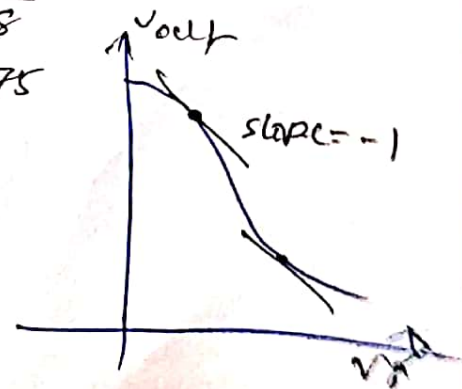
$$\Rightarrow 8V_p = 5V_p + 3V_{tn} - 5V_{tp}$$

$$\Rightarrow V_p = \frac{5V_p - 5V_{tp} + 3V_{tn}}{8} = V_{IH}$$

$$\begin{aligned} NM(1) &= V_{IH} - 5 \\ &= 2.875 - 5 \\ &= -2.125 \end{aligned} \quad \left. \begin{aligned} &5 - V_{IH} \\ &= 5 - 2.875 \\ &= 2.125 \end{aligned} \right\}$$

I'm confused !!

$$\begin{aligned} V_{IH} &= \frac{5.5 - 5.1 + 3.1}{8} \\ &= \frac{23}{8} \\ &= 2.875 \end{aligned}$$

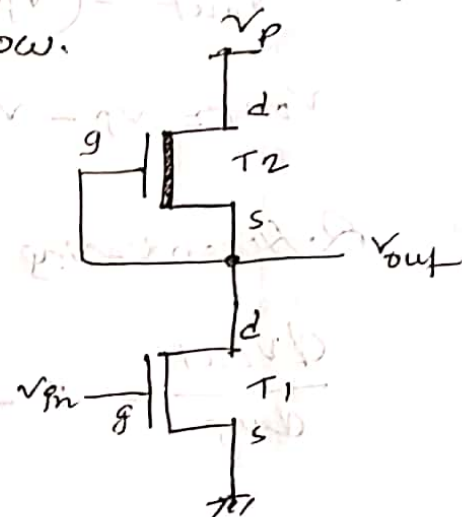


NOISE MARGIN (NMOS inverter)

V_{IL} = Highest voltage reliably recognized as LOW.

Enhancement NMOS (T_1) \rightarrow Satⁿ

Depletion NMOS (T_2) \rightarrow Resistive



$$I_D(\text{res}) = I_E(\text{sat}^n)$$

$$\Rightarrow \frac{C_{MP}}{D} \frac{W_D}{L_D} \left[(V_{GS2} - V_{TD}) V_{DS2} - \frac{V_{DS2}^2}{2} \right] = \frac{C_{ME}}{D} \frac{W_E}{L_E} \frac{(V_{GS1} - V_{TE})^2}{2}$$

For calculation purpose,

let, $\mu_E \rightarrow \mu_D$

$$\frac{W_E/L_E}{W_D/L_D} = K$$

$$\Rightarrow (V_{GS2} - V_{TD}) V_{DS2} - \frac{V_{DS2}^2}{2} = K \frac{(V_{GS1} - V_{TE})^2}{2}$$

$$\Rightarrow (\cancel{V_{GS1}} - V_{TD}) V_{DS}$$

$$\Rightarrow (0 - V_{TD})(V_P - V_{out}) - \frac{(V_P - V_{out})^2}{2} = K \frac{(V_P - V_{TE})^2}{2}$$

$$\Rightarrow -2V_{TD}(V_P - V_{out}) - (V_P - V_{out})^2 = K(V_P - V_{TE})^2$$

$$\Rightarrow (V_P - V_{out})^2 + 2V_{TD}(V_P - V_{out}) + K(V_P - V_{TE})^2 = 0$$

$$V_P - V_{out} = \frac{-2V_{TD} \pm \sqrt{4V_{TD}^2 - 4K(V_P - V_{TE})^2}}{2}$$

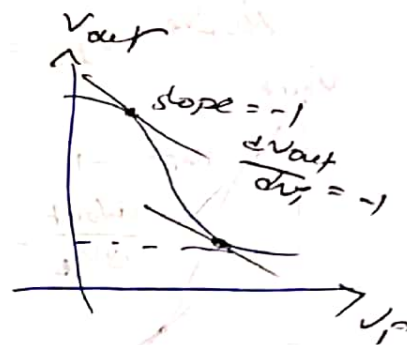
$$\Rightarrow V_P - V_{out} = -V_{TD} \pm \sqrt{V_{TD}^2 - K(V_P - V_{TE})^2}$$

$$\Rightarrow -V_{out} = -V_P - V_{TD} \pm \sqrt{V_{TD}^2 - K(V_P - V_{TE})^2}$$

$$\Rightarrow v_{out} = v_p + v_{td} \pm \sqrt{v_{td}^2 - k(v_p - v_t)^2}$$

Differentiating w.r.t v_p

$$\frac{dv_{out}}{dv_p} = 0 \pm \frac{-2(v_p - v_t)k}{2\sqrt{v_{td}^2 - k(v_p - v_t)^2}}$$



$$\Rightarrow -1 = \pm \frac{(v_p - v_t)k}{\sqrt{v_{td}^2 - k(v_p - v_t)^2}}$$

$$\Rightarrow v_{td}^2 - k(v_p - v_t)^2 = (v_p - v_t)^2 k$$

$$\Rightarrow v_{td}^2 = (v_p - v_t)^2 k + k(v_p - v_t)^2$$

$$\Rightarrow v_{td}^2 = (v_p - v_t)^2 (k + k)$$

$$\Rightarrow (v_p - v_t)^2 = \frac{v_{td}^2}{k + k}$$

$$\Rightarrow v_p - v_t = \sqrt{\frac{v_{td}^2}{k + k}}$$

$$\Rightarrow v_p = \sqrt{\frac{v_{td}^2}{k + k}} + v_t$$

$$v_{IL} = v_t + \sqrt{\frac{v_{td}^2}{k + k}} = v_t + \frac{v_{td}}{\sqrt{k + k}}$$

$$\left. \begin{array}{l} v_t = 1 \\ k = 6 \\ v_{td} = 4 \end{array} \right\}$$

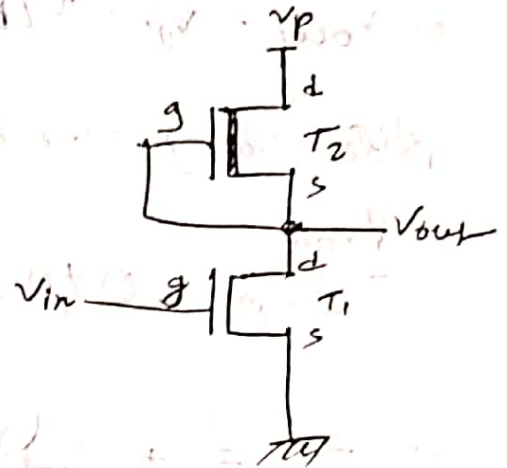
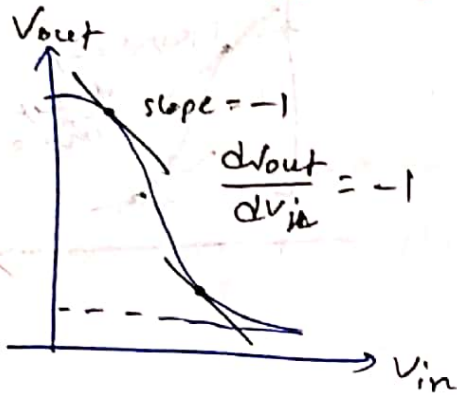
$$v_{IL} = 1 + \frac{4}{\sqrt{6 + 6}} = 1 + \frac{4}{\sqrt{12}} = 1.62$$

$$NM(0) = 1.62 - 0.2$$

$$v_{IL} \approx 1.42$$

Homework

Derive, $V_{IH} = V_t + \frac{2V_{DD}}{\sqrt{3K}}$



Enhancement NMOS (T_1) \rightarrow Resistive

Depletion NMOS (T_2) \rightarrow Saturation

$$I_E (res) = I_D (sat^n)$$

$$\Rightarrow \frac{\mu_n C_{ox}}{L} \frac{W_E}{L_E} \left[(V_{GS1} - V_t) V_{DS1} - \frac{V_{DS1}^2}{2} \right] = \frac{\mu_p C_{ox}}{L} \frac{W_D}{L_D} \frac{(V_{GS2} - V_{DD})^2}{2}$$

For calculation purposes,
we know

Let, $\mu_n \approx \mu_p$

$$\frac{W_E/L_E}{W_D/L_D} = K$$

$$\Rightarrow K \left[(V_{GS1} - V_t) V_{DS1} - \frac{V_{DS1}^2}{2} \right] = \frac{(V_{GS2} - V_{DD})^2}{2}$$

$$\Rightarrow \left[(V_{in} - V_t) V_{out} - \frac{V_{out}^2}{2} \right] = \frac{(0 - V_{DD})^2}{2K}$$

$$\Rightarrow \left[(V_{in} - V_t) V_{out} - \frac{V_{out}^2}{2} \right] = -\frac{V_{DD}^2}{2K}$$

$$\Rightarrow \left[2(V_{in} - V_t) V_{out} - V_{out}^2 \right] = -\frac{V_{DD}^2}{K}$$

$$\Rightarrow V_{out}^2 - 2(V_{in} - V_t) V_{out} + \frac{V_{DD}^2}{K} = 0$$

$$V_{out} = \frac{2(V_{in} - V_t) \pm \sqrt{4(V_{in} - V_t)^2 - 4 \cdot \frac{V_{DD}^2}{K}}}{2}$$

$$V_{out} = (V_i - V_E) \pm \sqrt{(V_i - V_E)^2 - V_{EO}^2/k}$$

Differentiating w.r.t V_i ,

$$\frac{dV_{out}}{dV_i} = (1 - 0) \pm \frac{2(V_i - V_E)}{2\sqrt{(V_i - V_E)^2 - V_{EO}^2/k}}$$

$$\Rightarrow -1 = 1 \pm \frac{2(V_i - V_E)}{2\sqrt{(V_i - V_E)^2 - V_{EO}^2/k}}$$

$$\Rightarrow 2 = \pm \frac{(V_i - V_E)}{\sqrt{(V_i - V_E)^2 - V_{EO}^2/k}}$$

$$\Rightarrow 4[(V_i - V_E)^2 - V_{EO}^2/k] = (V_i - V_E)^2$$

$$\Rightarrow 4(V_i - V_E)^2 - 4 \frac{V_{EO}^2}{k} = (V_i - V_E)^2$$

$$\Rightarrow 3(V_i - V_E)^2 = 4 \frac{V_{EO}^2}{k}$$

$$\Rightarrow \sqrt{3} (V_i - V_E) = \frac{4 V_{EO}}{3k}$$

$$\Rightarrow V_i - V_E = \frac{4 V_{EO}}{\sqrt{3} k}$$

$$\Rightarrow V_i = V_E + \frac{4 V_{EO}}{\sqrt{3} k} = V_{IH}$$

$$V_{IH} = V_E + \frac{4 V_{EO}}{\sqrt{3} k}$$

$$V_{IH} = 1 + \frac{2 \cdot 4}{\sqrt{3} \times 6} = 2.89 V$$

$$\left. \begin{aligned} NM(1) &= 5 - V_{IH} \\ &= 5 - 2.89 \\ &= 2.11 V \end{aligned} \right\} \begin{aligned} NM(1) &= V_{IH} - 5 \\ &= 2.89 - 5 \\ &= -2.11 \end{aligned}$$