

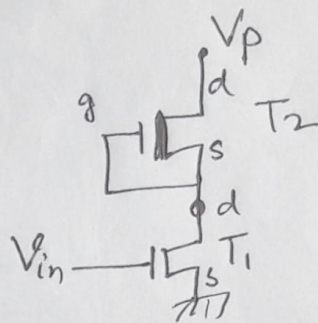
# Noise Margin (NMOS inverter with Depletion load)

When

$$V_{IL}$$

$T_1$  (enhancement) in Sat<sup>n</sup>

$T_2$  (depletion) in Resistive



$$\frac{\cancel{\epsilon \mu_N}}{\cancel{D}} \cdot \left( \frac{W_E}{L_E} \right)^{\frac{1}{2}} (V_{gs} - V_t)^r = \frac{\cancel{\epsilon \mu_N}}{\cancel{D}} \cdot \left( \frac{W_D}{L_D} \right) \cdot \left[ (V_{gsD} - V_{tD}) V_{dsD} - \frac{V_{dsD}^2}{2} \right]$$

$25 \approx 30$  ,  $\left( \frac{W_E}{L_E} \right) / \left( \frac{W_D}{L_D} \right) = K$

$$\Rightarrow \frac{1}{2} K (V_i - V_t)^r = (0 - V_{tD}) \cdot (V_p - V_o) - \frac{1}{2} (V_p - V_o)^r = 0$$

$$\Rightarrow (V_p - V_o)^r + 2V_{tD} (V_p - V_o) + K (V_i - V_t)^r = 0$$

$$\therefore V_p - V_o = \frac{-2V_{tD} \pm \sqrt{4V_{tD}^2 - 4K(V_i - V_t)^r}}{2 \cdot 1}$$

$$= -V_{tD} \pm \sqrt{V_{tD}^2 - K(V_i - V_t)^r}$$

$$\therefore V_o = V_p + V_{tD} \pm \sqrt{V_{tD}^2 - K(V_i - V_t)^r}$$

Differentiating w.r.t.  $V_i$ , we get

$$\frac{dV_o}{dV_i} = 0 \pm \frac{1}{2} \cdot \frac{1 \cdot (-2K) \cdot (V_i - V_t)^{r-1}}{\sqrt{V_{tD}^2 - K(V_i - V_t)^r}}$$

$$\Rightarrow -1 = \pm \frac{K(V_i - V_t)^{r-1}}{\sqrt{V_{tD}^2 - K(V_i - V_t)^r}}$$

$$\Rightarrow V_{tD}^2 - K(V_i - V_t)^2 = K^2(V_i - V_t)^2$$

$$\therefore (K^2 + K)(V_i - V_t)^2 = V_{tD}^2$$

$$\Rightarrow V_i - V_t = \frac{V_{tD}}{\sqrt{K^2 + K}}$$

$$\therefore V_i = \boxed{V_t + \frac{V_{tD}}{\sqrt{K^2 + K}} = V_{IL}}$$

$$V_{tD} = 4, \quad K = 6, \quad V_{out} = 0.3$$

$$V_{IL} = 1 + \frac{4}{\sqrt{6^2 + 6}} = 1 + \frac{4}{\sqrt{42}} = 1.617$$

$$\therefore NM(0) = 1.617 - 0.3$$

$$\boxed{NM(0) = 1.317 \text{ volt}}$$

$$\boxed{V_{OH} = V_t + \frac{2V_{tD}}{\sqrt{3K}}}$$