

REVISION HISTORY

DATE:	DESCRIPTION	REVISION
Nov. 2009	First Created	1.0

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Page 2: KSZ8895 Serial

Page 3: Transformer & RJ45 Ports

Page 4: Port 5 SW5-MII/RMII Interface

Page 5: P4 SW4-MII/RMII (reserved)
& P5-MII/RMII Interface

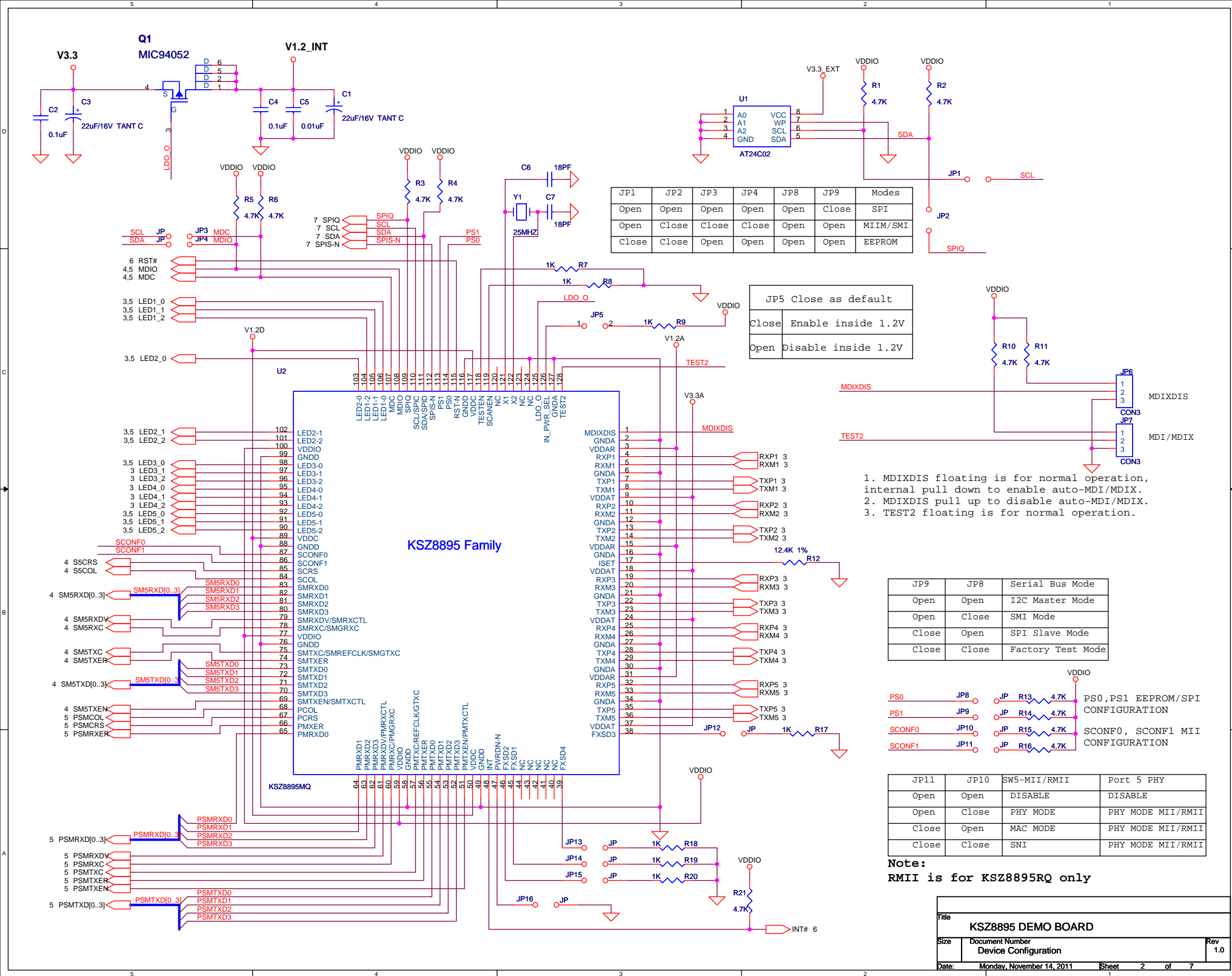
Page 6: Power & Reset

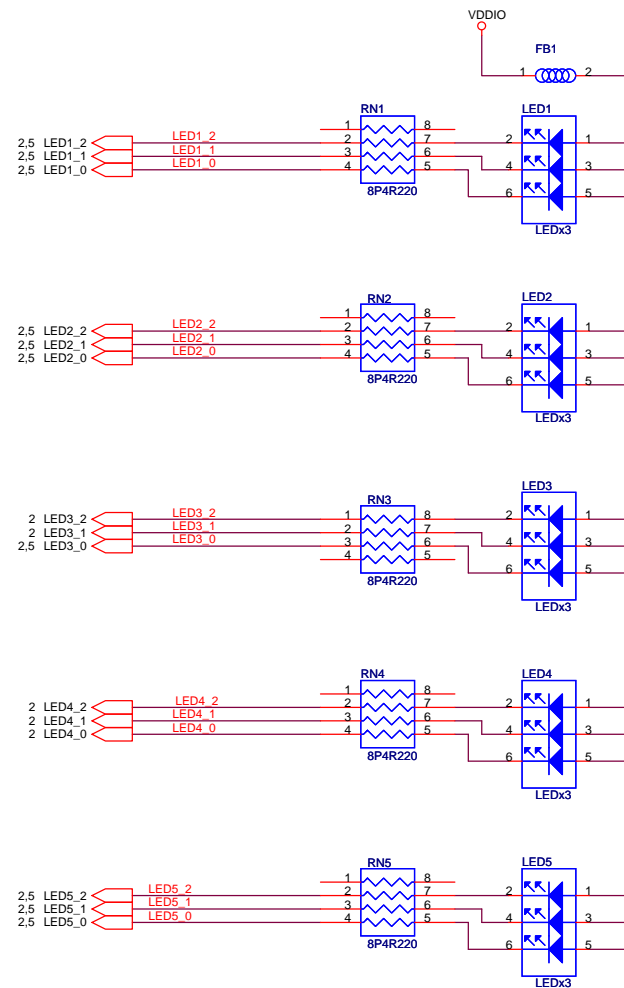
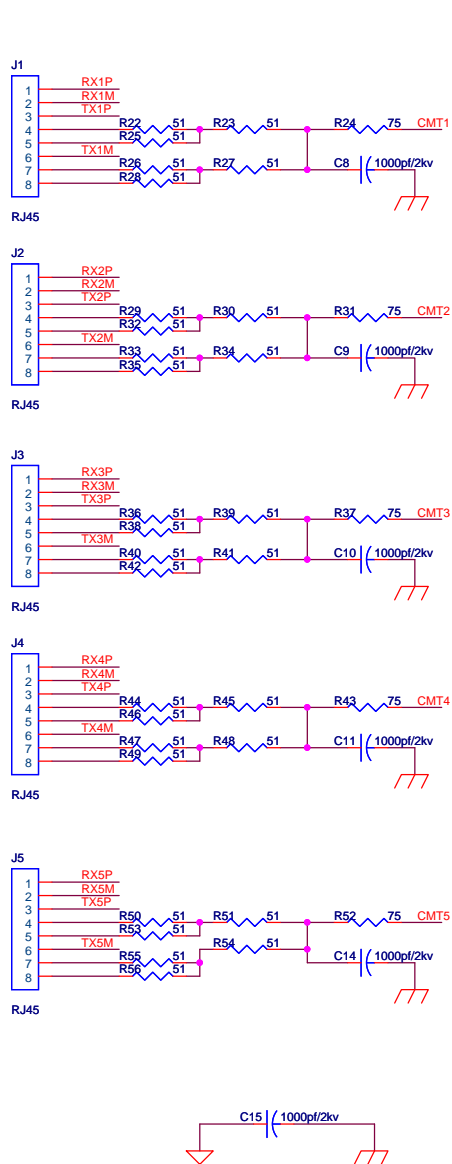
KSZ8895 Family Evaluation Demo Board



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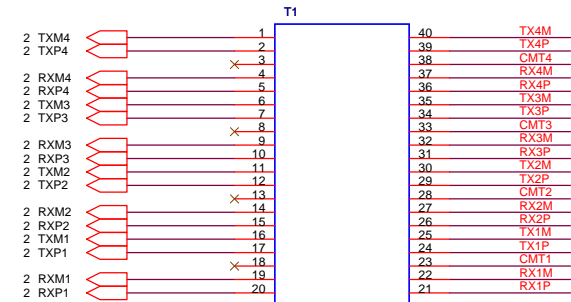
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KSZ8895 DEMO BOARD		
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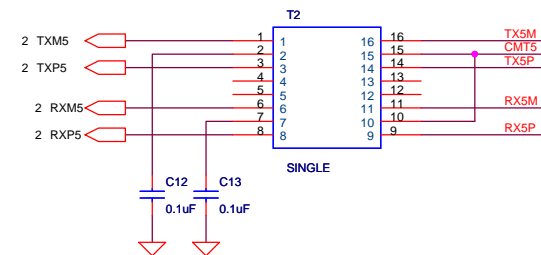


Note:

1. There are no 50ohm termination resistors and external biasing due to support on-chip termination and internal biasing.
2. Please do not connect the center tap of transformer RX/TX together to an external analog power.



Pulse H1664NL
YCL PH406082



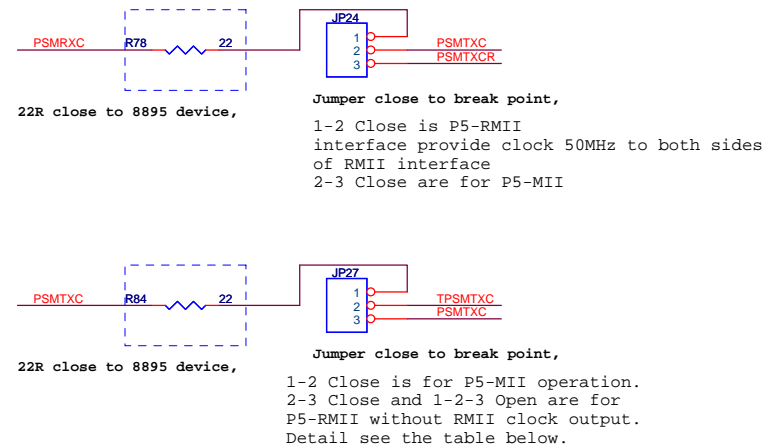
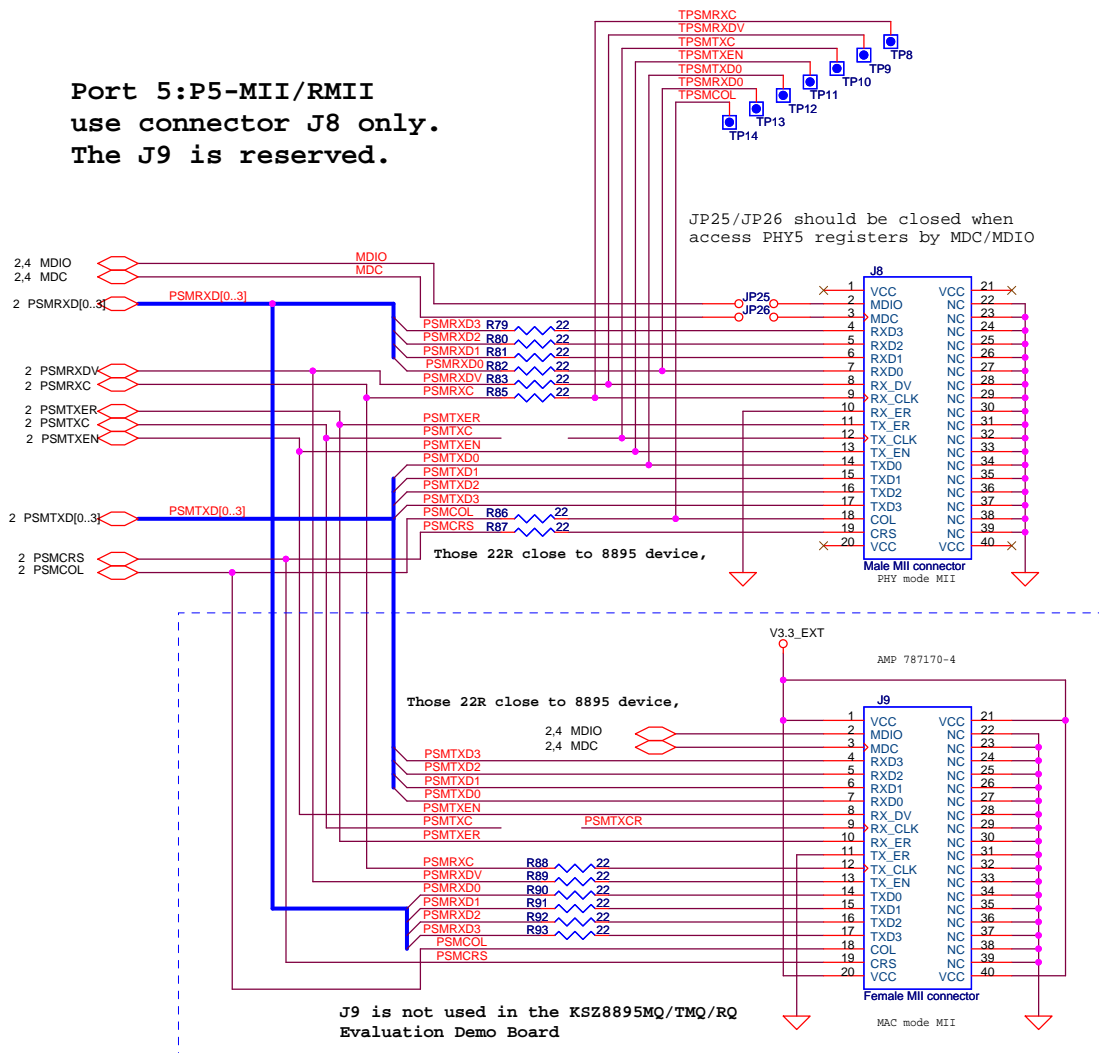
Pulse H1102
Bel Fuse S558-5999-U7
YCL PT163020
Transpower HB726
DELTA LF8505



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Port 5:P5-MII/RMII
use connector J8 only.
The J9 is reserved.



Port 5 PHY5 P5-MII (Default):

JP24 Pin2-3 CLOSE, JP27 Pin1-2 CLOSE,

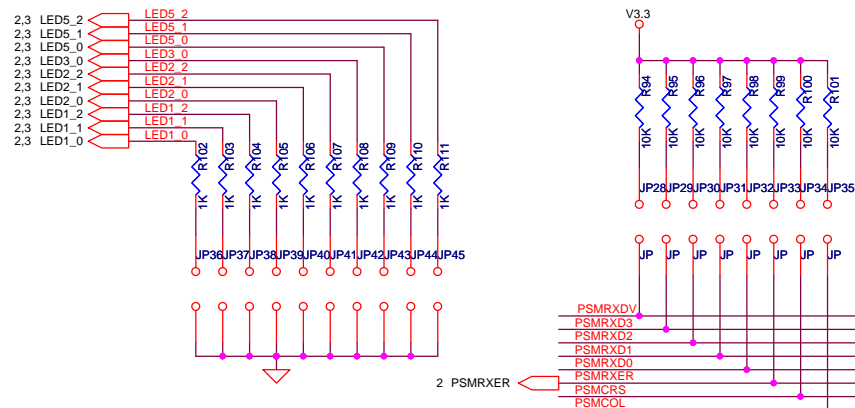
Port 5 PHY5 P5-RMII

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Whatever the device is set to clock mode (default)
or normal mode:
Port 5 RMII provides RMII Clock
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Port 5 PHY5 P5-RMII to any MAC RMII
with receiving RMII clock
JP24 1-2 CLOSE,
JP27 1-2-3 OPEN

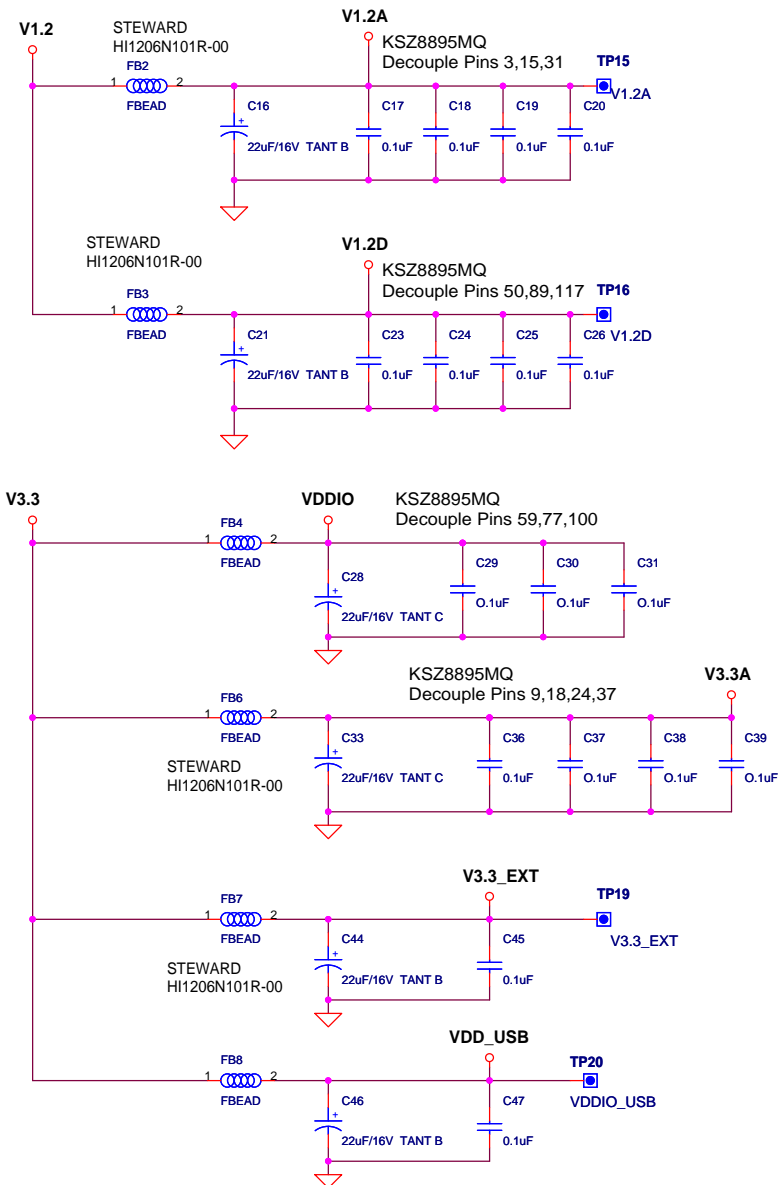
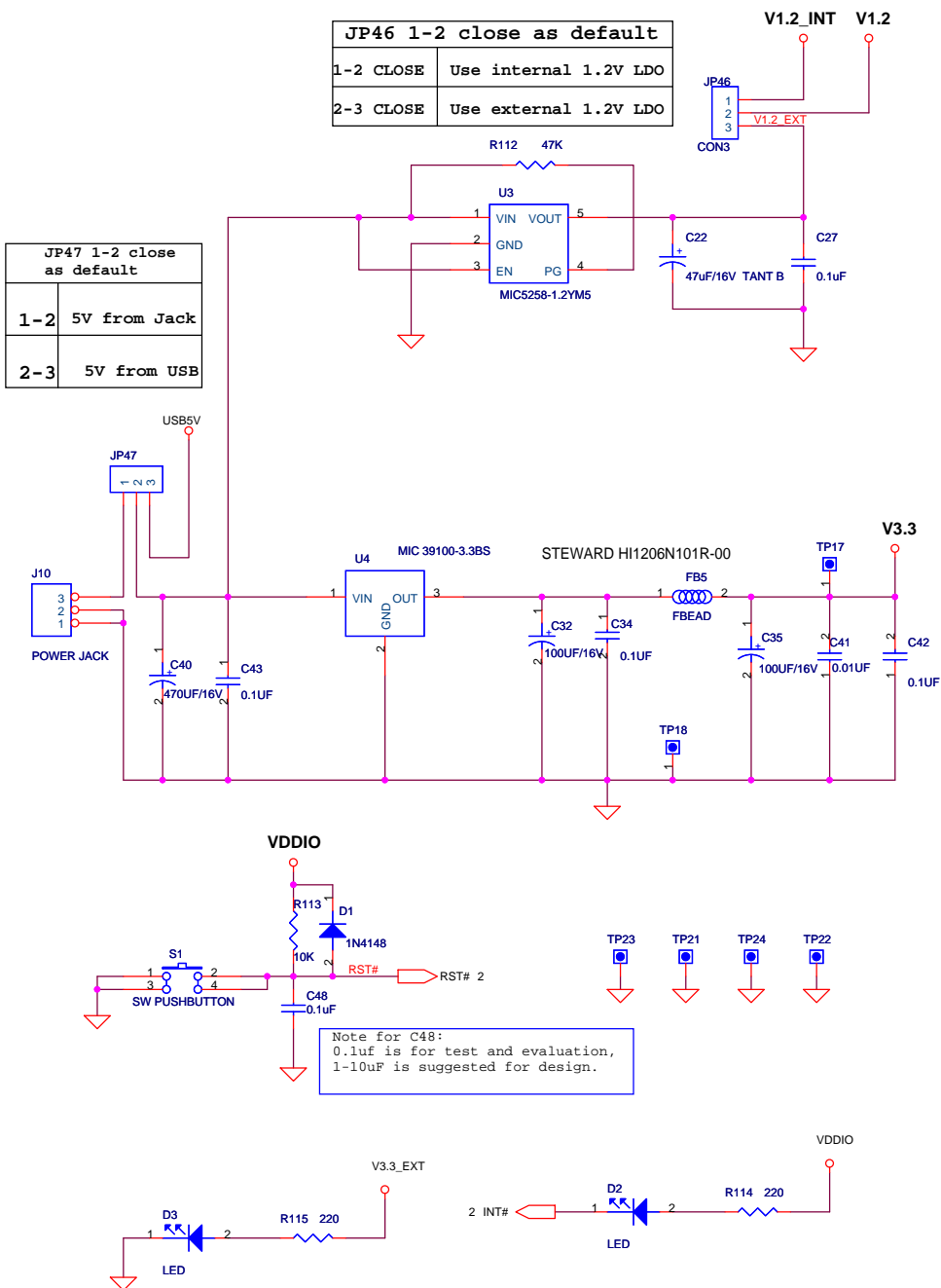
Note:

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MII interface mode is for KSZ8895MQ
RMII Interface mode is for KSZ8895RQ
J9 will be reserved for KSZ8875MQ
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JP46 1-2 close as default	
1-2 CLOSE	Use internal 1.2V LDO
2-3 CLOSE	Use external 1.2V LDO

JP47 1-2 close as default	
1-2	5V from Jack
2-3	5V from USB



Title		
KSZ8895MQ SOCKET BOARD		
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	Power and Reset	1.1
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