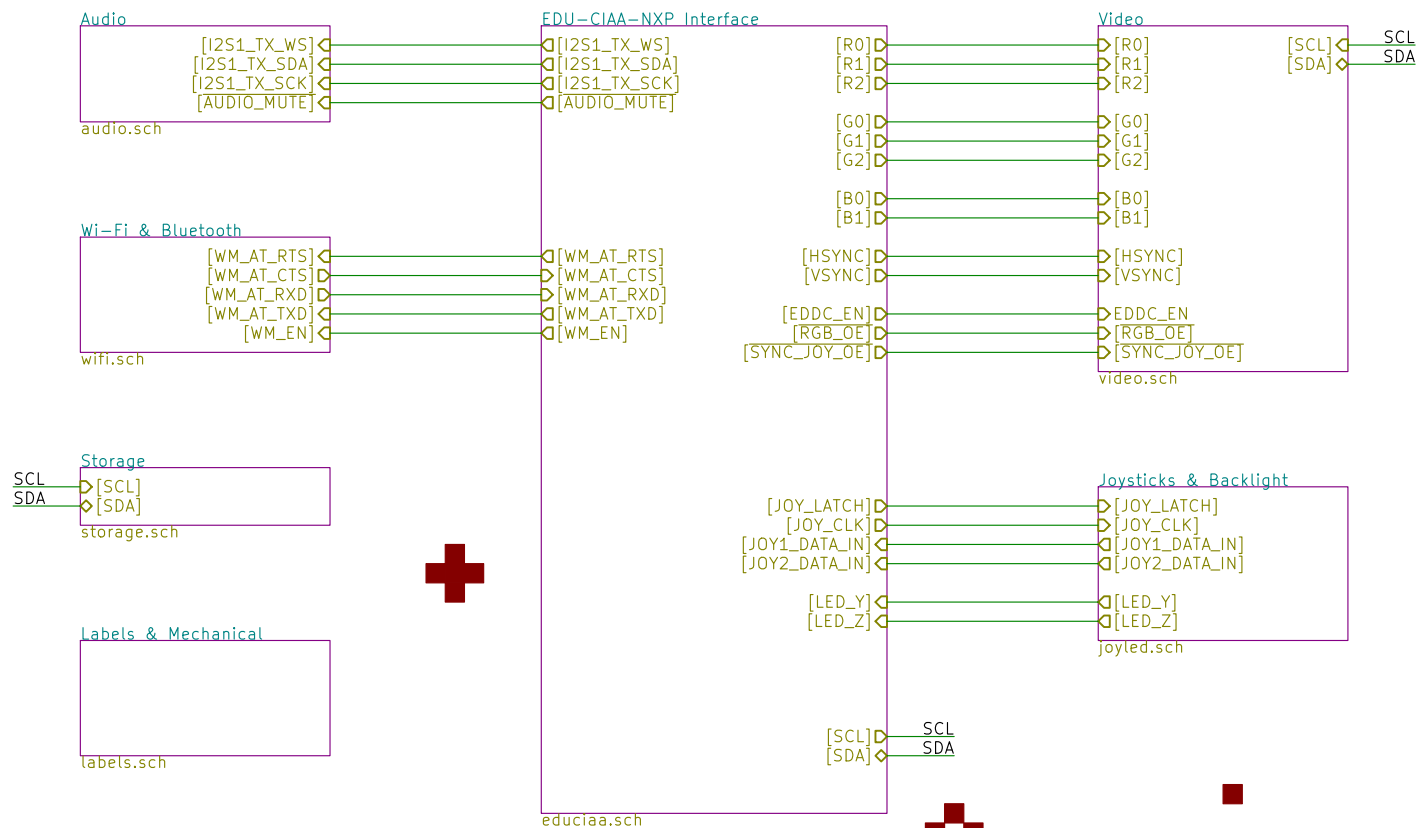


RETRO-CIAA



PAGE INDEX

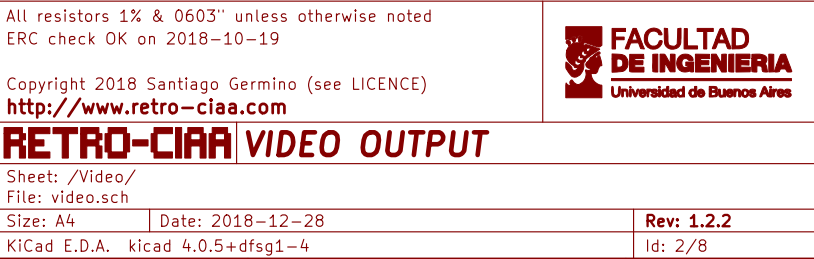
- 1/8 SUBSYSTEMS VIEW (ROOT)
- 2/8 VIDEO OUTPUT
- 3/8 JOYSTICKS & BACKLIGHT
- 4/8 AUDIO OUTPUT
- 5/8 WI-FI & BLUETOOTH
- 6/8 EDU-CIAA-NXP INTERFACE
- 7/8 STORAGE
- 8/8 LABELS & MECHANICAL

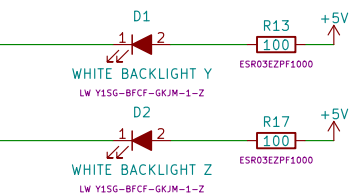
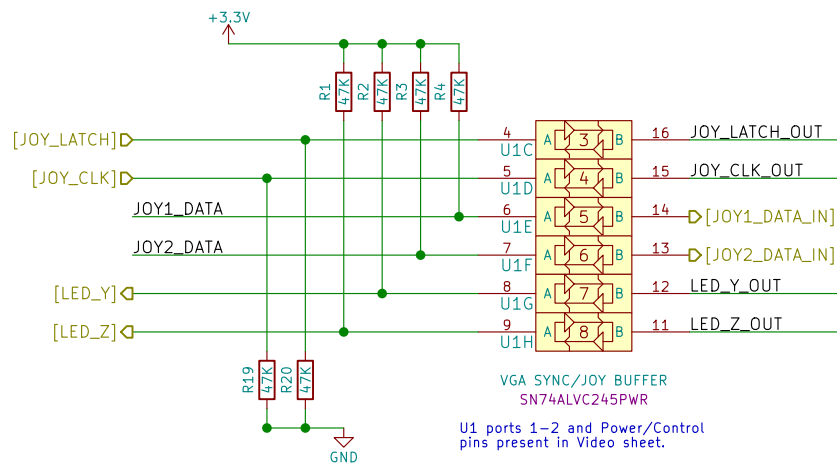
All resistors 1% & 0603" unless otherwise noted
ERC check OK on 2018-10-19
T-Rex image by Chromium Project, Sebastien Gabriel
Copyright 2018 Santiago Germino (see LICENCE)
<http://www.retro-ciaa.com>



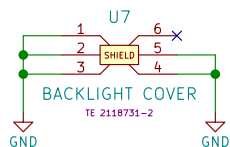
RETRO-CIAA SUBSYSTEMS VIEW

Sheet: /	
File: retro-ciaa.sch	
Size: A4	Date: 2018-12-28
KiCad E.D.A. kicad 4.0.5+dfsg1-4	Rev: 1.2.2
Id: 1/8	

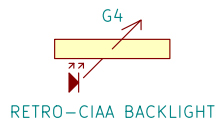




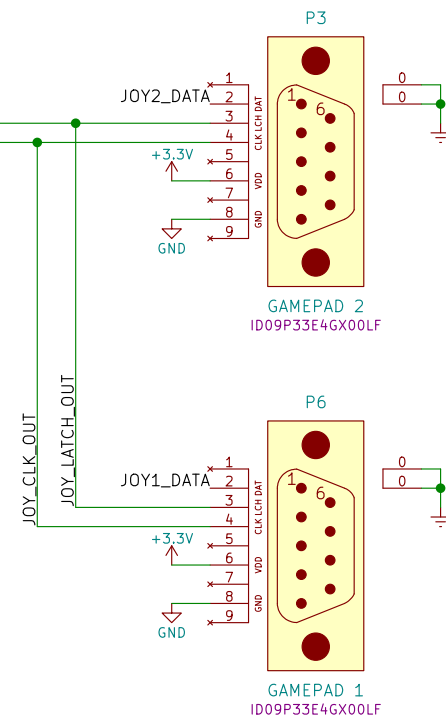
LED supply voltage: 4.83 V
voltage drop: 3.3 V Typ.
current @ 100 Ohm: 15 mA



RF shield used on the back side of the board to contain and reflect led light upwards through the PCB substrate



Glowing logos made with copper stencils and led light passing through the PCB substrate



NES 8 Bit (Family Game) Gamepads using CMOS 4021 shift register.

All resistors 1% & 0603" unless otherwise noted
ERC check OK on 2018-10-19

Copyright 2018 Santiago Germino (see LICENCE)
<http://www.retro-ciaa.com>

RETRO-CIAA JOYSTICKS & BACKLIGHT

Sheet: /Joysticks & Backlight/
File: joyled.sch

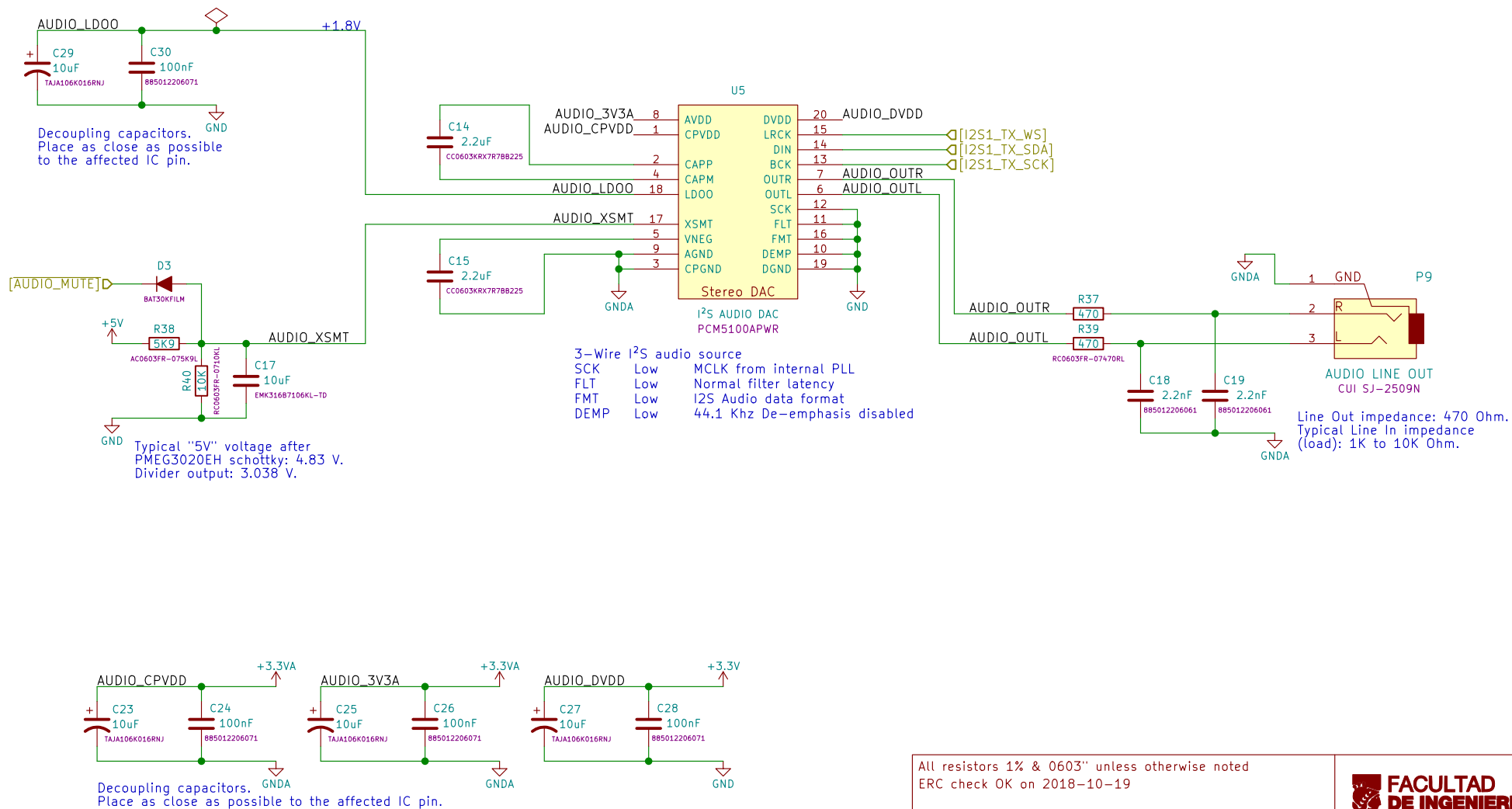
Size: A4 Date: 2018-12-28

KiCad E.D.A. kicad 4.0.5+dfsg1-4

Rev: 1.2.2

Id: 3/8





All resistors 1% & 0603" unless otherwise noted
ERC check OK on 2018-10-19

Copyright 2018 Santiago Germino (see LICENCE)
<http://www.retro-ciaa.com>

RETRO-CIAA AUDIO OUTPUT

Sheet: /Audio/
File: audio.sch

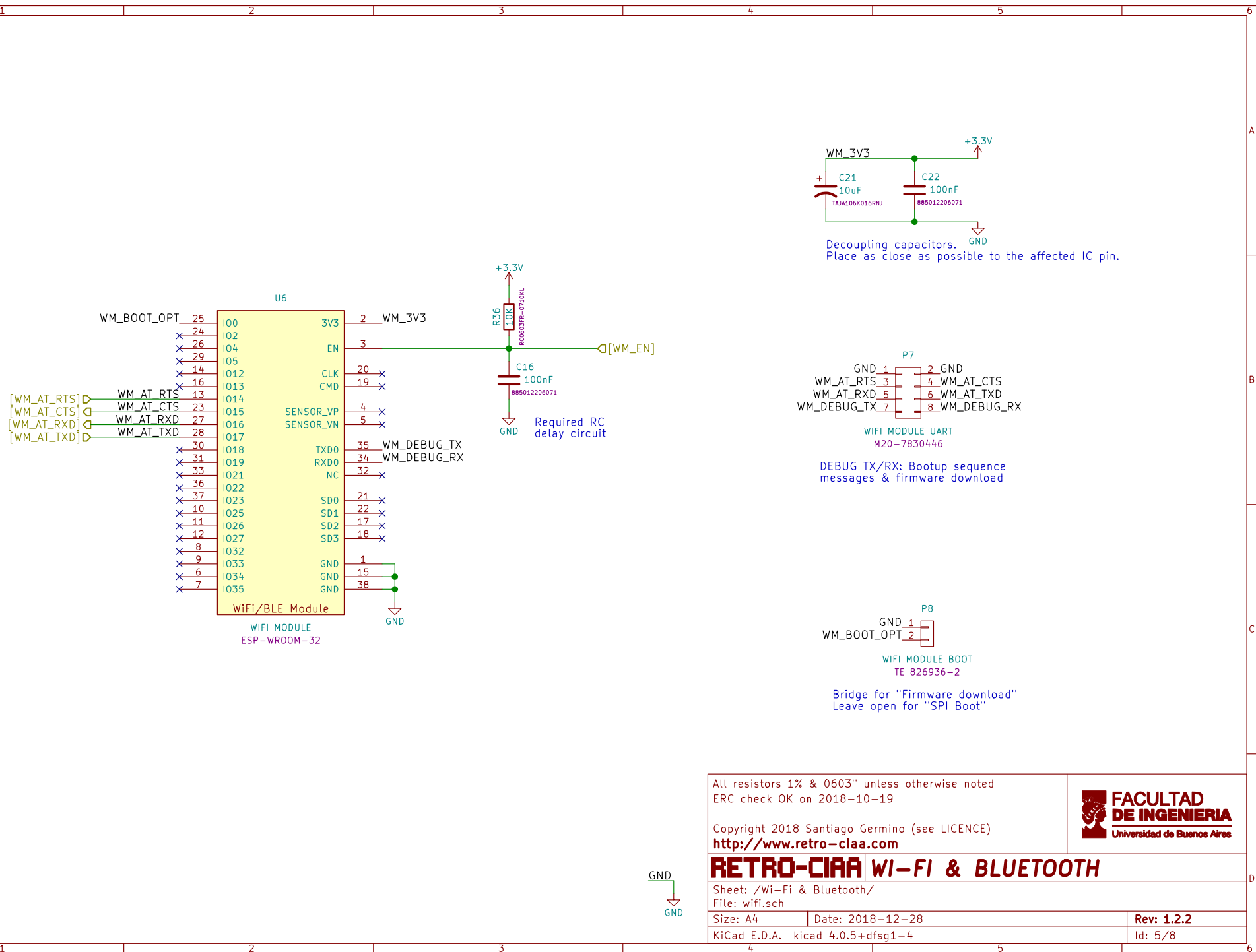
Size: A4 Date: 2018-12-28

KiCad E.D.A. kicad 4.0.5+dfsg1-4

Rev: 1.2.2

Id: 4/8





All resistors 1% & 0603" unless otherwise noted
ERC check OK on 2018-10-19

Copyright 2018 Santiago Germino (see LICENCE)
<http://www.retro-ciaa.com>



RETRO-CIAA WI-FI & BLUETOOTH

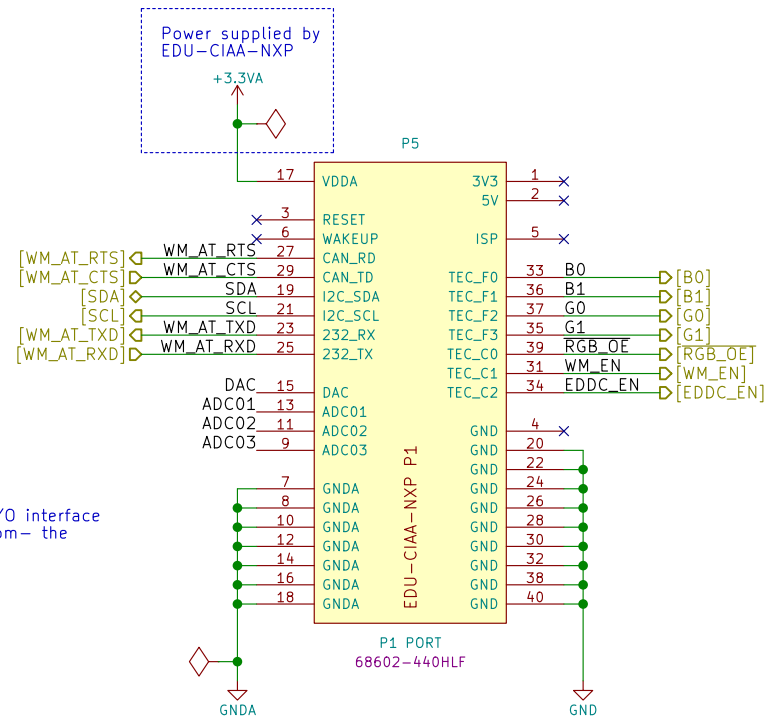
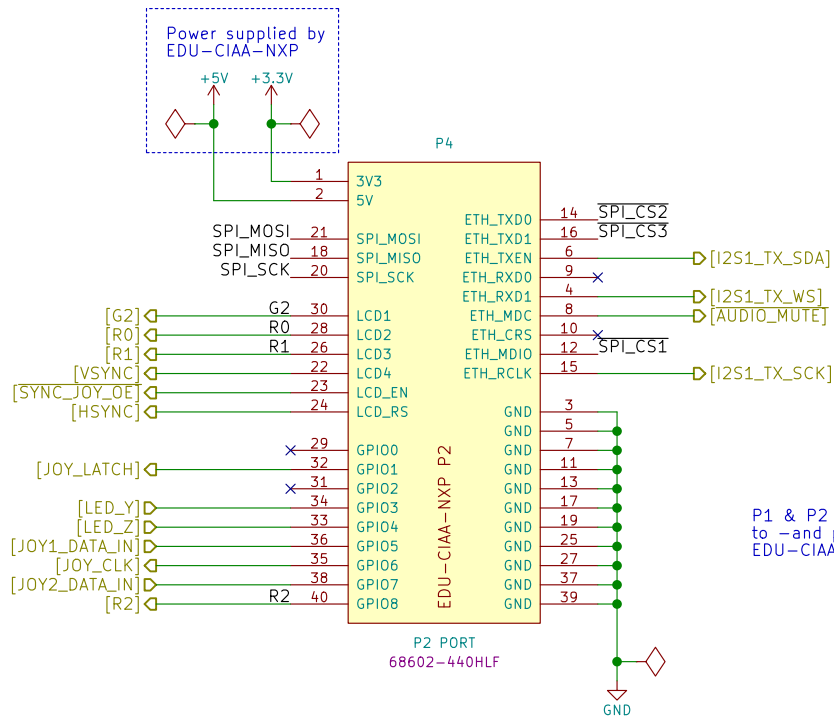
Sheet: /Wi-Fi & Bluetooth/
File: wifi.sch

Size: A4 Date: 2018-12-28

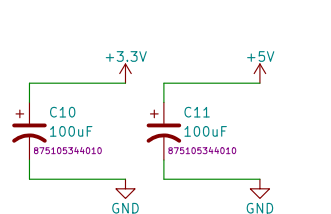
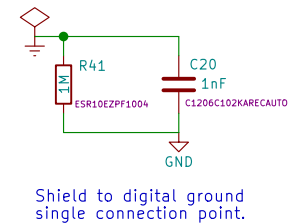
Rev: 1.2.2

KiCad E.D.A. kicad 4.0.5+dfsg1-4

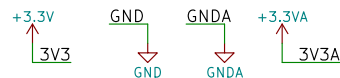
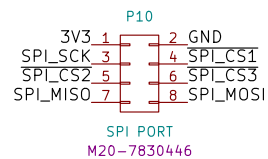
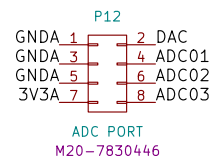
Id: 5/8



P1 & P2 ports are the I/O interface to -and power supply from- the EDU-CIAA-NXP board



Place close to 3V3 & 5V supplies on EDU-CIAA port pins



All resistors 1% & 0603" unless otherwise noted
ERC check OK on 2018-10-19
EDU-CIAA P1 & P2 are socket headers (M20-7832046 or equiv.)
Copyright 2018 Santiago Germino (see LICENCE)
<http://www.retro-ciaa.com>



RETRO-CIAA EDU-CIAA-NXP INTERFACE

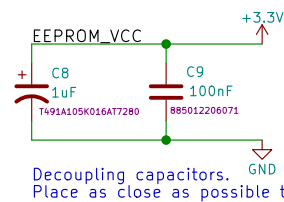
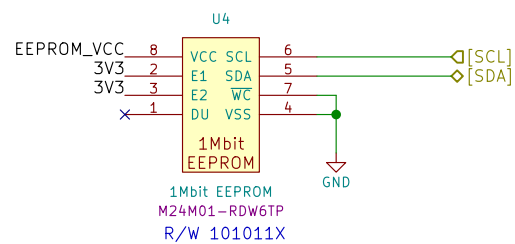
Sheet: /EDU-CIAA-NXP Interface/
File: educiaa.sch

Size: A4 Date: 2018-12-28

KiCad E.D.A. kicad 4.0.5+dfsg1-4

Rev: 1.2.2

Id: 6/8



All resistors 1% & 0603" unless otherwise noted
 ERC check OK on 2018-10-19
 EDU-CIAA P1 & P2 are socket headers (M20-7832046 or equiv.)
 Copyright 2018 Santiago Germino (see LICENCE)
<http://www.retro-ciaa.com>



RETRO-CIAA STORAGE

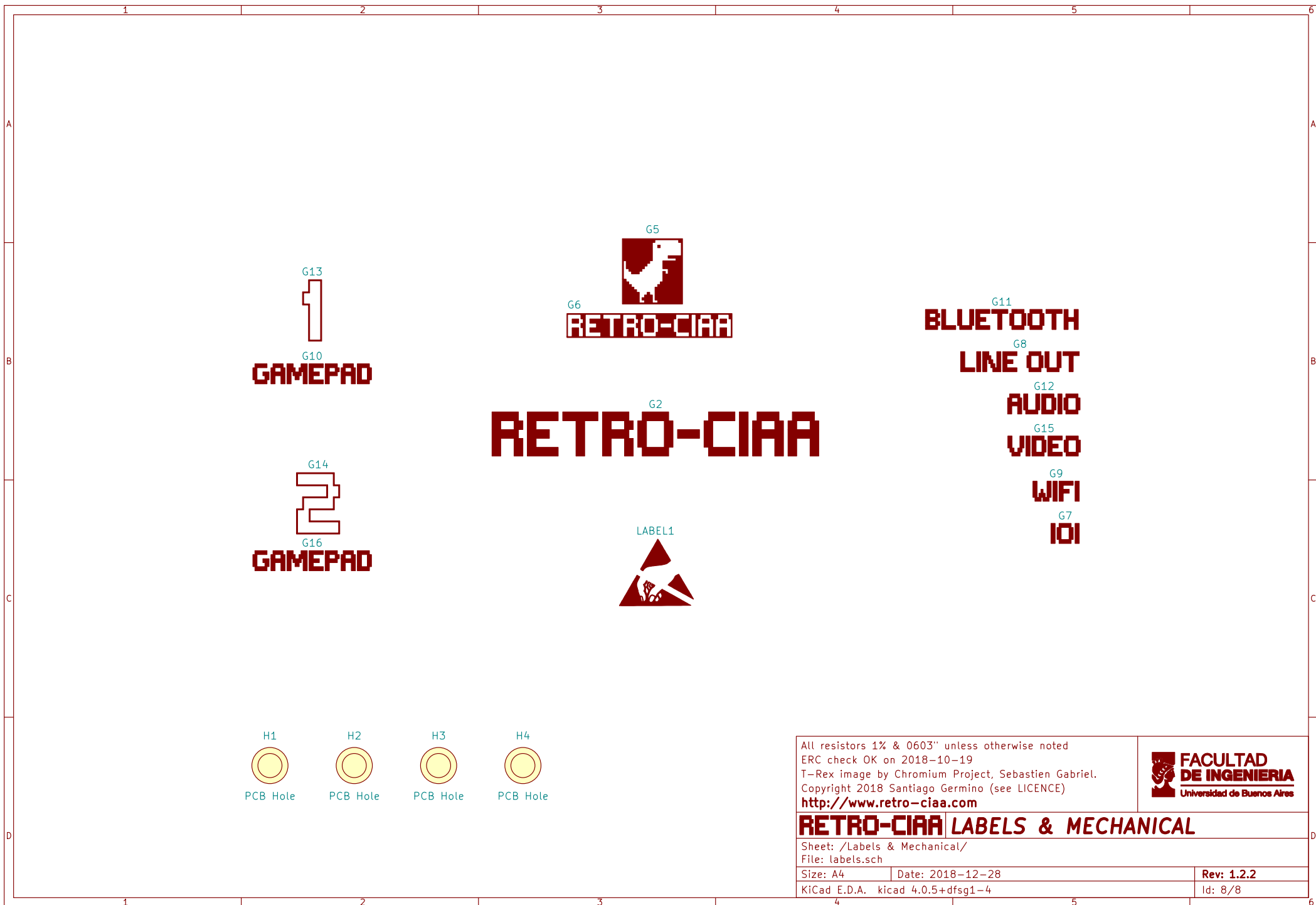
Sheet: /Storage/
 File: storage.sch


Size: A4 Date: 2018-12-28

KiCad E.D.A. kicad 4.0.5+dfsg1-4

Rev: 1.2.2

Id: 7/8



All resistors 1% & 0603" unless otherwise noted ERC check OK on 2018-10-19 T-Rex image by Chromium Project, Sebastian Gabriel. Copyright 2018 Santiago Germino (see LICENCE) http://www.retro-ciaa.com	 FACULTAD DE INGENIERIA Universidad de Buenos Aires
RETRO-CIAA LABELS & MECHANICAL	
Sheet: /Labels & Mechanical/ File: labels.sch	
Size: A4	Date: 2018-12-28
KiCad E.D.A. kicad 4.0.5+dfsg1-4	Rev: 1.2.2 Id: 8/8