Function: Electrical Fuse ESD Macro and 32bits Serial Interface Electrical Fuse
Technology: TSMC 65nm Technology
Doc. Version: 1.0

Description:

The VirtuosoLibrary folder includes:

- **Electrical Fuse ESD Macro (**folder name: **FUSES_tef65lpesd)**: ESD protection circuit to protect Electrical Fuse macro from ESD Damage. A detailed documentation can be found **here**.

- **32bits Serial Interface Electrical Fuse (**folder name: **FUSES_TEF65lp32x1s_i)**: 32bit one time programmable Electrical Fuses with Serial Interface. A detailed documentation can be found **here**.

Note to the user:

- LVS waivers: Multiple "Property errors" are reported running the LVS check. As these errors are linked to a macro directly provided by TSMC, they can be considered as negligible.

  In order for the user to distinguish between LVS errors derived from the EFUSE and LVS errors arose from the design, the "EFUSES_32bit_schematic.sp" provided here should be used as a replacement of the schematic spice netlist.

Changes list:

- 02/2019 – First release