# EDUCATION

**The University of Texas at Austin** Bachelor of Science, Electrical and Computer Engineering May 2025

Overall GPA: 3.8

**Relevant Coursework**: Computer Architecture, ML/HW Codesign, Compilers, Digital Logic Design, Embedded Systems, Software Design II, Linear Algebra, Algorithms

# WORK EXPERIENCE

**Amdocs** – *Software Engineering Intern*  May 2024 – Aug 2024

* Developed a Retrieval Augmented Generation chatbot to improve employee workflow and provide rapid responses regarding proprietary products
* Maintained a Milvus VectorDB to store embeddings of ingested documents
* Raised, set schema for, and maintained a PostgreSQL database for chat history and secure user information

# EXTRACURRICULAR ACTIVITIES

**Longhorn Racing - Solar** – *Power Generation Lead* Aug 2021 – Present

* Organizing a 14-person team to manufacture solar panels, create embedded devices for power electronics and telemetry, and write simulators for photovoltaic performance
* Communicating with members to hit intra- and inter-system deadlines while maintaining safety/efficiency standards
* Developing a new solar simulator to improve accessibility and utility for photovoltaic characterization and assembly
* Soldered and laminated solar cells into complete modules
* Characterized and validating power systems hardware for Maximum Power Point Tracking

# TEACHING EXPERIENCE

**UT Austin** – *Teaching Assistant for Software Design II* Aug 2023 – Present

* Providing Office Hours and Recitations for students, teaching concepts such as object-oriented design, inheritance, and polymorphism

# PROJECTS\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

# LHR - Solar | Array Simulator – *Python/C/QML* Sep 2022 – Present

* Implemented GUI elements of the Simulation Designer and Cell Characterization windows
* Developed serial communications between the Simulator and Curve Tracer PCB for custom testing configurations and analysis of test results

# COOL Compiler w/ LLVM – *C++* Dec 2023

* Wrote a compiler with the LLVM Framework for minimal, object-oriented language: COOL

# 5-Stage Pipelined Processor – *C* Mar 2024

* Implemented a 5-stage pipelined version of the LC-3b processor, simulated in C, capable of performing all LC3 assembly operations

# Intro to Embedded Systems Final Project – *C/C++* Apr 2022

* Designed a 2D Dungeon Crawler game in C & C++ for a microcontroller with interrupt triggered inputs and animations

**Digital Logic Design Final Project –** *Verilog*Dec 2022

* Developed a programmable 4-digit stopwatch using Verilog & RTL Design for an FPGA board

# SKILLS

**Programming:** C, C++, Java, JavaScript, Verilog, Python, QML, SQL

**Software:** Azure Suite, PostgreSQL, Milvus, Microsoft Office, Google Suite, Keil, VSCode, Mbed Studio, Vivado, Git

**Languages:** Fluent in English, Native in Hebrew

**Interests:** Rock Climbing, Piano Composition, Cooking

**Work Eligibility:** U.S. Citizen