CURRICULUM VITAE

Address : Jln. Kuningan 10 no. 6
Antapani – Bandung
40291
Phone : +62 – 896 5625 1646
022 – 7207946 (home)

Final Project :
Airplane Simulator Model using Gyroscope Concept

Name	Royyan Wibisono
Place, Date of Birth	Bandung, 7 December 1989
Email	royyanwibisono@gmail.com
Religion	Islam
Sex	Male
Height / Weight	174 cm / 80 Kg
Marital Status	single
Hobby	Basketball, Drawing, Make 3D Game and Animation
GPA	3.18

Educational Background

2008 – 2011 Polytechnic Bandung, Electronic Engineering

2005 – 2008 Senior High School 14 Bandung

2002 – 2005 Junior High School 14 Bandung

1996 – 2002 GRIBA 27/I Elementary School

Qualifications

Electronic Field	• Capable using gauges (oscilloscope, multi-meter) • Capable to design and build electronic circuit • Good in microcontroller programming • Good in maintenance and repair • Understand Arduino's module
Programming Field	• Capable programming in Notepad++, Atom, Sublime. • Understand Object Oriented Programming (OOP) • Experienced using this programming language : 1. C# (advanced) 2. VB.NET (visual basic 6) 3. Python 4. JAVA 5. C (microcontroller programming) 6. PHP 7. Assembly • Understand XML, JSON, .NET, OpenGL binding via OpenTK library, MySQL, SQL Server, HTML, CSS, Linq, ASP MVC 5, Javascript, Bootstrap, AngularJS, Google Polymer, JQuery, Laravel 5, WordPress, Android's native application development, QR-Code using ZXing library for Android, Web Services using PHP, Vagrant / Homestead, Git Command. • Understand 2D translation, rotation, scaling geometry object.
Operating System	WINDOWS, ANDROID, LINUX UBUNTU
Software / Tools	Visual Studio 2008/2010/2015, Sharp Develop, NotePad++, Atom, Sublime, PCVue, MS Office, Visio Drawing, Electronic Work Bench , Adobe Photoshop, GIMP, Altium Designer, Blender 3D, Android Studio, Eclipse, Git,

CodeVisionAVR, CodeBlocks, AVR Studio, Python 2.7/3.2, Marvelous Designer, VirtualBox, Google Chrome, Mozilla Firefox.

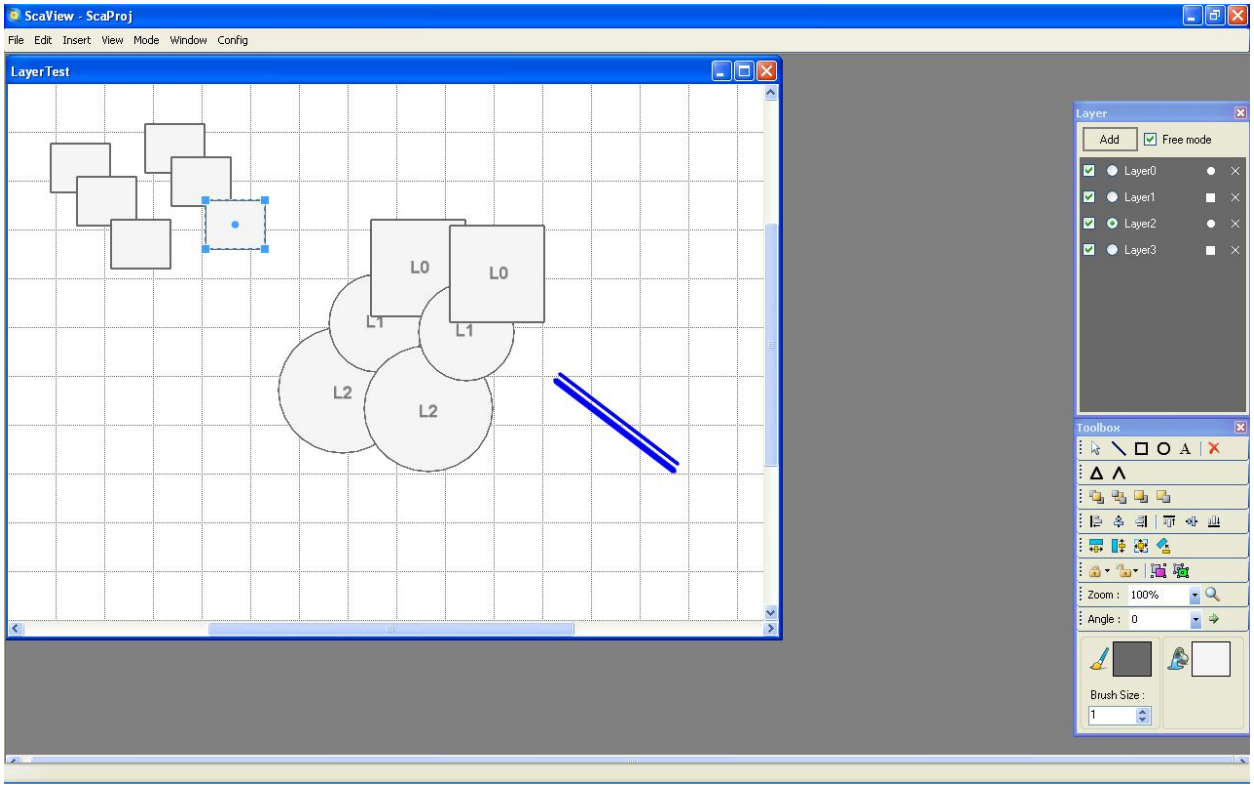
Working Experience

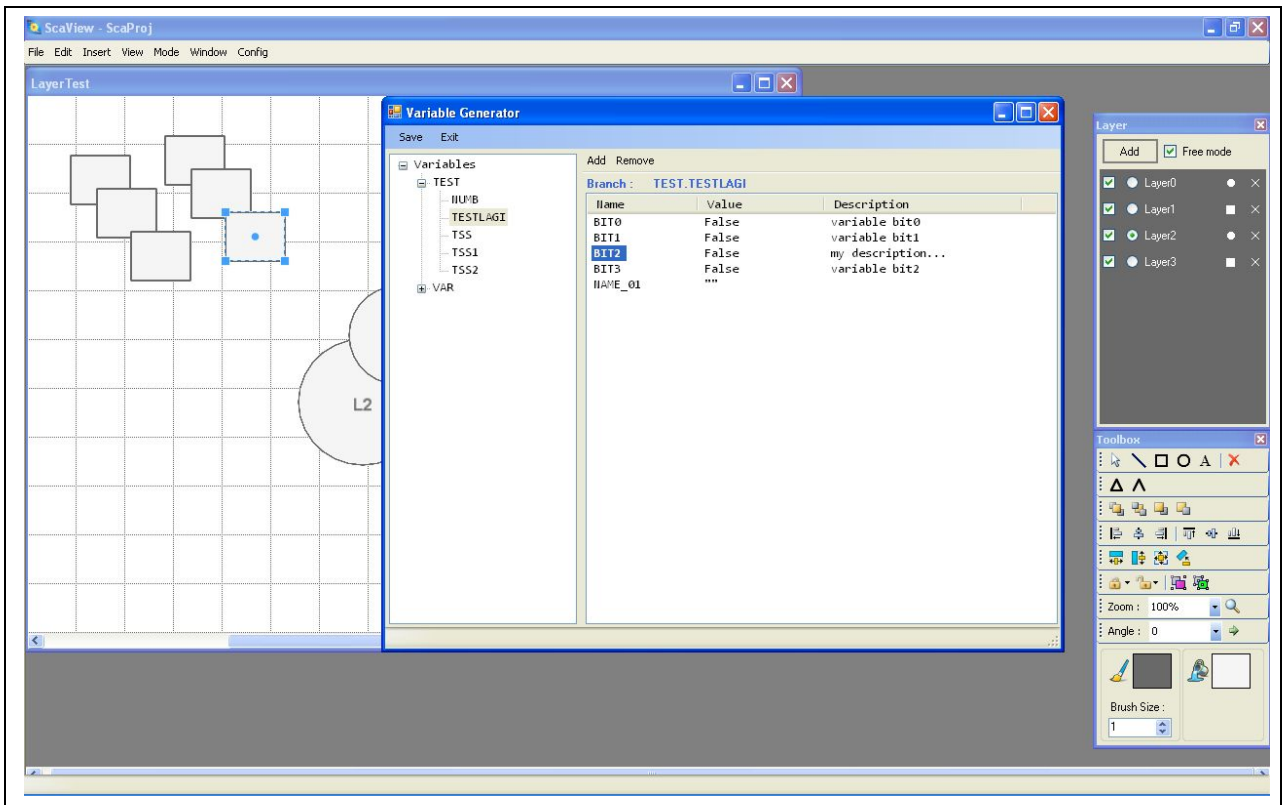
Nov 2014 – Now	TRANSPORT SYSTEM SOLUTIONS INDONESIA, PT • Develop software and tools for Simulation, HMI Design, and Develop SCADA software. Web Development (Freelance).
Oct 2011- Oct 2014	ALKMAAR ASIA PACIFIC, PT Develop software and tools for Simulation, and HMI Design.
19 Jul-3 Sept 2010	PT Dirgantara Indonesia (Praktek Kerja Lapangan) : AVIONICS

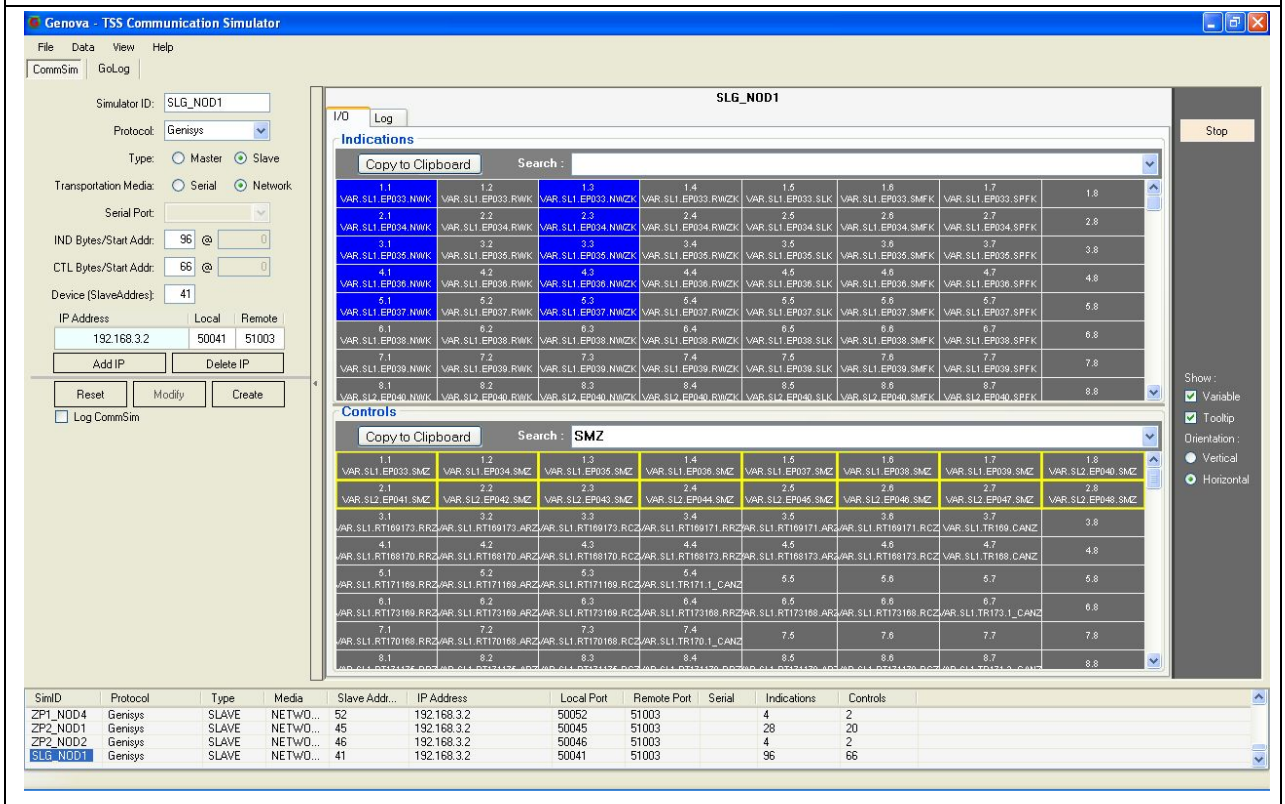
Bandung, November 24, 2015

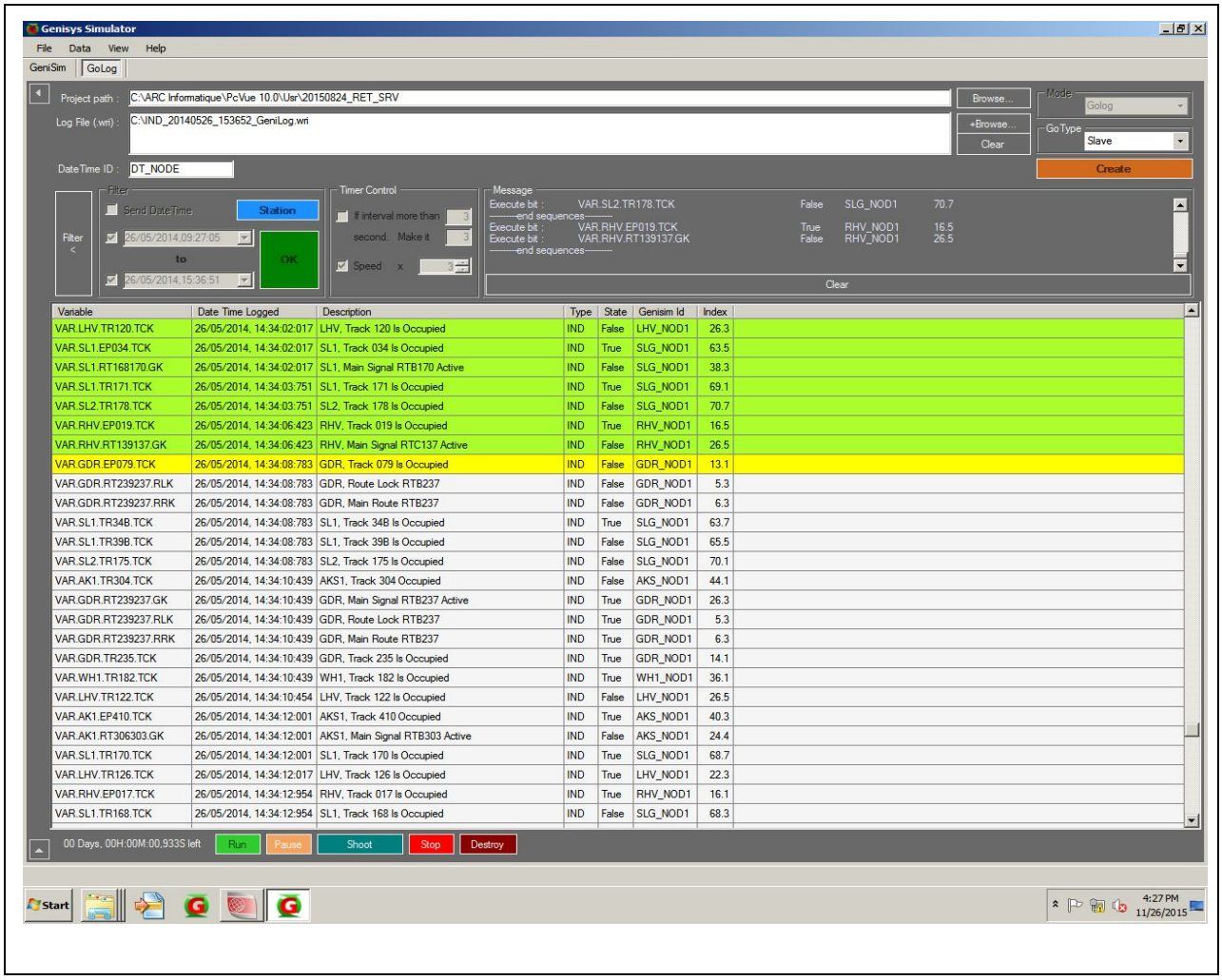
Royyan Wibisono

These are screen-shots of software/tools I have develop :

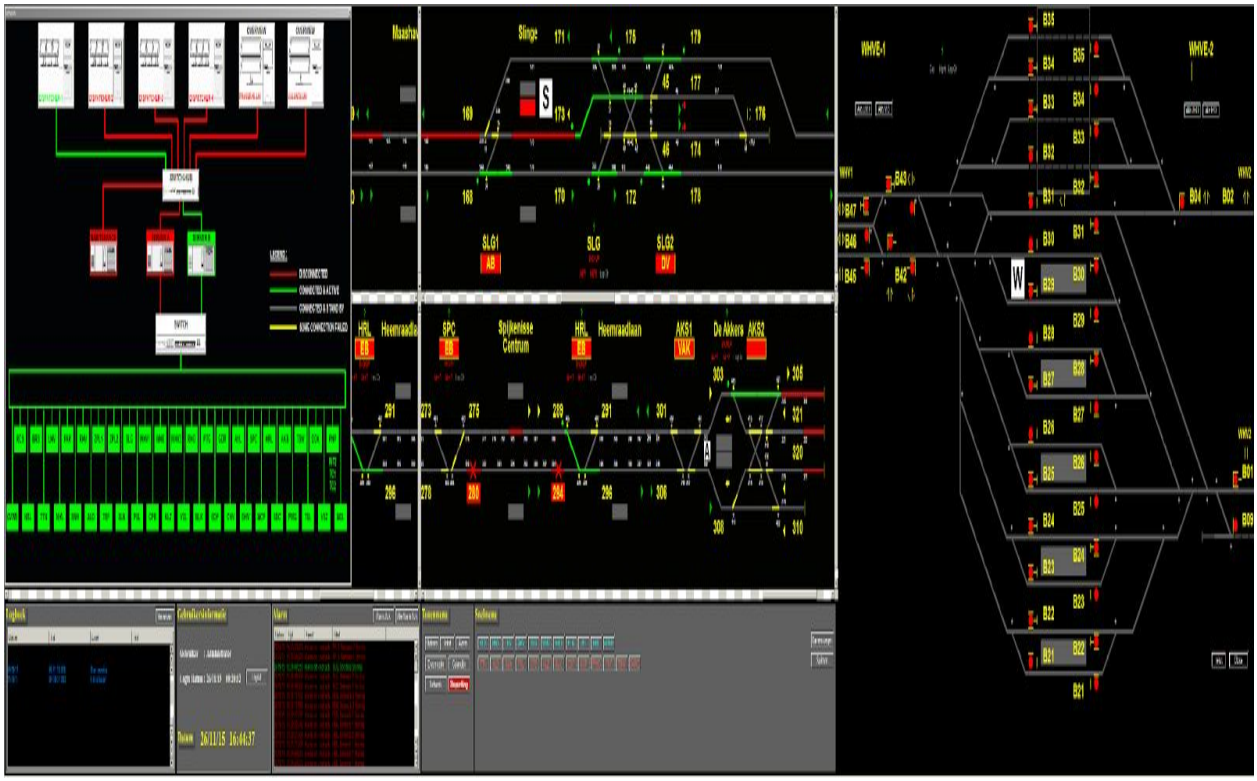
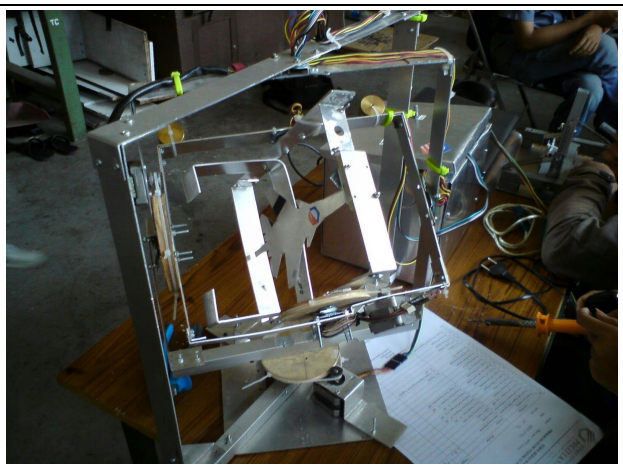
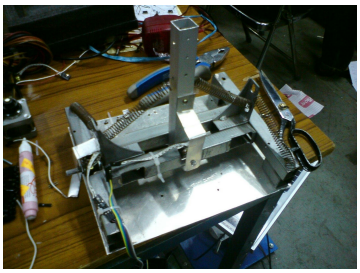
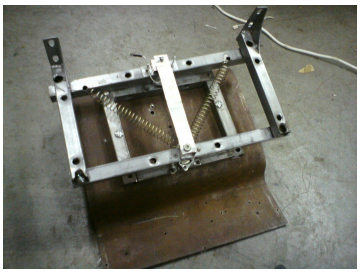
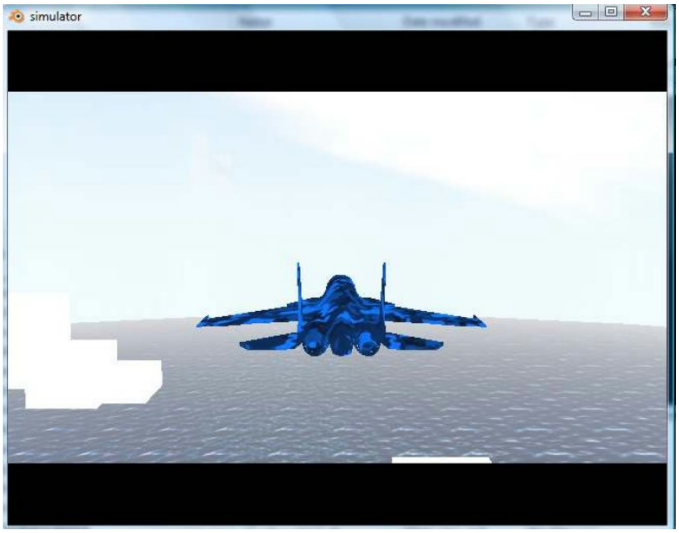



ScaView - ScaProj
File Edit Insert View Mode Window Config
LayerTest
L2
Variable Generator
Save Exit
Variables
TEST
NUMB
TESTLAGI
TSS
TSS1
TSS2
VAR
Add Remove
Branch : TEST.TESTLAGI
Name Value Description
BIT0 False variable bit0
BIT1 False variable bit1
BIT2 False my description...
BIT3 False variable bit2
NAME_01 ""
Layer
Add Free mode
Layer0
Layer1
Layer2
Layer3
Toolbox
Zoom : 100%
Angle : 0
Brush Size : 1


Genova - TSS Communication Simulator
File Data View Help
CommSim GoLog
Simulator ID: SLG_NOD1
Protocol: Genisys
Type: Master Slave
Transportation Media: Serial Network
Serial Port:
IND Bytes/Start Addr: 96 @ 0
CTL Bytes/Start Addr: 66 @ 0
Device (SlaveAddres): 41
IP Address Local Remote
192.168.3.2 50041 51003
Add IP Delete IP
Reset Modify Create
Log CommSim
SLG_NOD1
I/O Log
Indications
Copy to Clipboard Search :
Controls
Copy to Clipboard Search : SMZ
Stop
Show :
Variable
Tooltip
Orientation :
Vertical
Horizontal
SimID Protocol Type Media Slave Addr... IP Address Local Port Remote Port Serial Indications Controls
ZP1_NOD4 Genisys SLAVE NETWO... 52 192.168.3.2 50052 51003 4 2
ZP2_NOD1 Genisys SLAVE NETWO... 45 192.168.3.2 50045 51003 28 20
ZP2_NOD2 Genisys SLAVE NETWO... 46 192.168.3.2 50046 51003 4 2
SLG_NOD1 Genisys SLAVE NETWO... 41 192.168.3.2 50041 51003 96 66


Genisys Simulator
File Data View Help
GeniSim GoLog
Project path : C:\ARC Informatique\PcVue 10.0\Usr\20150824_RET_SRV
Log File (.wri) : C:\IND_20140526_153652_GeniLog.wri
Browse...
+Browse...
Clear
Mode
Gotog
GoType
Slave
DateTime ID : DT_NODE
Create
Filter
Filter <
Send DateTime
Station
26/05/2014.09:27:05
to
26/05/2014.15:36:51
OK
Timer Control
If interval more than 3
second. Make it 3
Speed x 3
Message
Execute bit : VAR.SL2.TR178.TCK False SLG_NOD1 70.7
end sequences
Execute bit : VAR.RHV.EP019.TCK True RHV_NOD1 16.5
Execute bit : VAR.RHV.RT139137.GK False RHV_NOD1 26.5
end sequences
Clear
Variable | Date Time Logged | Description | Type | State | Genisim Id | Index
VAR.LHV.TR120.TCK | 26/05/2014, 14:34:02:017 | LHV, Track 120 Is Occupied | IND | False | LHV_NOD1 | 26.3
VAR.SL1.EP034.TCK | 26/05/2014, 14:34:02:017 | SL1, Track 034 Is Occupied | IND | True | SLG_NOD1 | 63.5
VAR.SL1.RT168170.GK | 26/05/2014, 14:34:02:017 | SL1, Main Signal RTB170 Active | IND | False | SLG_NOD1 | 38.3
VAR.SL1.TR171.TCK | 26/05/2014, 14:34:03:751 | SL1, Track 171 Is Occupied | IND | True | SLG_NOD1 | 69.1
VAR.SL2.TR178.TCK | 26/05/2014, 14:34:03:751 | SL2, Track 178 Is Occupied | IND | False | SLG_NOD1 | 70.7
VAR.RHV.EP019.TCK | 26/05/2014, 14:34:06:423 | RHV, Track 019 Is Occupied | IND | True | RHV_NOD1 | 16.5
VAR.RHV.RT139137.GK | 26/05/2014, 14:34:06:423 | RHV, Main Signal RTC137 Active | IND | False | RHV_NOD1 | 26.5
VAR.GDR.EP079.TCK | 26/05/2014, 14:34:08:783 | GDR, Track 079 Is Occupied | IND | False | GDR_NOD1 | 13.1
VAR.GDR.RT239237.RLK | 26/05/2014, 14:34:08:783 | GDR, Route Lock RTB237 | IND | False | GDR_NOD1 | 5.3
VAR.GDR.RT239237.RRK | 26/05/2014, 14:34:08:783 | GDR, Main Route RTB237 | IND | False | GDR_NOD1 | 6.3
VAR.SL1.TR34B.TCK | 26/05/2014, 14:34:08:783 | SL1, Track 34B Is Occupied | IND | True | SLG_NOD1 | 63.7
VAR.SL1.TR39B.TCK | 26/05/2014, 14:34:08:783 | SL1, Track 39B Is Occupied | IND | False | SLG_NOD1 | 65.5
VAR.SL2.TR175.TCK | 26/05/2014, 14:34:08:783 | SL2, Track 175 Is Occupied | IND | False | SLG_NOD1 | 70.1
VAR.AK1.TR304.TCK | 26/05/2014, 14:34:10:439 | AKS1, Track 304 Occupied | IND | False | AKS_NOD1 | 44.1
VAR.GDR.RT239237.GK | 26/05/2014, 14:34:10:439 | GDR, Main Signal RTB237 Active | IND | True | GDR_NOD1 | 26.3
VAR.GDR.RT239237.RLK | 26/05/2014, 14:34:10:439 | GDR, Route Lock RTB237 | IND | True | GDR_NOD1 | 5.3
VAR.GDR.RT239237.RRK | 26/05/2014, 14:34:10:439 | GDR, Main Route RTB237 | IND | True | GDR_NOD1 | 6.3
VAR.GDR.TR235.TCK | 26/05/2014, 14:34:10:439 | GDR, Track 235 Is Occupied | IND | True | GDR_NOD1 | 14.1
VAR.WH1.TR182.TCK | 26/05/2014, 14:34:10:439 | WH1, Track 182 Is Occupied | IND | True | WH1_NOD1 | 36.1
VAR.LHV.TR122.TCK | 26/05/2014, 14:34:10:454 | LHV, Track 122 Is Occupied | IND | False | LHV_NOD1 | 26.5
VAR.AK1.EP410.TCK | 26/05/2014, 14:34:12:001 | AKS1, Track 410 Occupied | IND | True | AKS_NOD1 | 40.3
VAR.AK1.RT306303.GK | 26/05/2014, 14:34:12:001 | AKS1, Main Signal RTB303 Active | IND | False | AKS_NOD1 | 24.4
VAR.SL1.TR170.TCK | 26/05/2014, 14:34:12:001 | SL1, Track 170 Is Occupied | IND | True | SLG_NOD1 | 68.7
VAR.LHV.TR126.TCK | 26/05/2014, 14:34:12:017 | LHV, Track 126 Is Occupied | IND | True | LHV_NOD1 | 22.3
VAR.RHV.EP017.TCK | 26/05/2014, 14:34:12:954 | RHV, Track 017 Is Occupied | IND | True | RHV_NOD1 | 16.1
VAR.SL1.TR168.TCK | 26/05/2014, 14:34:12:954 | SL1, Track 168 Is Occupied | IND | False | SLG_NOD1 | 68.3
00 Days, 00H:00M:00,933S left
Run
Pause
Shoot
Stop
Destroy
Start
4:27 PM
11/26/2015

RET SCADA interface using PCVue :



My Final Project when I graduated from POLBAN: