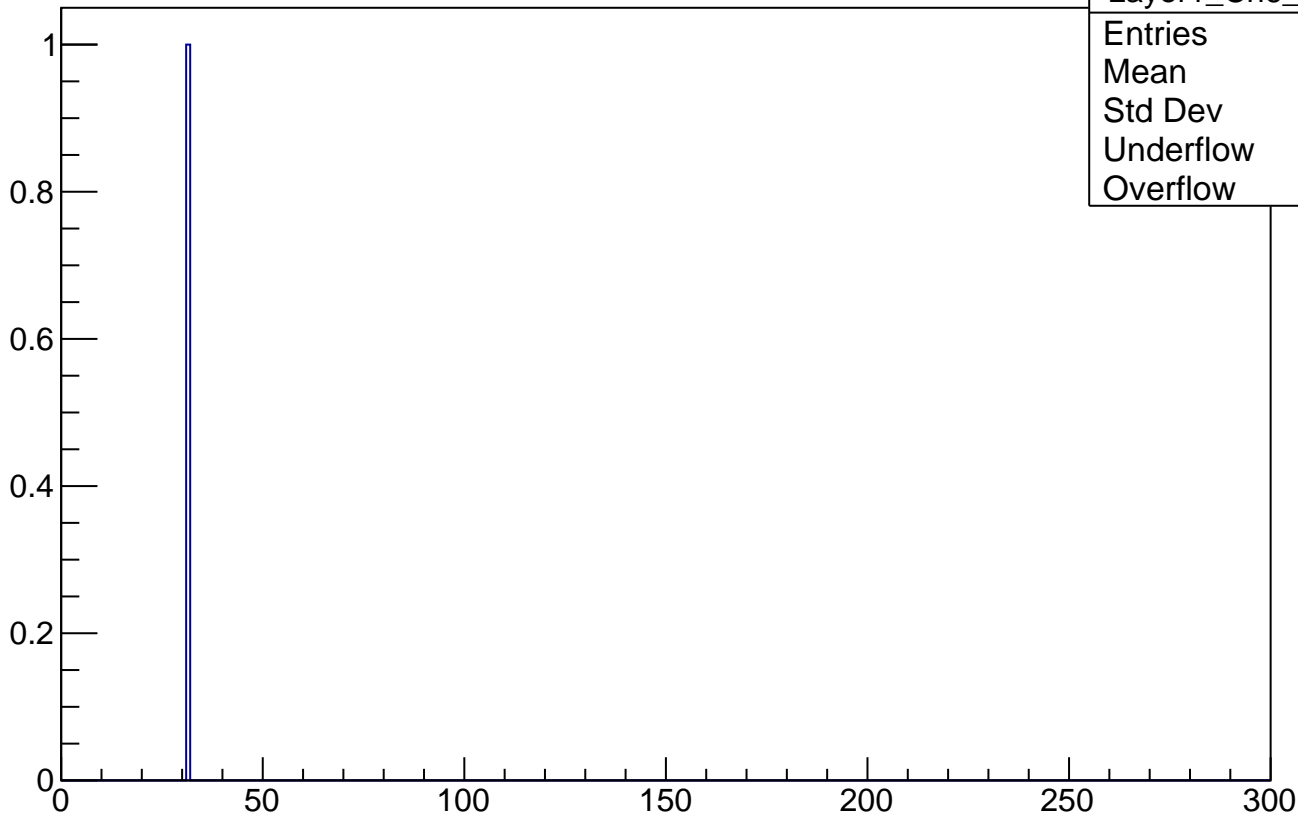


Layer 1 Ch 6 ADC[0]



Layer1_Ch6_adc0	
Entries	1
Mean	31
Std Dev	0
Underflow	0
Overflow	0