How to use LTSpice

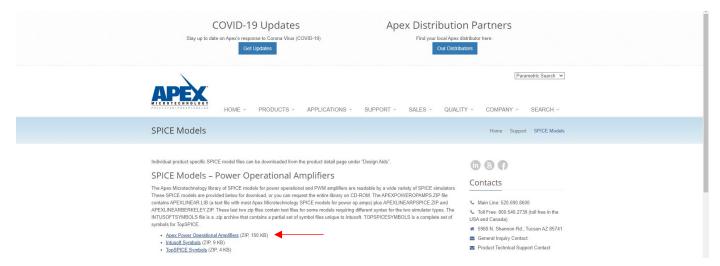
October 13th, 2020

Contents

How to insert third-party components	3
How to run square wave simulation on the circuit (Step response)	6
How to run frequency response simulation on the circuit	9

How to insert third-party components

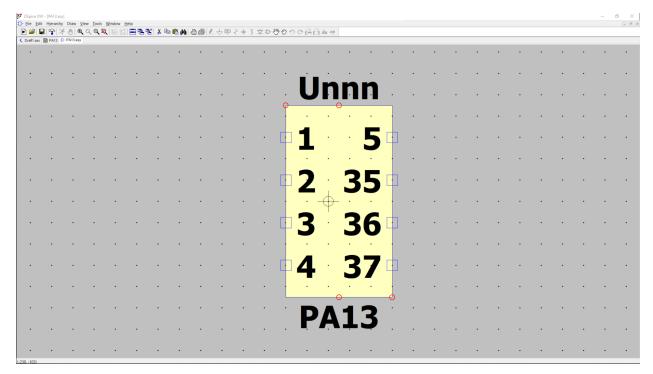
- 1) Go to Apex analog website to download PA13 op-amp model spice model (Link).
- 2) Select Apex Power Operational Amplifiers zip file as shown in the picture below.



- 3) Unzip apexpoweropamps.zip file at any file location of your choice and look for **PA13.lib** file inside the apexpoweropamps unzipped folder.
- 4) Copy and paste PA13.lib file in the same file directory as your LTSpice saved file (.asc type).
- 5) Open **PA13.lib** file in your LTSpice by drag and drop the file or go to File -> Open. After inserted the file, you should get the same code as below on your LTSpice.

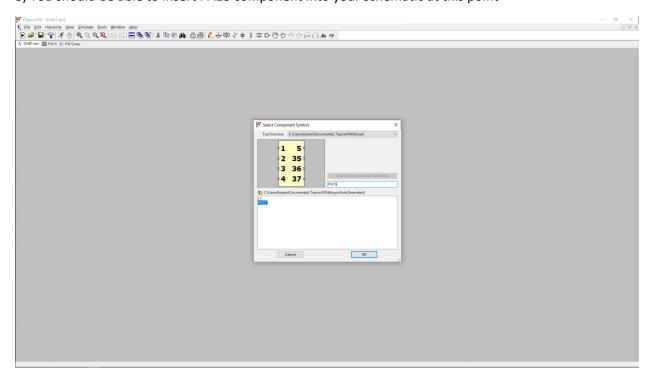
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- 6) Right click on the blue letter in the code that start with .SUBCKT then select Create symbol.
- 7) LTSpice should be able to autogenerate PA13 component for you.

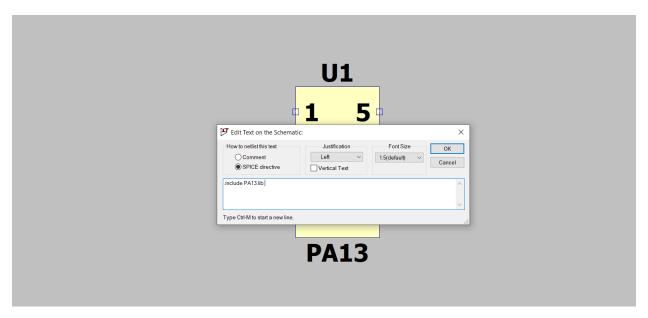


Note: To find out what these pins number represent. Go back to PA13.lib file and look at the top section of the file.

8) You should be able to insert PA13 component into your schematic at this point



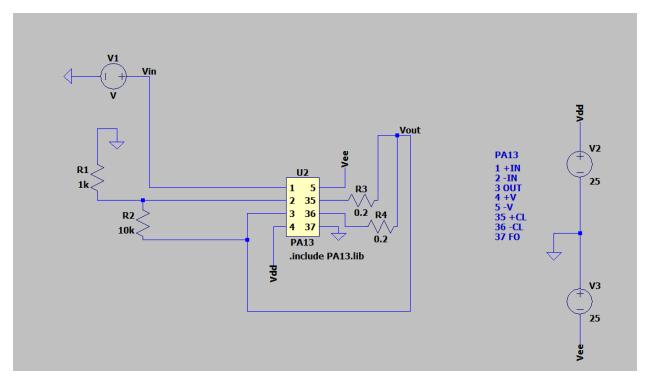
9) Insert PA13 op-amp simulation file into LTSpice by go to Edit -> Spice Directive. Then type .include PA13.lib



Note: Make sure that you have PA13.lib file in the same folder as your LTSpice file.

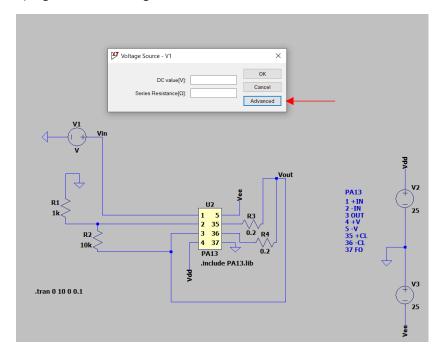
How to run square wave simulation on the circuit (Step response)

1) Assemble a circuit on LTSpice as shown in the picture below

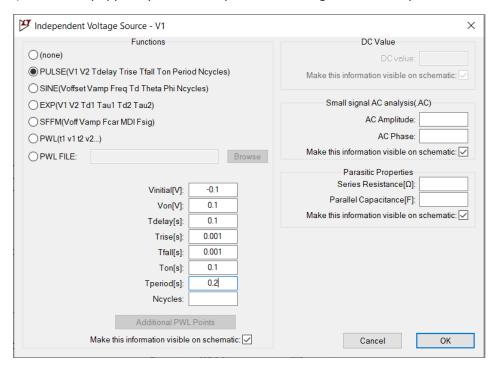


Note: The PA13 table on the schematic is for pin labeling only. It's optional to the circuit

2) Right click on voltage source 1 and select Advanced.



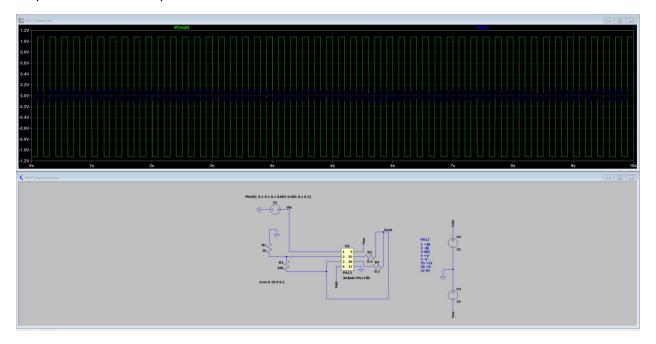
3) In the new popped up window, input the following values in the picture below for 10Hz square wave.



4) Run simulation by go to Simulate -> Run and input simulation detail as shown below

	linear, time-domain si Stop ti	
	Stop ti	- 10
		me: 10
	Time to start saving d	ata: 0
	Maximum Times	tep: 0.1
Start external	DC supply voltages at	t0V:
Stop simulating i	f steady state is detec	ted:
Don't reset T=0 when	steady state is detec	ted:
Ste	p the load current sou	rce:
Skip initia	al operating point solu	tion:
ntax: .tran <tprint> <tstop> [<tstart< td=""><td>> [<tmaxstep>]] [<opt< td=""><td>ion> [<option>]]</option></td></opt<></tmaxstep></td></tstart<></tstop></tprint>	> [<tmaxstep>]] [<opt< td=""><td>ion> [<option>]]</option></td></opt<></tmaxstep>	ion> [<option>]]</option>
n 0 10 0 0.1		
Cancel		

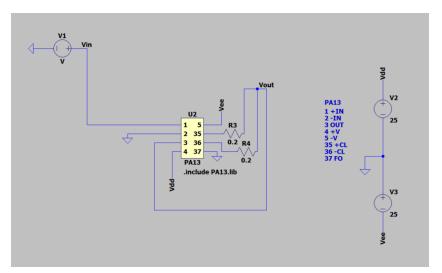
5) Put a probe symbol cursor on both Vin and Vout to read the simulated output. If done correctly, your LTSpice should look like picture shown below.



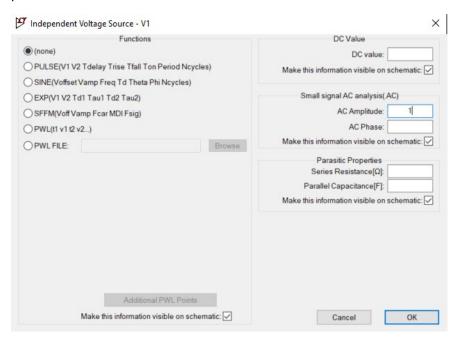
6) Zoom into the rising edge of the waveform and take the reading according to the lab manual

How to run frequency response simulation on the circuit (Bode plot)

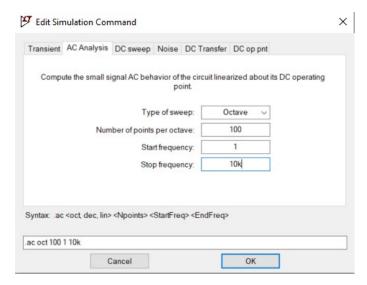
1) Create a schematic as shown in the picture below.



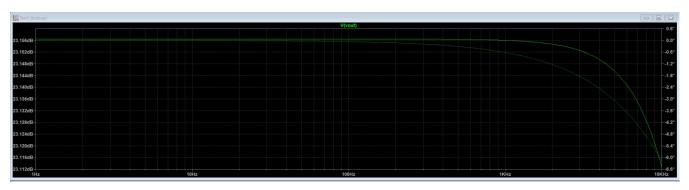
2) Right click on voltage source and select advanced setting. Set AC amplitude to 1 as shown in the picture below.



3) Go to Simulate -> Run -> AC Analysis and input the following setting.



4) Press run and put a probe cursor on the Vout or output of the Op-amp. You should get the following frequency response.



Note: The solid green line is the gain and the dotted line is the phase.