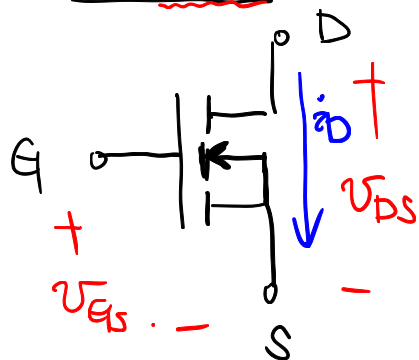


L23 – Power Electronics: Switching Amplifier

◦ MOSFET



- Gate is AC coupled : $\bar{i}_G = 0$
- Arrow : p-type body \rightarrow n-channel.
- Body-source short : "Body diode"
- Dot - enhancement mode (normally off)

Terminal vars

- Voltages : V_{GS} , V_{DS}
- Current : i_G , i_D , i_S ($i_G = 0$ at DC $\Rightarrow i_D = i_S$)

Terminal Relations

① Cutoff . ($V_{GS} < V_{th}$) .

$$i_D = 0$$

$$\underline{V_{DS} > V_{GS} - V_{th} > 0}$$

② Saturation . ($V_{GS} > V_{th}$, $V_{DS} > V_{GS} - V_{th}$) .

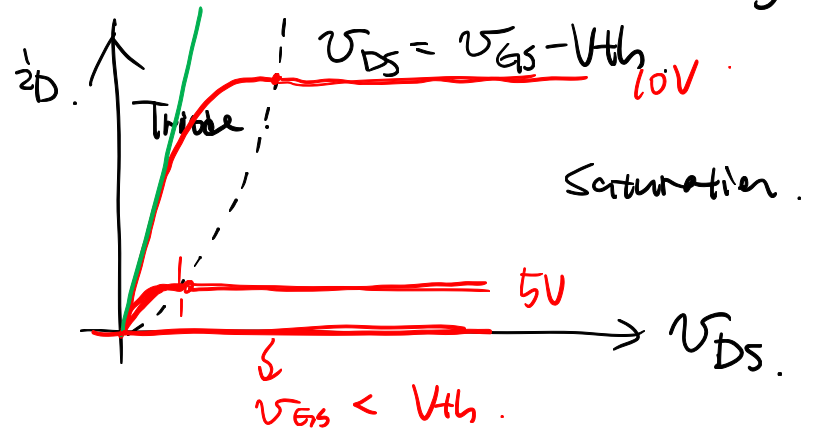
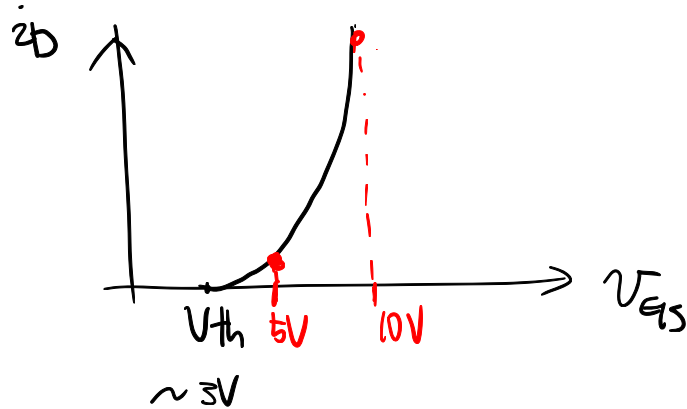
$$i_D = \frac{1}{2} K (V_{GS} - V_{th})^2 \rightarrow g_m \stackrel{\circ}{=} \frac{d i_D}{d V_{GS}} = K (V_{GS} - V_{th})$$

VCCS

i_D insensitive to v_{DS} .

→ BJT in

"Active region"



< output >

< Input >.

③ Triode. ($v_{GS} > V_{th}$, $v_{DS} < v_{GS} - V_{th}$)

$$i_D = K \left\{ (v_{GS} - V_{th}) v_{DS} - \frac{1}{2} v_{DS}^2 \right\}$$

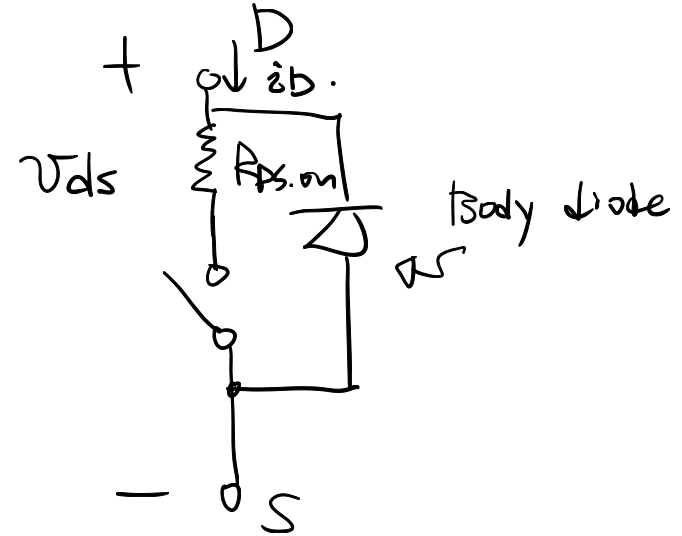
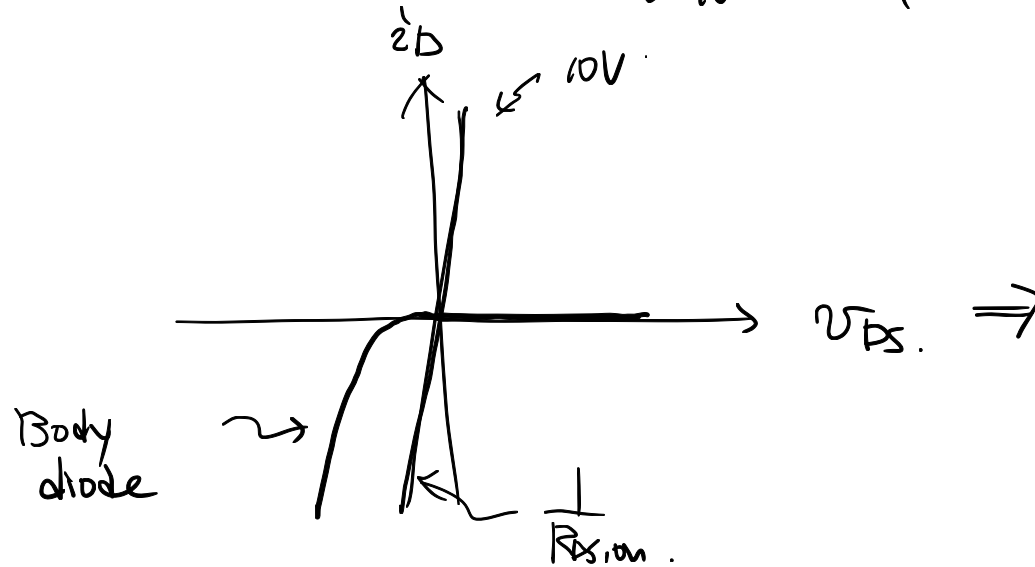
$$\approx K (v_{GS} - V_{th}) v_{DS}$$

$$\cong \frac{1}{R_{DS, on}}$$

$$R_{DS, on} = \frac{1}{K (v_{GS} - V_{th})}$$

o MOSFET as a power switch.

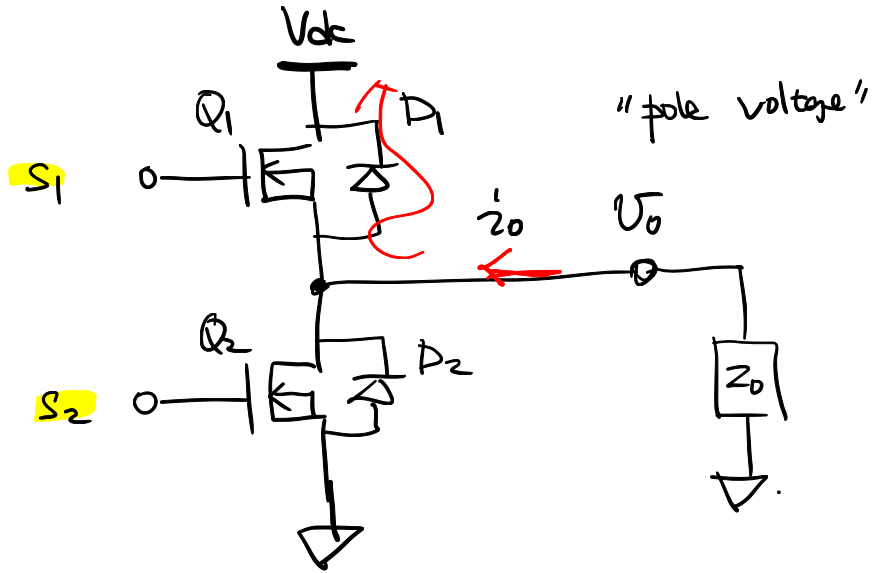
Suppose $V_{GS} = \begin{cases} 0 & (\text{switch off}) \\ 10\text{ V} & (\text{switch on}) \end{cases}$



. Block +V.

. Conduct $\pm i$

◦ Half-bridge stage (Totem pole circuit).



• Unipolar / two-quadrant.
(+ v_0). ($\pm i_0$).

- State table.

k	s_1	s_2	v_0	Conduction.
0	0	0	0 / V_{dc}	D_2 ($i_0 > 0$) / D_1 ($i_0 < 0$).
1	1	0	V_{dc}	<u>Q_1</u>
2	0	1	0	Q_2 .
3.	<u>1</u>	<u>1</u>	X	<u>Shoot through.</u>

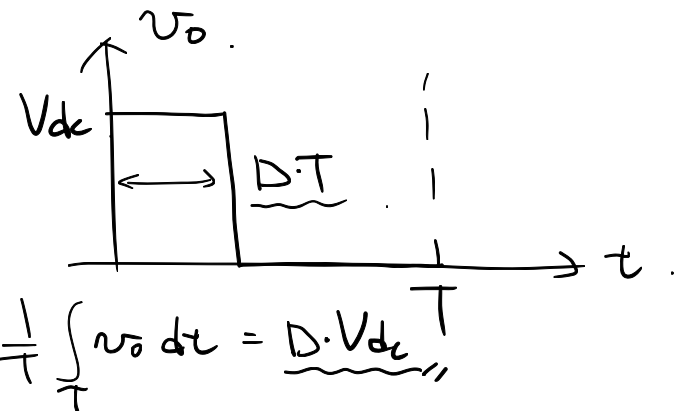
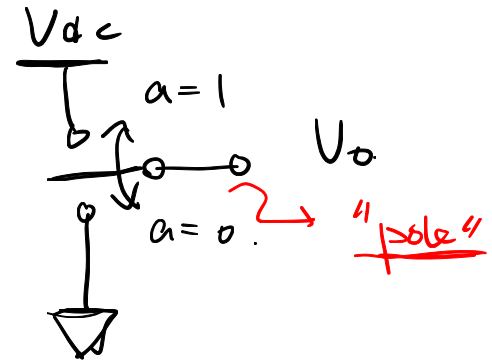
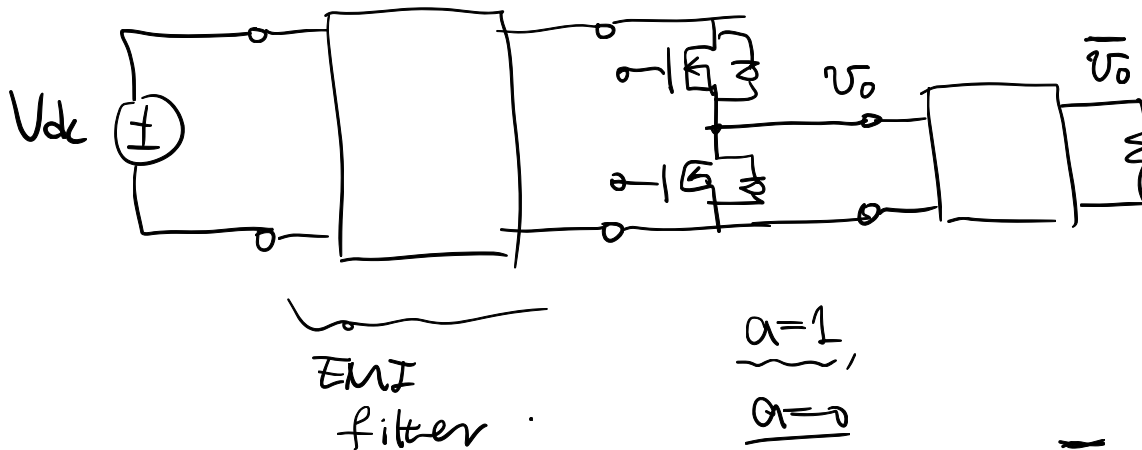
◦ $k=1 \leftrightarrow k=2$. "Complementary Switching"

- Never use $k=3$ "Shoot through"
- Insert $k=0$ between $k=1 \leftrightarrow k=2$. "Dead-time"

• State var.

"a" $a = S_1 = \overline{S_2} \Rightarrow \begin{cases} a=0 : v_o = 0 \\ a=1 : v_o = V_{dc} \end{cases}$

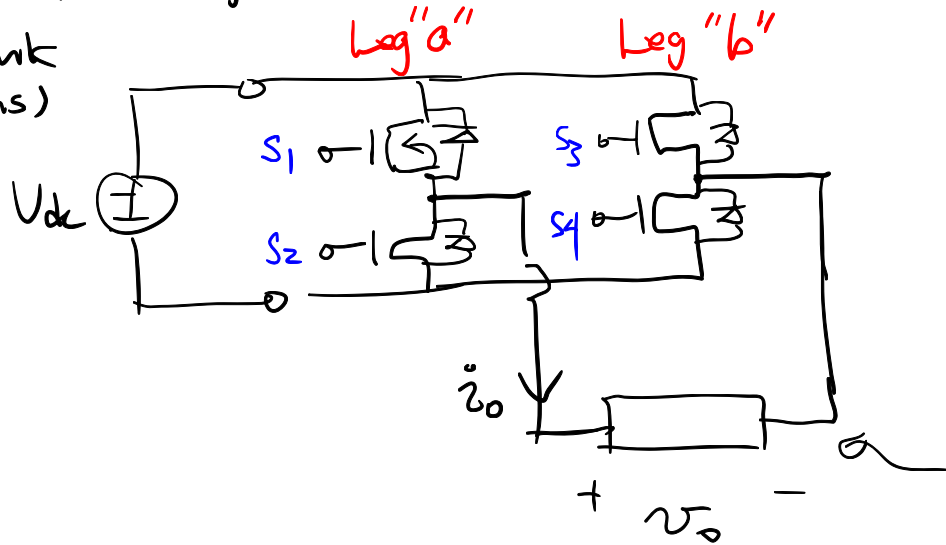
• Sync. Buck Conv.



$$\overline{v} = \frac{1}{T} \int_T v_o dt = \underline{D \cdot V_{dc}}$$

◦ H-bridge (dc / ac).

DC link
(Bus)



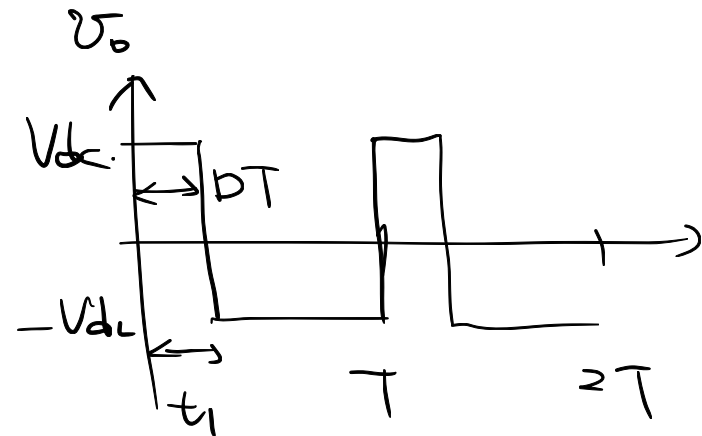
"Four quad"

$\pm v_o, \pm i_o$.

"Bridge"

◦ State table.

n	a	b	v_o
0	0	0	0
1	1	0	V_{dc}
2	0	1	$-V_{dc}$
3	1	1	0



◦ Duty Ratio Control. $D \equiv \frac{t_1}{T}$.

$$\overline{v_o} = \frac{1}{T} V_{dc} (DT + (D-1)T) = \underline{(2D-1) V_{dc}}.$$

$$D = 0.6 \rightarrow \underline{\hat{v}_0 = 0}$$