MZ	ECH 423	Lecture	10	
ADC			Typical	1/5
-106	if ADC		V <sub>R</sub> .	= OV
N =	1023.	Vin - VR-	$V_{Rt} =$	3,3V
		VR+ - VR-	)	1.5V
	ADC corver		not	
	Instartazeou			
Key bits:	ADC 1 Ø B		Enable Co	2 verysta
	ADC 105		start con	
	ADC 1\$ 7			
> Wait	for conven			
Method 1:	while (ADC	1PCTL1	el ADC1	LOBUSY);
Motod 2:	While (CAD			
Method 7:	- Set up in		경영 등을 내용하고 있는데 경우의 경우를 받는	
Retrieve rei	ut: Res	wlt = AD	C LØME MØ	8
If 8 bit	result is d	estred:		
			LØ MAM Ø	>> 2
		right s	shift 2 places_	
Variables		Unsigned	Si	gned -128 12
8 bit			-12f	10000000
ODIT	71,001	0255	0 0	00000000

16 bit - int 0 0000 0000 127 0111111 255 1111 1111 \* Always declared unsigned explicitly Unsigned int X=0; Global Variables - Variables can fail to rydete across subvortines because int global\_van; of compilier optimization. Int mask (void) - Define an "volatile" - turn off compilier optimization int my ZSR() Volatile unsigned int global van=0; MCU Programming Structures 1. Polling — constantly checking E while (1) ETF (PLIN & BIT3)

3

1 do Something - Can still wiss events. 2. Interrupt - chiver Programing int main () E ble local Extensels

	while	(1);	Enable 9	lobal interru	pts
#pr	ngma. UIS	R - r	nost of you	n code.	
Problem	: Whil	e ISK is	executing, M	to other in	tempts
For exi	ingle, 1 bi	UAKT at $t = 833$	dock cycle	id s (clock	
Evert-		te 2 840	o clock cyde	J	
#	proguno 11 I	SR =	- Keep Minin	al act bate	From
	11 Se	e.g. for U.	AKI Kik —	_ get byte Rx bu	Her
Îns		-up registe			
	while	ble interroy (1) Tf (fl	ag == True)		1-0
		&// Ev	est handler ag = 0 j		tempted.
	3		2 = True)		
	U U				

## Lab #2 Exam 5 Exercises - Yes/No octobre Total time: 2.5 hours Open Conjuter. Completion Grade Time Grade 20% 20% 40% 60% 80% 10% 90% 10% D Be organized 1 Don't Paris