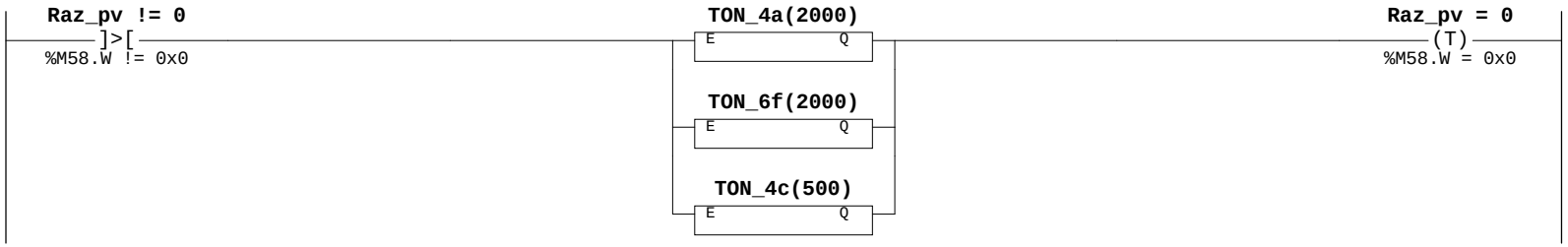
00 Label: Step: **Raz_pv** %M58.W **= 0**



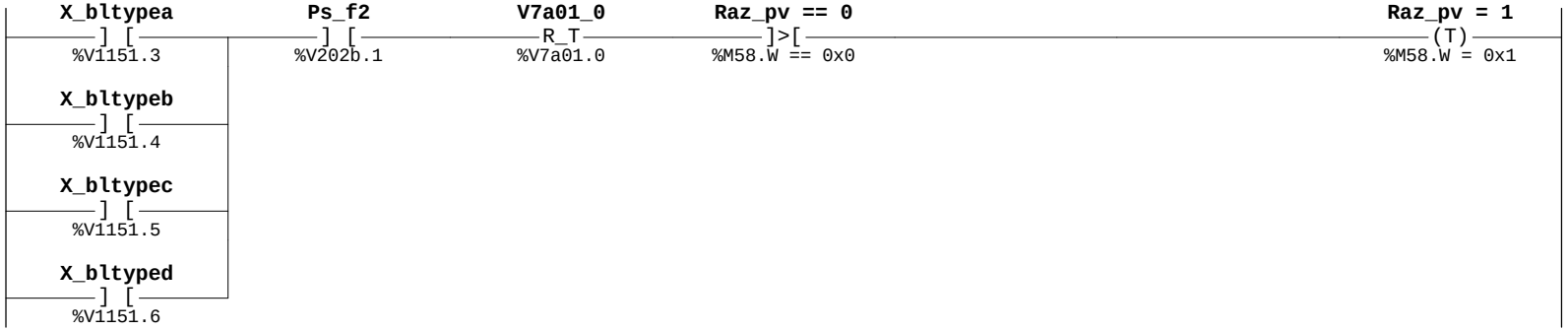
[T] TON_4a(0x7d0) : TON_4a(2000)
[T] TON_6f(0x7d0) : TON_6f(2000)
[T] TON_4c(0x1f4) : TON_4c(500)

01 Label: Step: **Raz_pv** %M58.W **= 0**

Reset da emergenza HW

02 Label: Step: **Raz_pv** %M58.W **= 0**

Emergenza icla



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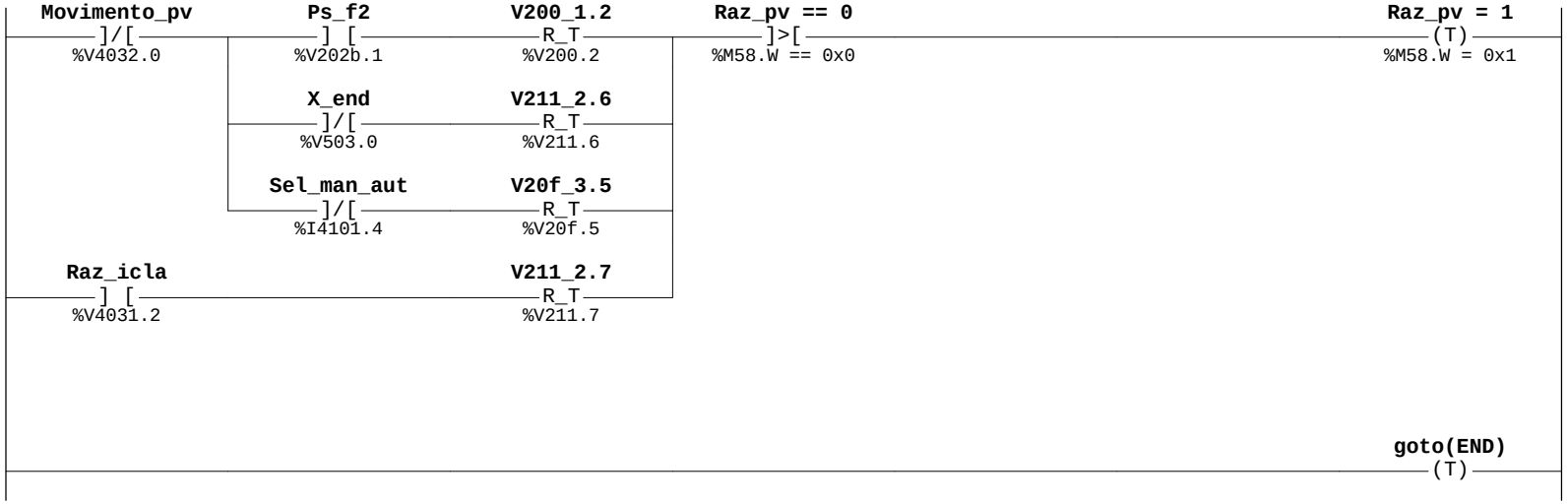
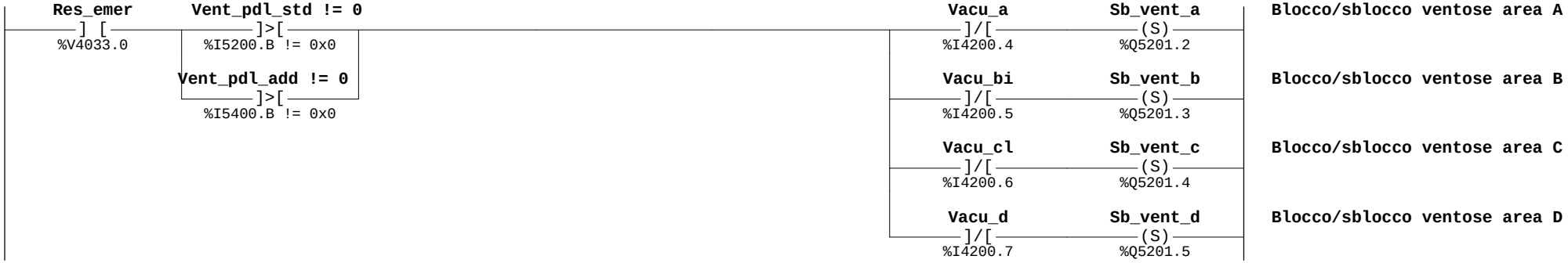
03 Label: Step: **Raz_pv** %M58.W **= 0**

Reset a fine posizionamento

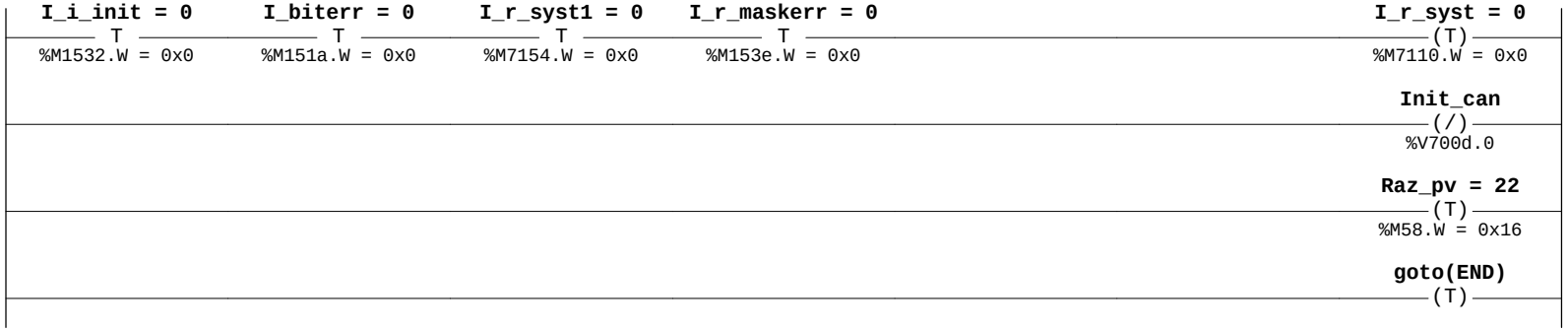


04 Label: Step: **Raz_pv** %M58.W **= 1**

Reset memorie

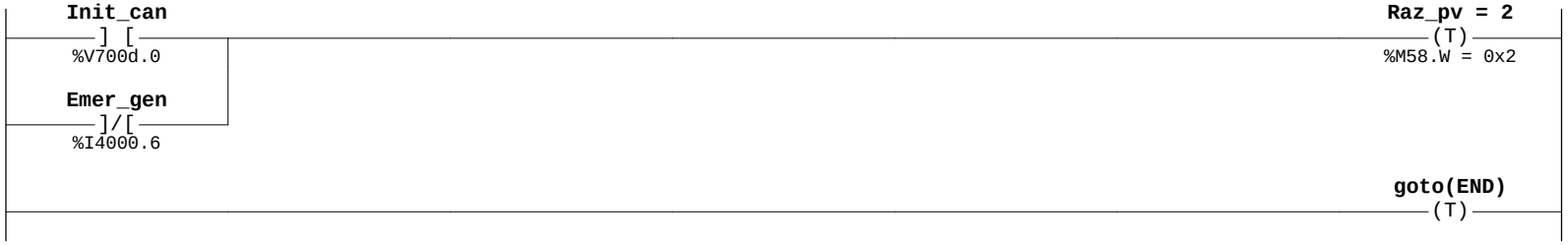


05 Label: Step: **Raz_pv** %M58.W **= 1**



Init ICLA

06 Label: Step: **Raz_pv** %M58.W **= 22**



07 Label: **LO** Step: **Raz_pv** %M58.W **= 2**

Raz motori icla

I_r_syst >= 0]>[%M7110.W >= 0x0

I_r_syst <= 16 * (N_assi - 1)]>[%M7110.W <= 0x10 * (%V7002.B - 0x1)

Raz_1[I_r_syst] ( ) %V7010.6[%M7110.W]

Prog_1[I_r_syst] (R) %V7010.2[%M7110.W]

P_syncro_1[I_r_syst] (R) %V7010.3[%M7110.W]

Jog_1[I_r_syst] (R) %V7010.5[%M7110.W]

I_r_syst += 16 (T) %M7110.W += 0x10

goto(LO) (T)

08 Label: Step: **Raz_pv** %M58.W **= 2**

I_r_syst = 0 (T) %M7110.W = 0x0

Raz_pv = 3 (T) %M58.W = 0x3

goto(END) (T)

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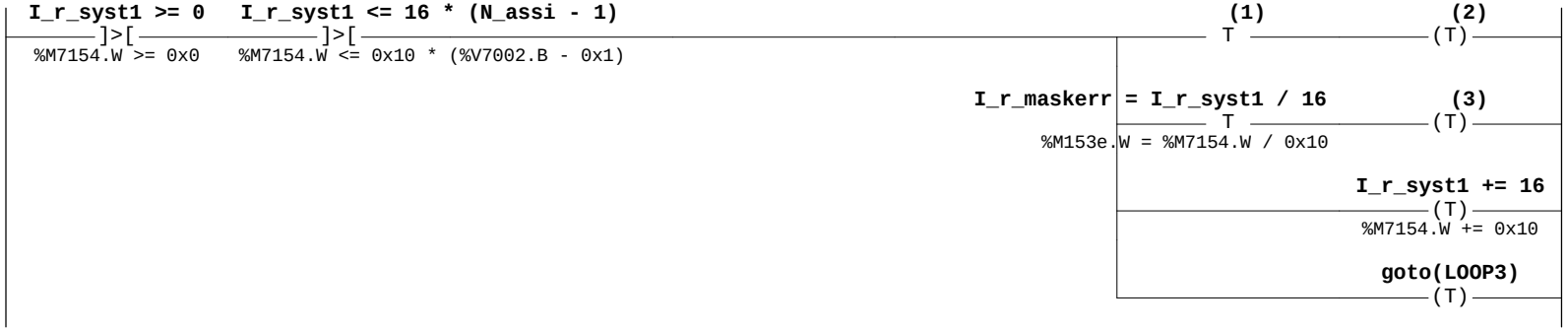
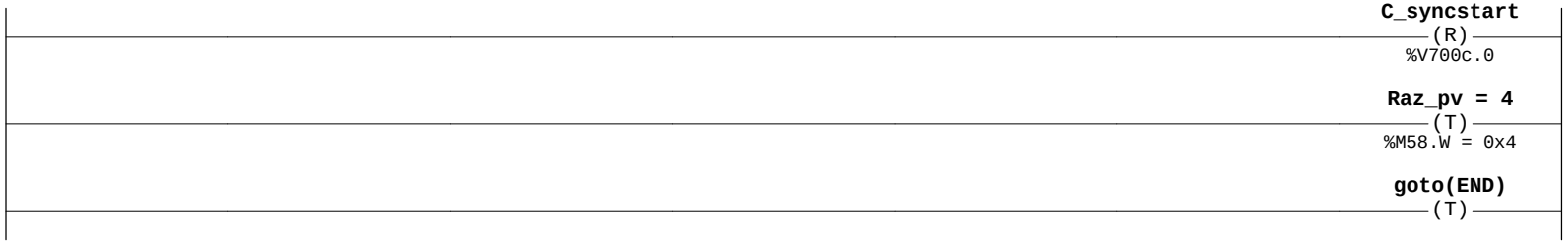
09 Label: **LOOP3** Step: **Raz_pv** %M58.W **= 3**

Reset maschere errori



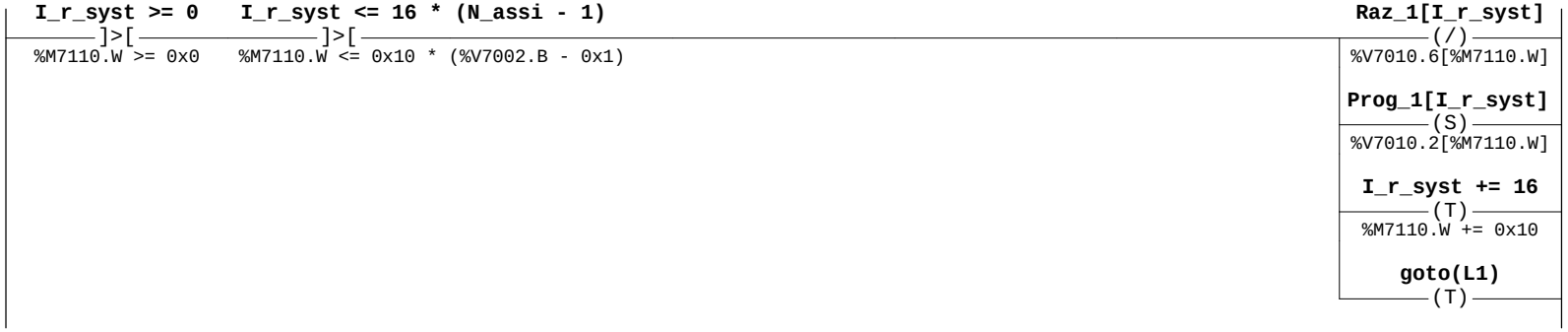
(1) %M1530.W = %M7154.W / 0x10 * 0x2 : I_r_biterr = I_r_syst1 / 16 * 2
(2) %V1202.W[%M1530.W] = 0x0 : Tab_err1[I_r_biterr] = 0
(3) %V1401.B[%M153e.W] = 0x0 : Mask_err1[I_r_maskerr] = 0

10 Label: Step: **Raz_pv** %M58.W **= 3**



11 Label: **L1** Step: **Raz_pv** %M58.W **= 4**

Abilitazione Prog motori Icla



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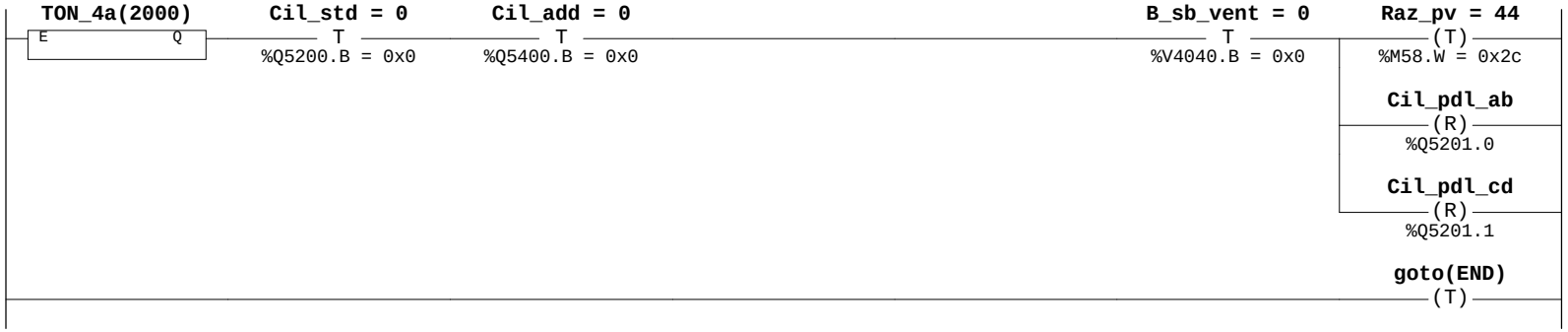
12 Label: Step: **Raz_pv** %M58.W **= 4**

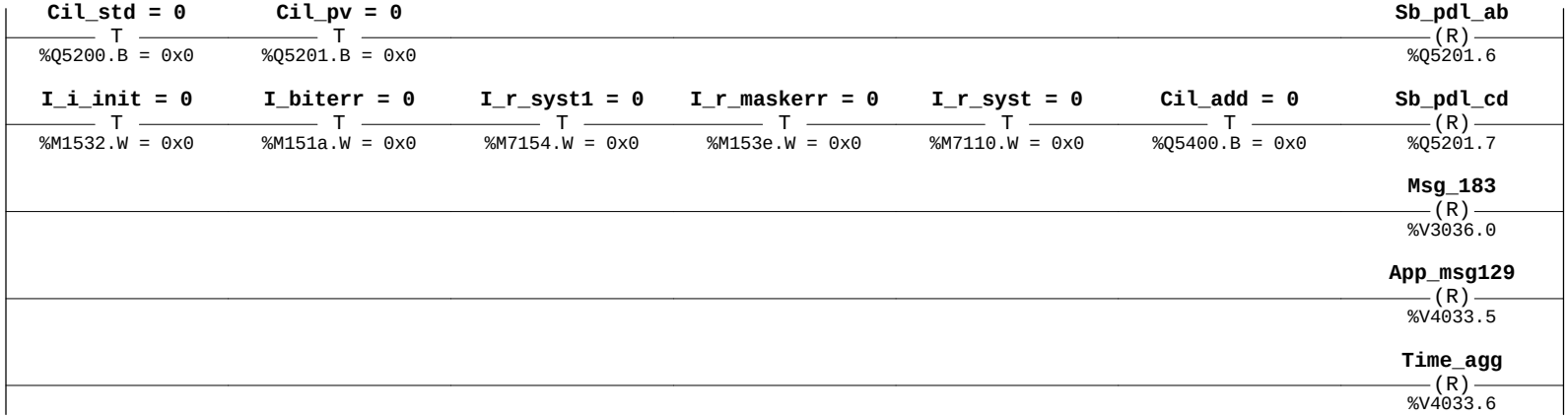


[T] TON_4a(0x7d0) : TON_4a(2000)

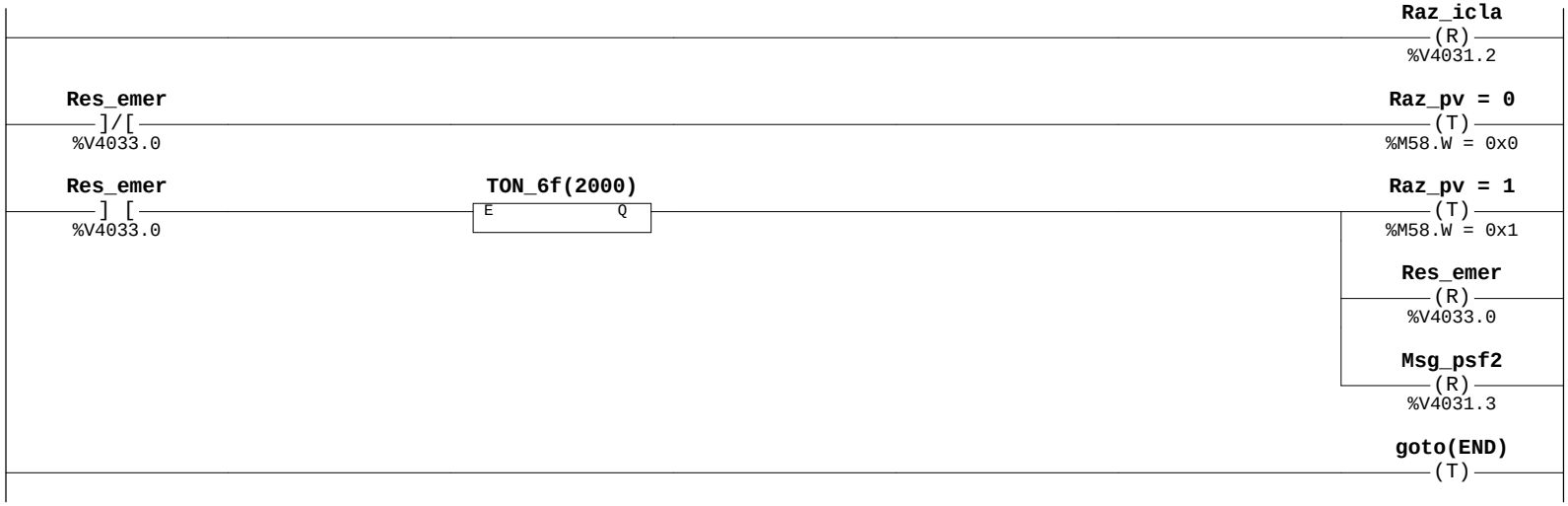
13 Label: Step: **Raz_pv** %M58.W **= 44**

[T] TON_4c(0x1f4) : TON_4c(500)

14 Label: Step: **Raz_pv** %M58.W **= 5**



15 Label: Step: **Raz_pv** %M58.W **= 5**



[T] TON_6f(0x7d0) : TON_6f(2000)

16 Label: **END** Step:

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