

Author:		NUM	TOO	T C
Company:		NOM	100.	по
Project: 1040_78.mch	TITRE		Date	28-02-2018
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03 Label: Step: Raz_pv %M58.W Reset a fine posizionamento = 0 Movimento_pv Ps_f2 V200_1.2 Raz_pv == 0 $Raz_pv = 1$ %M58.W = 0x1 -] / [-—R T— _]>[_ %V4032.0 %V202b.1 %V200.2 %M58.W == 0x0 X_{end} V211_2.6 _]/[_ __R_T_ %V503.0 %V211.6 V20f_3.5 Sel_man_aut —] / [— %I4101.4 Raz_icla V211_2.7 ___R_T___ %V211.7 —] [— %V4031.2 goto(END) — (T)—

04 Label: Step: Raz_pv %M58.W = 1 Reset memorie

Res_emer	<pre>Vent_pdl_std != 0</pre>	Vacu_a]/[]	Sb_vent_a (S)	Blocco/sblocco ventose area A
%V4033.0	%I5200.B != 0x0	%I4200.4	%Q5201.2	
	Vent_pdl_add != 0	Vacu_bi	Sb_vent_b	Blocco/sblocco ventose area B
	%I5400.B != 0x0		%Q5201.3	
		Vacu_cl	Sb_vent_c	Blocco/sblocco ventose area C
			(S)	
		Vacu_d	Sb_vent_d	Blocco/sblocco ventose area D
]/[———] %I4200.7	(S) %Q5201.5	

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05 Label: Step: Raz_pv %M58.W = 1 $I_r_{syst1} = 0$ $I_r_{maskerr} = 0$ $I_r_syst = 0$ ____ т ____ — т — ____ т ___ — т — —— (T)—— M1532.W = 0x0 M151a.W = 0x0M7154.W = 0x0 M153e.W = 0x0%M7110.W = 0x0 Init_can Init ICLA — (/) — %V700d.0 $Raz_pv = 22$ —— (T)—— M58.W = 0x16goto(END) —— (T)—

06 Label: Step: Raz_pv %M58.W = 22

Init_can	Raz_pv = 2
%V700d.0	%M58.W = 0x2
Emer_gen	
%I4000.6	
	goto(END)
	(T)

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07 Label: LO Step: Raz_pv %M58.W = 2 Raz motori icla I_r_syst >= 0 I_r_syst <= 16 * (N_assi - 1)</pre> Raz_1[I_r_syst] ____]>[__ %V7010.6[%M7110.W] Prog_1[I_r_syst] ——— (R)— %V7010.2[%M7110.W] P_syncro_1[I_r_syst] ——— (R)—— %V7010.3[%M7110.W] Jog_1[I_r_syst] ——— (R)—— %V7010.5[%M7110.W] $I_r_syst += 16$ ——— (T)— %M7110.W += 0x10 goto(LO) —— (T)— 08 Label: Step: Raz_pv %M58.W = 2 $I_r_syst = 0$ —— (T) —— %M7110.W = 0x0 $Raz_pv = 3$ —— (T)—— M58.W = 0x3

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goto(END)

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09 Label: LOOP3
                                  %M58.W = 3
                                                                              Reset maschere errori
                 Step: Raz pv
    I_r_{syst1} >= 0 I_r_{syst1} <= 16 * (N_assi - 1)
                                                                                                               (2)
                                                                                              (1)
                     ____]>[ __
     M7154.W >= 0x0 M7154.W <= 0x10 * (%V7002.B - 0x1)
                                                                           I_r_maskerr = I_r_syst1 / 16
                                                                                                              (3)
                                                                                         — т —
                                                                                                             —(T)—
                                                                                M153e.W = M7154.W / 0x10
                                                                                                        I_r_{syst1} += 16
                                                                                                         —— (T)—
                                                                                                        %M7154.W += 0x10
                                                                                                         goto(LOOP3)
                                                                                                          —— (T)—
   (1) %M1530.W = %M7154.W / 0x10 * 0x2 : I_r_biterr = I_r_syst1 / 16 * 2
   (2) V1202.W[M1530.W] = 0x0 : Tab_err1[I_r_biterr] = 0
   (3) V1401.B[M153e.W] = 0x0 : Mask_err1[I_r_maskerr] = 0
10 Label:
                  Step: Raz_pv
                                M58.W = 3
                                                                                                          C syncstart
                                                                                                                          start assi sincronizzato
                                                                                                            — (R)—
                                                                                                           %V700c.0
                                                                                                          Raz_pv = 4
                                                                                                           — (T)—
                                                                                                          M58.W = 0x4
                                                                                                           goto(END)
                                                                                                           — (T)—
11 Label: L1
                                M58.W = 4
                                                                          Abilitazione Prog motori Icla
                Step: Raz pv
     I_r_syst >= 0    I_r_syst <= 16 * (N_assi - 1)</pre>
                                                                                                        Raz_1[I_r_syst]
        ___]>[____
                   _____]>[ ___
                                                                                                         —— ( / ) —
     M7110.W >= 0x0 M7110.W <= 0x10 * (%V7002.B - 0x1)
                                                                                                       %V7010.6[%M7110.W]
                                                                                                       Prog_1[I_r_syst]
                                                                                                         ——(S)—
                                                                                                       %V7010.2[%M7110.W]
                                                                                                        I_r_syst += 16
                                                                                                          —— (T)—
                                                                                                        %M7110.W += 0x10
                                                                                                           goto(L1)
                                                                                                           — (T)—
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                                                                                                                              NUM TOOLS
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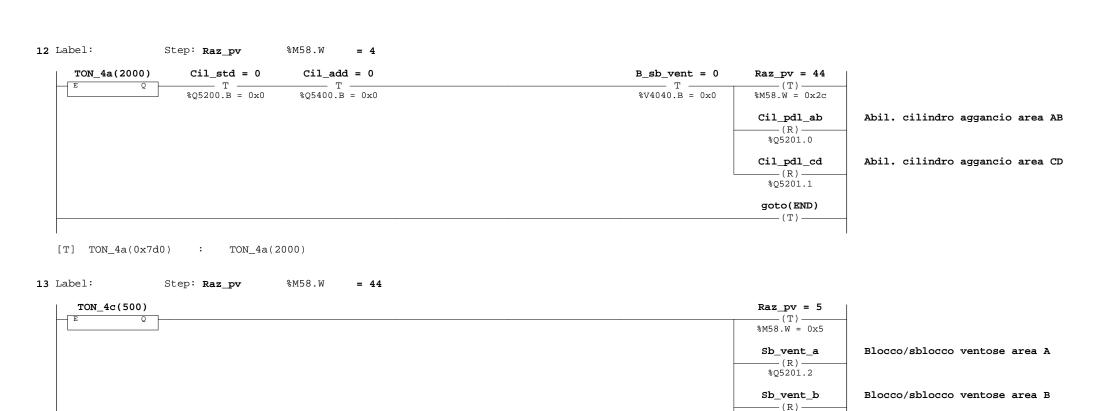
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[T] TON_4c(0x1f4) : TON_4c(500)

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%Q5201.3 **Sb_vent_c**

(R) %Q5201.5 goto(END) ——(T)—— Blocco/sblocco ventose area C

Blocco/sblocco ventose area D

14 Label: M58.W = 5Step: Raz pv $Cil_std = 0$ $Cil_pv = 0$ Sb_pdl_ab sblocco pdl area AB — т — — т — — (R)— 205200.B = 0x0205201.B = 0x0%Q5201.6 $I_i_i = 0$ $I_biterr = 0$ $I_r_{syst1} = 0$ $I_r_{maskerr} = 0$ Cil add = 0Sb_pdl_cd sblocco pdl area CD $I_r_syst = 0$ — т — — т — — т — — т — — т — — т — — (R)— M1532.W = 0x0M151a.W = 0x0M7154.W = 0x0M153e.W = 0x0M7110.W = 0x0%Q5201.7 Q5400.B = 0x0Setup piani e ventose in corso Msg_183 — (R)— %V3036.0 Appoggio MSG 129 App_msg129 — (R)— %V4033.5 bit per timer di attesa aggancio Time_agg — (R)-%V4033.6 **15** Label: %M58.W = **5** Step: Raz_pv Raz_icla Reset a fine posizionamento moto — (R)— %V4031.2 Res emer Raz pv = 0—] / [— — (T)— %V4033.0 M58.W = 0x0Res_emer TON_6f(2000) $Raz_pv = 1$ —][— —— (T)— %V4033.0 %M58.W = 0x1Res emer — (R)— %V4033.0 Msg_psf2 — (R)— %V4031.3 goto(END) —(T)— [T] TON_6f(0x7d0) : TON_6f(2000)

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Step:

16 Label: END