

00 Label: Step:

Verifica abbinamento aree

<div>X_end ] [  %V503.0</div>		<div>Abb_aree_ab (R)  %V1.0</div>	Abbinamento aree AB
		<div>Abb_aree_cd (R)  %V1.1</div>	Abbinamento aree CD
		<div>Abb_aree_ad (R)  %V1.2</div>	Abbinamento aree AD
		<div>Abb_aree_ef (R)  %V1.3</div>	Abbinamento aree EF
		<div>Abb_aree_gh (R)  %V1.4</div>	Abbinamento aree GH
		<div>Abb_aree_eh (R)  %V1.5</div>	Abbinamento aree EH

01 Label: Step:

Verifica abbinamento aree

<div>X_abb_ab ] [  %V530.2</div>			<div>Abb_aree_ab ( )  %V1.0</div>	Abbinamento aree AB
<div>X_abb_ef ] [  %V530.6</div>	<div>D_bdf ] [  %M803.3</div>	<div>E10028 ] [  %Re.4</div>	<div>Abb_aree_ab (S)  %V1.0</div>	Abbinamento aree AB
<div>X_abb_cd ] [  %V530.1</div>			<div>Abb_aree_cd ( )  %V1.1</div>	Abbinamento aree CD
<div>X_abb_gh ] [  %V530.5</div>	<div>D_bdf ] [  %M803.3</div>	<div>E10028 ] [  %Re.4</div>	<div>Abb_aree_cd (S)  %V1.1</div>	Abbinamento aree CD
<div>X_abb_ad ] [  %V530.0</div>			<div>Abb_aree_ad ( )  %V1.2</div>	Abbinamento aree AD
<div>X_abb_eh ] [  %V530.4</div>	<div>D_bdf ] [  %M803.3</div>	<div>E10028 ] [  %Re.4</div>	<div>Abb_aree_ad (S)  %V1.2</div>	Abbinamento aree AD

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Company:			
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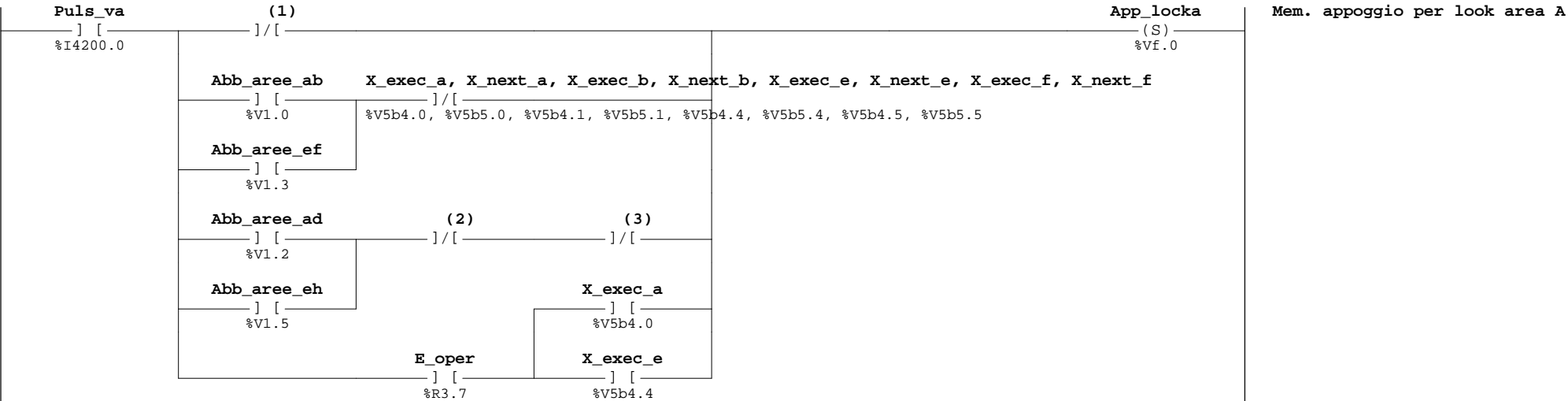
02 Label:Step:

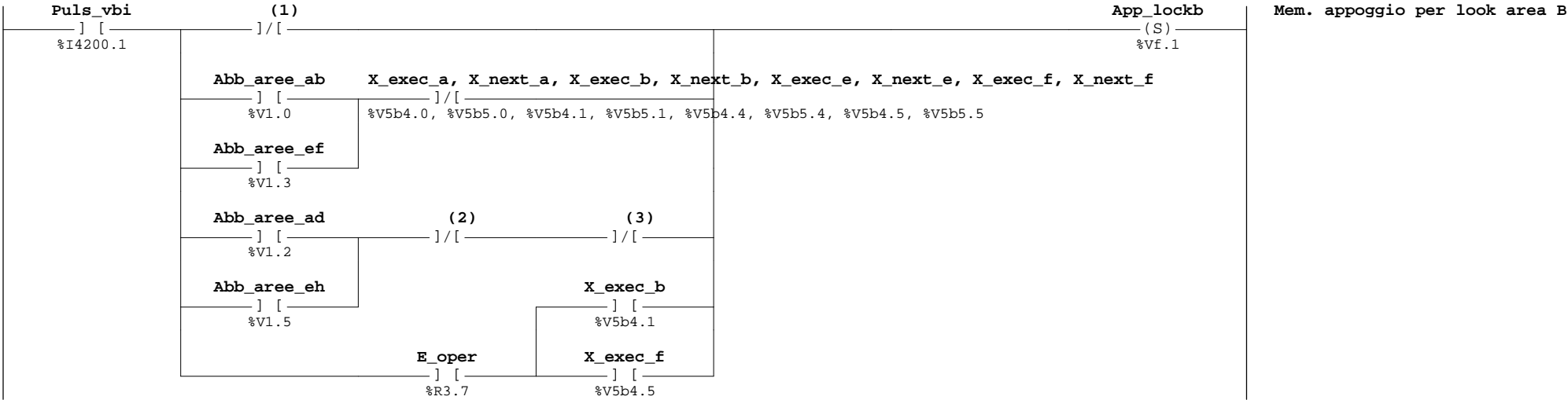
Verifica se piano nesting



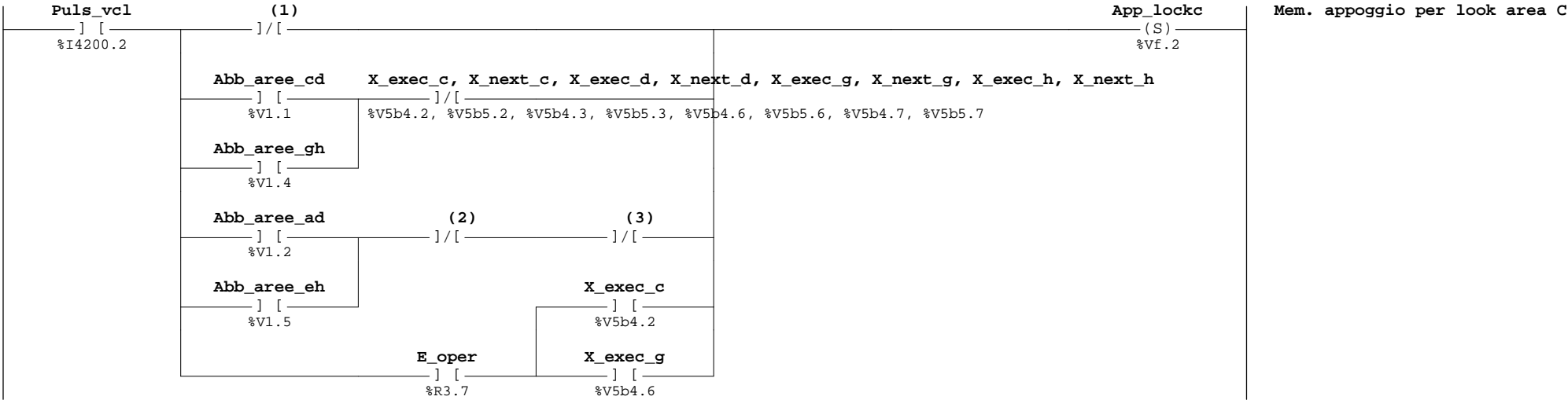
03 Label:Step:

Blocco sblocco pannello





- (1) %V5b4.1, %V5b5.1, %V5b4.5, %V5b5.5, %V1.0, %V1.2, %V1.3, %V1.5 : X\_exec\_b, X\_next\_b, X\_exec\_f, X\_next\_f, Abb\_aree\_ab, Abb\_aree\_ad, Abb\_aree\_ef, Abb\_aree\_eh
- (2) %V5b4.0, %V5b5.0, %V5b4.1, %V5b5.1, %V5b4.2, %V5b5.2, %V5b4.3, %V5b5.3 : X\_exec\_a, X\_next\_a, X\_exec\_b, X\_next\_b, X\_exec\_c, X\_next\_c, X\_exec\_d, X\_next\_d
- (3) %V5b4.4, %V5b5.4, %V5b4.5, %V5b5.5, %V5b4.6, %V5b5.6, %V5b4.7, %V5b5.7 : X\_exec\_e, X\_next\_e, X\_exec\_f, X\_next\_f, X\_exec\_g, X\_next\_g, X\_exec\_h, X\_next\_h

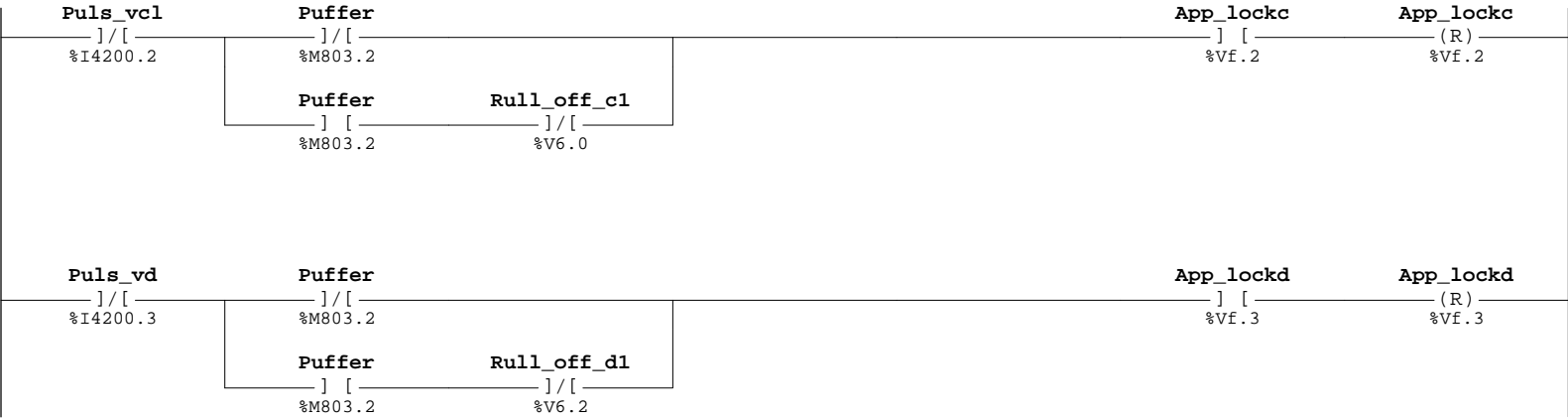


- (1) %V5b4.2, %V5b5.2, %V5b4.6, %V5b5.6, %V1.1, %V1.4, %V1.2, %V1.5 : X\_exec\_c, X\_next\_c, X\_exec\_g, X\_next\_g, Abb\_aree\_cd, Abb\_aree\_gh, Abb\_aree\_ad, Abb\_aree\_eh
- (2) %V5b4.0, %V5b5.0, %V5b4.1, %V5b5.1, %V5b4.2, %V5b5.2, %V5b4.3, %V5b5.3 : X\_exec\_a, X\_next\_a, X\_exec\_b, X\_next\_b, X\_exec\_c, X\_next\_c, X\_exec\_d, X\_next\_d
- (3) %V5b4.4, %V5b5.4, %V5b4.5, %V5b5.5, %V5b4.6, %V5b5.6, %V5b4.7, %V5b5.7 : X\_exec\_e, X\_next\_e, X\_exec\_f, X\_next\_f, X\_exec\_g, X\_next\_g, X\_exec\_h, X\_next\_h



08 Label: Step:

Blocco sblocco pannello



Mem. appoggio per look area C

Mem. appoggio per look area D

09 Label: Step:

Salita di tutte le rulliere

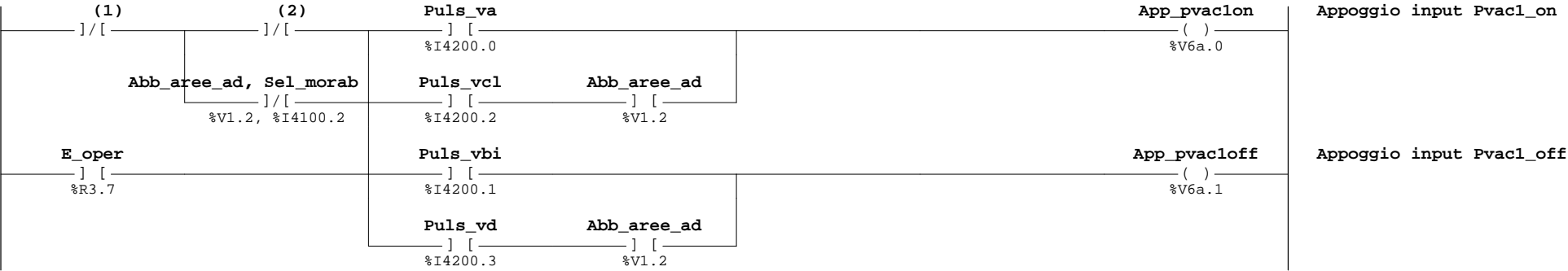


Soll. di tutte le ventose AD

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10 Label: **NESTING**      Step:

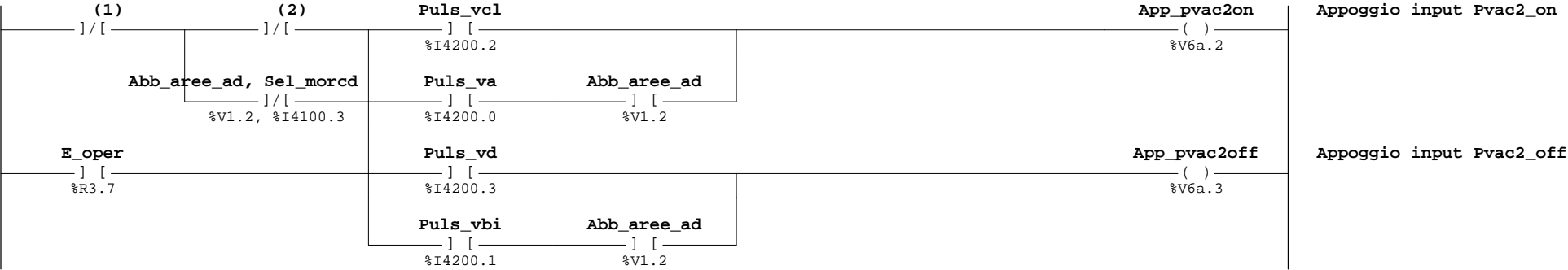
Blocco sblocco pannello



(1) %V5b4.0, %V5b5.0, %V5b4.4, %V5b5.4, %V5b4.1, %V5b5.1, %V5b4.5, %V5b5.5 : X\_exec\_a, X\_next\_a, X\_exec\_e, X\_next\_e, X\_exec\_b, X\_next\_b, X\_exec\_f, X\_next\_f  
(2) %V5b4.2, %V5b5.2, %V5b4.6, %V5b5.6, %V5b4.3, %V5b5.3, %V5b4.7, %V5b5.7 : X\_exec\_c, X\_next\_c, X\_exec\_g, X\_next\_g, X\_exec\_d, X\_next\_d, X\_exec\_h, X\_next\_h

11 Label:              Step:

Blocco sblocco pannello



(1) %V5b4.2, %V5b5.2, %V5b4.6, %V5b5.6, %V5b4.3, %V5b5.3, %V5b4.7, %V5b5.7 : X\_exec\_c, X\_next\_c, X\_exec\_g, X\_next\_g, X\_exec\_d, X\_next\_d, X\_exec\_h, X\_next\_h  
(2) %V5b4.0, %V5b5.0, %V5b4.4, %V5b5.4, %V5b4.1, %V5b5.1, %V5b4.5, %V5b5.5 : X\_exec\_a, X\_next\_a, X\_exec\_e, X\_next\_e, X\_exec\_b, X\_next\_b, X\_exec\_f, X\_next\_f

12 Label: **FINE**              Step: