

Author:		NUM	TOOLS	•
Company:		NOM	TOOL	•
Project: 1040_78.mch	TITRE		Date	28-02-2018
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02 Label: Step: Verifica se piano nesting

| Nesting | goto(NESTING) | (T) - (T

03 Label: Step: Blocco sblocco pannello

(1) Mem. appoggio per look area A Puls_va App_locka]/[--(S)-%I4200.0 %Vf.0 Abb_aree_ab X_exec_a, X_next_a, X_exec_b, X_next_b, X_exec_e, X_next_e, X_exec_f, X_next_f %V1.0 %V5b4.0, %V5b5.0, %V5b4.1, %V5b5.1, %V5b4.4, %V5b5.4, %V5b4.5, %V5b5.5 Abb_aree_ef —][-%V1.3 Abb aree ad (2) (3) _][_ %V1.2 Abb_aree_eh X_exec_a _][-—][— %V1.5 %V5b4.0 E_oper X_exec_e -1 [-— 1 [— %R3.7 %V5b4.4

- (1) %V5b4.0, %V5b5.0, %V5b4.4, %V5b5.4, %V1.0, %V1.2, %V1.3, %V1.5 : X_exec_a, X_next_a, X_exec_e, X_next_e, Abb_aree_ab, Abb_aree_ad, Abb_aree_ef, Abb_aree_eh
- (2) %V5b4.0, %V5b5.0, %V5b4.1, %V5b5.1, %V5b4.2, %V5b5.2, %V5b4.3, %V5b5.3 : X_exec_a, X_next_a, X_exec_b, X_next_b, X_exec_c, X_next_c, X_exec_d, X_next_d
- (3) %V5b4.4, %V5b5.4, %V5b4.5, %V5b5.5, %V5b4.6, %V5b5.6, %V5b4.7, %V5b5.7 : X_exec_e, X_next_e, X_exec_f, X_next_f, X_exec_g, X_next_g, X_exec_h, X_next_h

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Step:

Blocco sblocco pannello

Puls_vbi	(1)		App_lockb	Mem. appoggio per look area B
%14200.1]/[%Vf.1	
	Abb_aree_ab	<pre>X_exec_a, X_next_a, X_exec_b, X_next_b, X_exec_e</pre>	, X_next_e, X_exec_f, X_next_f	
	%V1.0	%V5b4.0, %V5b5.0, %V5b4.1, %V5b5.1, %V5b4.4, %V5b5.4, %V	√5b4.5, %V5b5.5	
	Abb_aree_ef			
	Abb_aree_ad	(2) (3)		
	%V1.2			
	Abb_aree_eh	X_exec_b		
	%V1.5	%V5b4.1		
		E_oper		
		*R3.7		

(1) %V5b4.1, %V5b5.1, %V5b4.5, %V5b5.5, %V1.0, %V1.2, %V1.3, %V1.5 : X_exec_b, X_next_b, X_exec_f, X_next_f, Abb_aree_ab, Abb_aree_ad, Abb_aree_ef, Abb_aree_eh

(2) %V5b4.0, %V5b5.0, %V5b4.1, %V5b5.1, %V5b4.2, %V5b5.2, %V5b4.3, %V5b5.3 : X_exec_a, X_next_a, X_exec_b, X_next_b, X_exec_c, X_next_c, X_exec_d, X_next_d

(3) %V5b4.4, %V5b5.4, %V5b4.5, %V5b5.5, %V5b4.6, %V5b5.6, %V5b4.7, %V5b5.7 : X_exec_e, X_next_e, X_exec_f, X_next_f, X_exec_g, X_next_g, X_exec_h, X_next_h

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Blocco sblocco pannello

Puls_vcl	(1)		App_lockc	Mem. appoggio per look area C
%I4200.2]/[%Vf.2	
	Abb_aree_cd] [<pre>X_exec_c, X_next_c, X_exec_d, X_next_d, X_exec_g, X_next_g,</pre>		
	Abb_aree_gh			
	Abb_aree_ad 	(2) 		
	Abb_aree_eh] [%V1.5	X_exec_c] [%V5b4.2		
		E_oper		

- (1) %V5b4.2, %V5b5.2, %V5b4.6, %V5b5.6, %V1.1, %V1.4, %V1.2, %V1.5 : X_exec_c, X_next_c, X_exec_g, X_next_g, Abb_aree_cd, Abb_aree_gh, Abb_aree_ad, Abb_aree_eh
- (2) %V5b4.0, %V5b5.0, %V5b4.1, %V5b5.1, %V5b4.2, %V5b5.2, %V5b4.3, %V5b5.3 : X_exec_a, X_next_a, X_exec_b, X_next_b, X_exec_c, X_next_c, X_exec_d, X_next_d
- (3) %V5b4.4, %V5b5.4, %V5b4.5, %V5b5.5, %V5b4.6, %V5b5.6, %V5b4.7, %V5b5.7 : X_exec_e, X_next_e, X_exec_f, X_next_f, X_exec_g, X_next_g, X_exec_h, X_next_h

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06 Label: Step: Blocco sblocco pannello Puls_vd (1) App_lockd Mem. appoggio per look area D -(S) %I4200.3 %Vf.3 Abb aree cd X_exec_c, X_next_c, X_exec_d, X_next_d, X_exec_g, X_next_g, X_exec_h, X_next_h _] [_ %V1.1 %V5b4.2, %V5b5.2, %V5b4.3, %V5b5.3, %V5b4.6, %V5b5.6, %V5b4.7, %V5b5.7 Abb_aree_gh — 1 [-%V1.4 Abb_aree_ad (2) (3) — l [--]/[-%V1.2 Abb_aree_eh X_exec_d _1 [_ _][-%V1.5 %V5b4.3 E_oper X_exec_h -1 [--][-%R3.7 %V5b4.7 (1) %V5b4.3, %V5b5.3, %V5b4.7, %V5b5.7, %V1.1, %V1.4, %V1.2, %V1.5 X_exec_d, X_next_d, X_exec_h, X_next_h, Abb_aree_cd, Abb_aree_gh, Abb_aree_ad, Abb aree eh (2) %V5b4.0, %V5b5.0, %V5b4.1, %V5b5.1, %V5b4.2, %V5b5.2, %V5b4.3, %V5b5.3 X_exec_a, X_next_a, X_exec_b, X_next_b, X_exec_c, X_next_c, X_exec_d, X_next_d (3) %V5b4.4, %V5b5.4, %V5b4.5, %V5b5.5, %V5b4.6, %V5b5.6, %V5b4.7, %V5b5.7 X_exec_e, X_next_e, X_exec_f, X_next_f, X_exec_g, X_next_g, X_exec_h, X next h **07** Label: Step: Blocco sblocco pannello Puls va Puffer App_locka App_locka Mem. appoggio per look area A -]/[-]/[-(R)-%I4200.0 %M803.2 %Vf.0 %Vf.0 Puffer Rull off a1 -]/[-%M803.2 %V5.4

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App_lockb

%Vf.1

App_lockb

-(R)

%Vf.1

Mem. appoggio per look area B

Puffer

-]/[

%M803.2

Puffer —] [—

%M803.2

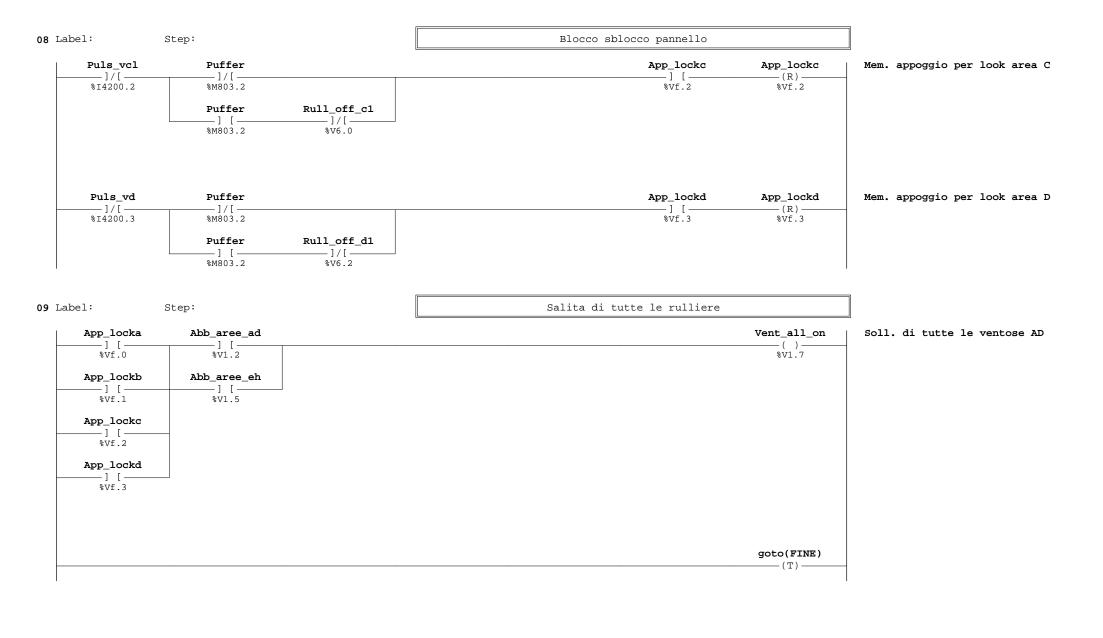
Rull_off_b1

—]/[— %V5.6

Puls vbi

—] / [-

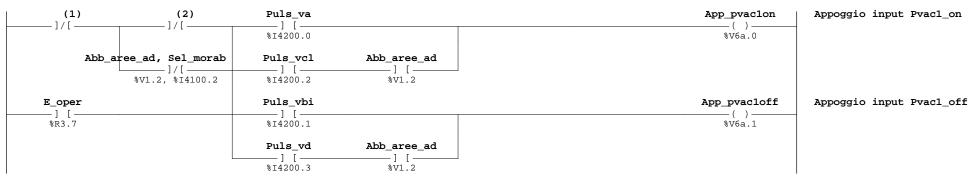
%I4200.1



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Company:		INOM	тооп	.
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10 Label: NESTING Step:

Blocco sblocco pannello



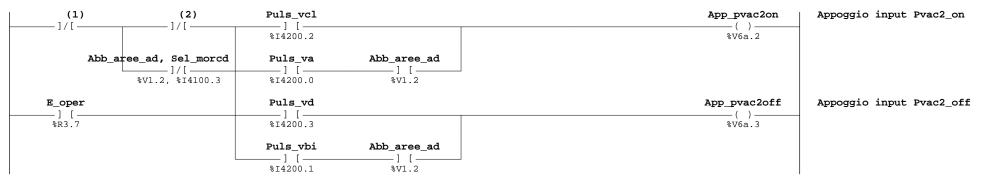
- (1) %V5b4.0, %V5b5.0, %V5b4.4, %V5b5.4, %V5b4.1, %V5b5.1, %V5b4.5, %V5b5.5 : X_exec_a, X_next_a, X_exec_e, X_next_e, X_exec_b, X_next_b, X_exec_f, X_next_f
- (2) %V5b4.2, %V5b5.2, %V5b4.6, %V5b5.6, %V5b4.3, %V5b5.3, %V5b4.7, %V5b5.7 : X_exec_c, X_next_c, X_exec_g, X_next_g, X_exec_d, X_next_d, X_exec_h, X_next_h

11 Label:

Step:

Copyright by...

Blocco sblocco pannello



- (1) %V5b4.2, %V5b5.2, %V5b4.6, %V5b5.6, %V5b4.3, %V5b5.3, %V5b4.7, %V5b5.7 : X_exec_c, X_next_c, X_exec_g, X_next_g, X_exec_d, X_next_d, X_exec_h, X_next_h
- (2) %V5b4.0, %V5b5.0, %V5b4.4, %V5b5.4, %V5b4.1, %V5b5.1, %V5b4.5, %V5b5.5 : X_exec_a, X_next_a, X_exec_e, X_next_e, X_exec_b, X_next_b, X_exec_f, X_next_f

12 Label: FINE Step:

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