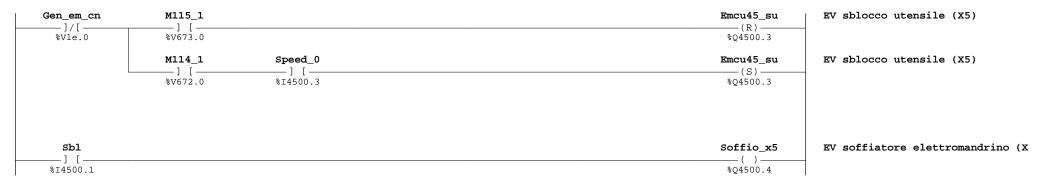
00 Label:



01 Label: Step:



[T] $TON_07(0x5dc)$: $TON_07(1500)$

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Step:

02 Label: Step:



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03 Label: Step: Icil_basso Icil_alto (1) Evolution Gruppox5_on Discesa gruppo X5 —][— — 1 / F — —(R)— %Q4501.1 %I4500.4 %I4500.6 %M803.1 Cuffia_dis Gruppox5_off Salita gruppo X5 —][— —(S)-%I4500.7 %Q4501.0 M117 1 App_iniz_lub.5 Mem. appoggio inizio lubrificazi __] [_ —(S)-%V675.0 %V44.5 M116_1 App_iniz_lub.5 Mem. appoggio inizio lubrificazi —] [— —(R)-%V674.0 %V44.5 E_raz %R3.0 Ra47 0 —] [— %Ra47.0 (1) %V88.5, %V84.3, %Vf.6, %V44.5 : Ab_asst1, Test_el1, Pez_sblo, App_iniz_lub.5 Verifica se AUT o MDI **04** Label: Step: (1) Pres_el1, El_1_on goto(END) —__]/[— —— (T) — %I4000.0, %Q4100.2 X test fora goto(TESTIO) __1 [_ — (T) — %V503.2 X_modo_sim, E_oper goto(AUTO) —] / [— — (T) — %V503.1, %R3.7 goto(RESET) —— (F)— (1) %M800.4, %V100.0 : El_11kw, Conf_el1_cu

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05 Label: TESTIO Step: Inverter on

 $V50_w > 5$ Speed_0 (1) $X_{conv} == 1$ Inv1_on_em1 Res_sel1 __]>[_ _]>[_ _][_ —] / [– —(S)-%V50.W > 0x5 %V401.B == 0x1 %I4500.3 %04101.3 %V8.0 Test el1 Inv1 on em1 Inv1 on em1 —] / [— _1 [_ —(R)-%V84.3 %V8.0 %V8.0 X_ccw, Test_el1 $X_{conv} == 1$ Inv1_ccw_em1 —] [— ___]>[___ — () – %V402.2, %V84.3 %V401.B == 0x1 %V8.1

Mem. inverter 1 richiesto da EM1

Mem. inverter 1 richiesto da EM1

Mem. inverter 1 CCW da EM1

(1) %V84.3, %V22.6, %V402.1 : Test_ell, App_freq1_0, X_convon

06 Label: AUTO Step:

Inverter on

goto(RESET)

]/[Speed_0][[2)	App_freq1_0 	Res_sel1]/[%Q4101.3	Inv1_on_em1 (S)
Ra4b_4	Inv1_on_em1 [] [Inv1_ccw_em1 (S)
%Ra4b.4	%V8.0				%V8.1
(3)	Itir			Ra37_0	Ab_asst1
]/[] [] [(S)
	%I4500.2			%Ra37.0	%V88.5

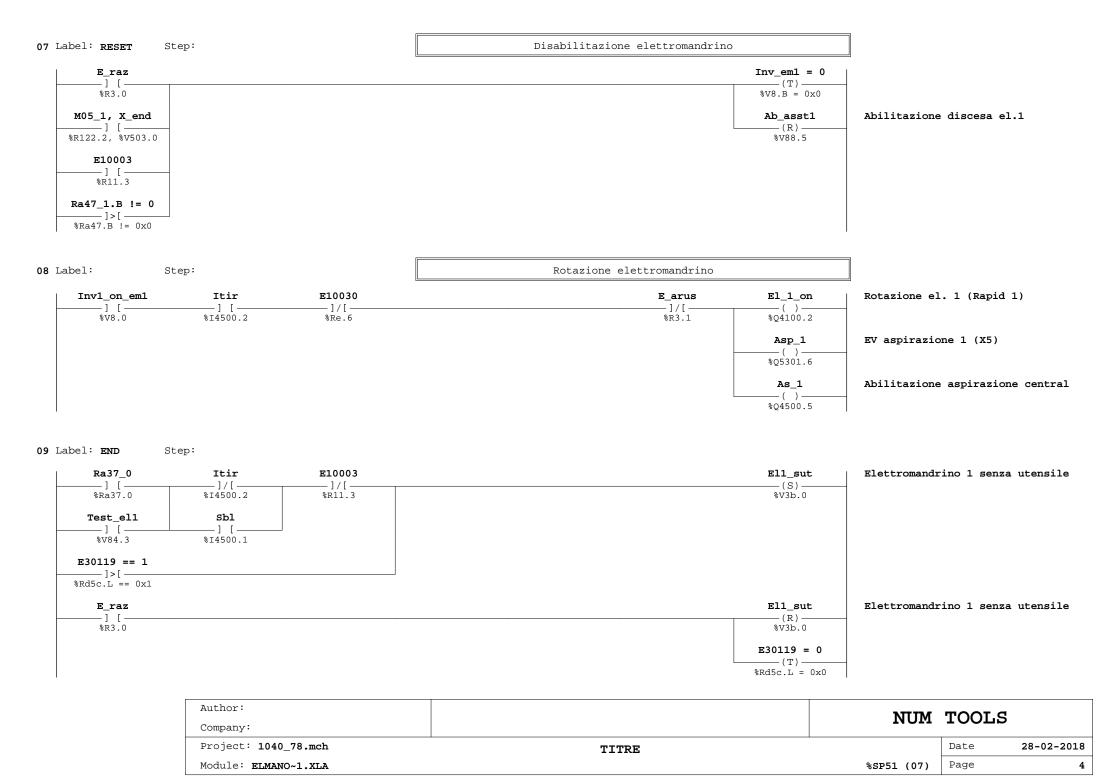
Mem. inverter 1 richiesto da EM1

Mem. inverter 1 CCW da EM1

Abilitazione discesa el.1

- (1) %V503.1, %R3.7, %R11.3, %V33.0 : X_modo_sim, E_oper, E10003, Ciclo_cul
- (2) Ra4b.B & 0xf == 0x1 : $Ra4b_4.B \& 15 == 1$
- (3) %V503.1, %R3.7, %I4101.3, %R11.6, %V33.0 : X_modo_sim, E_oper, Setting, E10006, Ciclo_cul

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10 Label: Step:

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