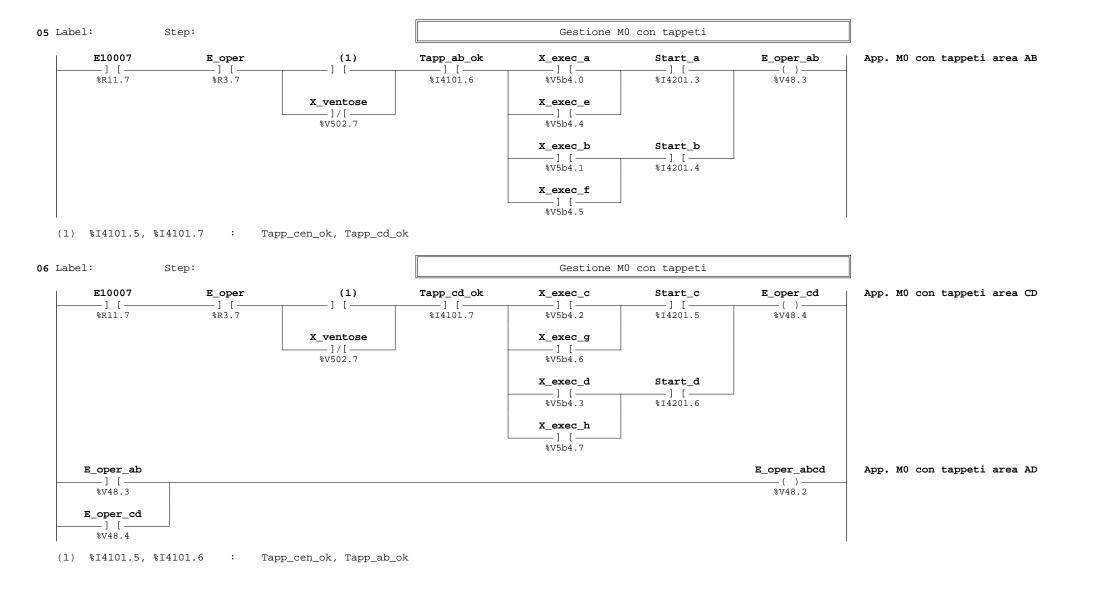
```
00 Label:
                   Step:
                                                                     Lettura potenziometri analogica e bit lampeggio
                                                                                                                    (1)
                                                                                                                   ·(T)-
                                                                                                                    (2)
                                                                                                                   -(T)-
                                                                                                              Cont_40 += 1
                                                                                                                  — (T)—
                                                                                                               Mle.B += 0x1
     Cont_40 > 20
                                                                                                                  M30_1
                                                                                                                                Lampeggio leed
       ____1>[ ___
                                                                                                                  — ( ) —
     Mle.B > 0x14
                                                                                                                  %M30.1
     Cont_40 > 40
                                                                                                               Cont_40 = 0
        — (T) —
      M1e.B > 0x28
                                                                                                               Mle.B = 0x0
   (1) anai(0x10, %V66.&)
                                  anai(16, V_pot2.&)
   (2) anai(0x11, %V64.&)
                                  anai(17, V_pot1.&)
01 Label:
                                                                            Verifica ed emergenza 10 percento
                   Step:
                                                                                                                    (1)
                                                                                                                    (2)
                                                                                                                   -(T)-
     End_acc_inv1
                      Diff_inv1 > 0
                                              (3)
                                                           M_rip1_st4
                                                                           TON_0f(1000)
                                                                                                               Emer10perc
                                                                                                                                Emergenza per +10% analogica inv
                          — l>[ —
                                                              — 1 / [ -
        %I4001.2
                        %V52.W > 0x0
                                                             %M31.3
                                                                                                                 %V1e.6
                      Diff_inv1 < 0 Diff_inv1 * -1 > CallOperc * 2
                                                                                                                                ANOMALIA INVERTER 10%
                                                                                                                 Msg_159
                        ____]>[___
                                           ___]>[___
                                                                                                                  -(S)-
                        %V52.W < 0x0
                                      %V52.W * 0xfffffffff > %V54.W * 0x2
                                                                                                                 %V301e.0
   (1) V52.W = V66.W - V50.W: Diff_inv1 = V_pot2 - V50_w
   (2) %V54.W = %V50.W * 0xa / 0x64 : CallOperc = V50_w * 10 / 100
   (3) V52.W >= V54.W: Diff_inv1 >= CallOperc
   [T] TON_0f(0x3e8) : TON_0f(1000)
```

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```
02 Label:
                                                                               Rapporto potenziometro al 100%
                    Step:
                                                                                                                       (1)
                                                                                                                      (T)-
                                                                                                  Pot_100 = Pot_100 * 100 / 120
                                                                                                                     — (T) —
                                                                                                  V2060.W = V2060.W * 0x64 / 0x78
   (1) %V2060.W = %V2020.W + %V201d.B : Pot_100 = Ps_pt1app + Ps_pt1low
03 Label:
                                                                               Scrittura valore potenziometri
                    Step:
        Rapid1
                                                                                                                       (1)
                                                                                                                      (T) -
         —][—
         %R103.0
      Modcour == 7
         __]>[_
      %R16.B == 0x7
         Rapid1
                       Modcour != 7
                                                                                                                       (2)
         — ] / [ —
                          ___]>[__
                                                                                                                      ·(T)-
                        %R16.B != 0x7
         %R103.0
   (1) %W102.B = %V2060.W
                             : Potav1 = Pot_100
   (2) %W102.B = %V202f.B : Potav1 = Ps_pot2
04 Label:
                    Step:
                                                                                  Gestione modo sequenziale
      Modcour == 1
                                                                                                                                   Mem. modo SEQ
                           E_prog
                                                                                                                    Ab_seq
         ___]>[___
                           _][_
                                                                                                                     —(S)-
      %R16.B == 0x1
                           %R5.1
                                                                                                                    %V2a.3
         Ab_seq
                        Modcour == 0
                                                                                                                  Ab_aut_seq
                                                                                                                                   Mem. modo AUT disabilitando modo
        —][—
                          __]>[__
                                                                                                                    — ( ) —
                        %R16.B == 0x0
                                                                                                                    %V2a.4
         %V2a.3
     Rich_cicl_pa
                                                                                                                    Ab_seq
                                                                                                                                   Mem. modo SEQ
         — 1 [ —
                                                                                                                     —(R)-
         %V21.4
                                                                                                                    %V2a.3
      Rich_raz_pan
         —11—
         %V21.0
```

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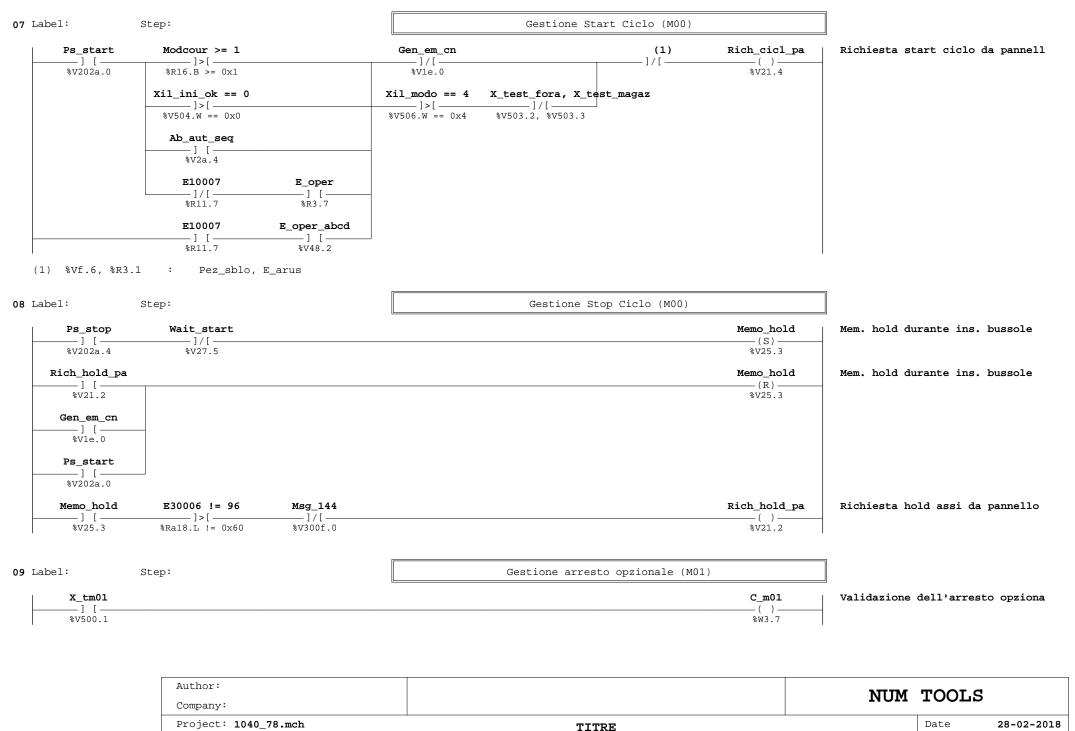
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10 Label: Step: Gestione manipolatori assi

(1) Xil_modo != 3 Ps_selax == 1 Ps_piu Jogpos0

JOG positivo asse nø 0 -]>[-_]>[_ _][-%V506.W != 0x3 %V202d.B == 0x1 %V202b.0 %W9.0 Setting Ps meno Joqneq0 JOG negativo asse nø 0 —] [– —] / [– _ () _ %I4101.3 %V202a.3 %Wd.0 Ps_selax == 12 Ps_piu Jogpos1 JOG positivo asse nø 1 —] **>** [— —][-%V202d.B == 0xc %V202b.0 %W9.1 Ps_meno JOG negativo asse nø 1 Jogneg1 __1 [_ **–** () – %V202a.3 %Wd.1 JOG positivo asse nø 2 Ps_selax == 10 Ps_piu Jogpos2 _1 [_ _ () -%V202d.B == 0xa %V202b.0 %W9.2 Jogneg2 JOG negativo asse nø 2 Ps meno _ 1 [_ _ () _ %V202a.3 %Wd.2

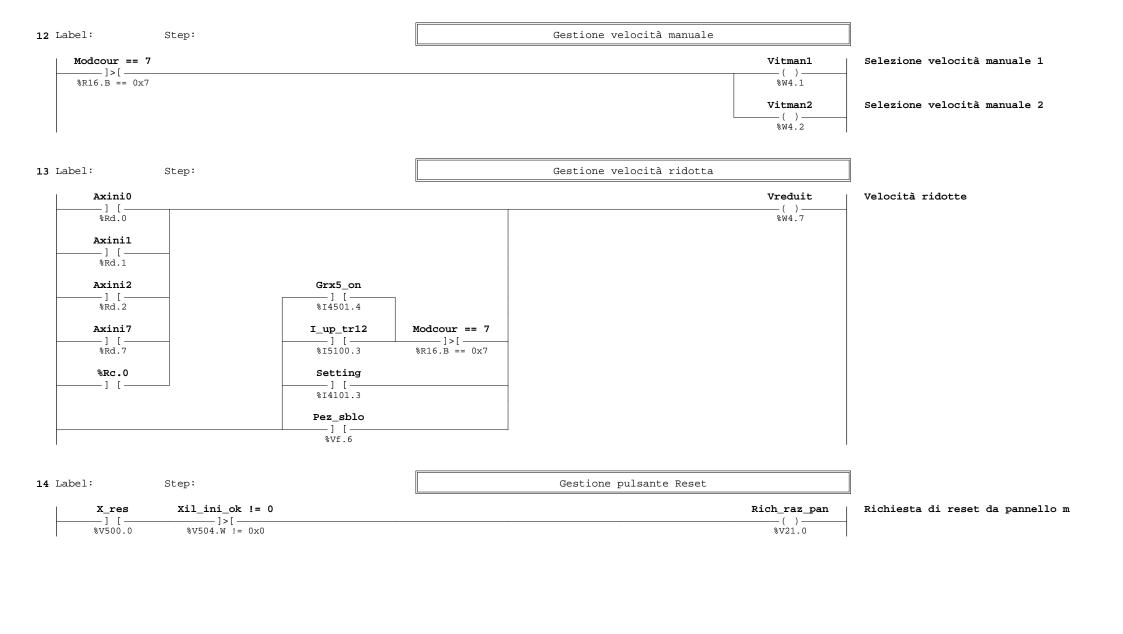
(1) %I4101.3, %I4001.3, %I4100.7 : Setting, Pul_um1, Pul_um2

11 Label: Step: Gestione manipolatori assi

(1) Xil modo != 3 JOG positivo asse nø 7 Ps selax == 3 Ps piu Jogpos7 __1>[_]>[_ -][-**- () -**%V506.W != 0x3 %V202d.B == 0x3 %V202b.0 %W9.7 Ps_meno Setting Jogneg7 JOG negativo asse nø 7 —] / [— —] [– %I4101.3 %Wd.7 %V202a.3 JOG positivo asse nø 8 Ps selax == 4 Ps piu Jogpos8 _]>[_ —] [— — () – %V202d.B == 0x4 %V202b.0 %W8.0 JOG negativo asse nø 8 Ps meno Joqneq8 —][-%V202a.3 %Wc.0

(1) %I4101.3, %I4001.3, %I4100.7 : Setting, Pul_um1, Pul_um2

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16 Label: Step:

Ps_cuffia 	V205_0 	E10003]/[%R11.3	M_app1 ()	Mem. ar
M_app1][Enab_cuff] [%V28.2		M_app2 ()	Mem. ar
M_app1] [M_app2]/[%V28.4		Enab_cuff()_ %V28.2	Mem. so
Enab_cuff] [

Mem. appoggio soll. cuffie da op

Mem. appoggio discesa cuffie da

Mem. sollevamento cuffie da oper

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