el: Step:		VERIFICA CARICAMENTO NUC	OVO PROGRAMMA	
X_pgm_a	Sel_man_aut	V207_4	Change_prg_a	Nuovo programma in corso area
%V531.0	%I4101.4	R_T	%V4034.4	
X_pgm_e				
%V531.4				
X_pgm_b	Sel_man_aut	v207_5	Change_prg_b	Nuovo programma in corso area E
] [] [R_T	(S)———(S)——————————————————————————————	
X_pgm_f				
%V531.5				
X_pgm_c	Sel_man_aut	V207_5.6	Change_prg_c	Nuovo programma in corso area C
%V531.2][%I4101.4	R_T	(S)— %V4034.6	
X_pgm_g				
*V531.6				
el: Step:				
X pam d	Sel man aut	V207 7	Change pro d	Nuovo programma in corso area D

X_pgm_d		Sel_man_aut	V207_7	Change_prg_d	Nuovo programma in corso area D
%V531.3		%I4101.4		*V4034.7	
X_pgm_h					
%V531.7					
Start_a	Change_prg_a			Inib_start_a	Inibizione start area A
%I4201.3	%V4034.4			(S)- %V4035.0	
Start_b	Change_prg_b			Inib_start_b	Inibizione start area B
%I4201.4	%V4034.5			(S)- %V4035.1	
Start_c	Change_prg_c			Inib_start_c	Inibizione start area C
%I4201.5	~~~~~~] [~~~~~~ %V4034.6			(S)— %V4035.2	
Start_d	Change_prg_d			Inib_start_d	Inibizione start area D
%I4201.6	%V4034.7			(S)— %V4035.3	

Author:		NTTM	TOOLS	3
Company:		NOM	TOOL	5
Project: 1040_78.mch	TITRE		Date	28-02-2018
Module: START_PV.XLA		%SP214 (00)	Page	1

02 Label:

Step:

App_setupa	Change_prg_a	Nuovo programma in corso area A
%V4034.0	(R)- %V4034.4	
X_end]/[Inib_start_a(R)	Inibizione start area A
Sel_man_aut		
App_setupb	Change_prg_b	Nuovo programma in corso area B
App_setupb][Change_prg_b	Nuovo programma in corso area B
] [(R)	Nuovo programma in corso area B Inibizione start area B

03 Label: Step:

App_setupc] [Change_prg_c —(R)	Nuovo programma in corso area C
X_end]/[%V503.0	Inib_start_c(R)	Inibizione start area C
App_setupd] [Change_prg_d 	Nuovo programma in corso area D
X_end]/[Tnib_start_d	Inibizione start area D
Sel_man_aut 		

Author:		NTTM	TOOLS	
Company:		NOM	тоопр	
Project: 1040_78.mch	TITRE		Date	28-02-2018
Module: START_PV.XLA		%SP214 (02)	Page	2

04 Label:

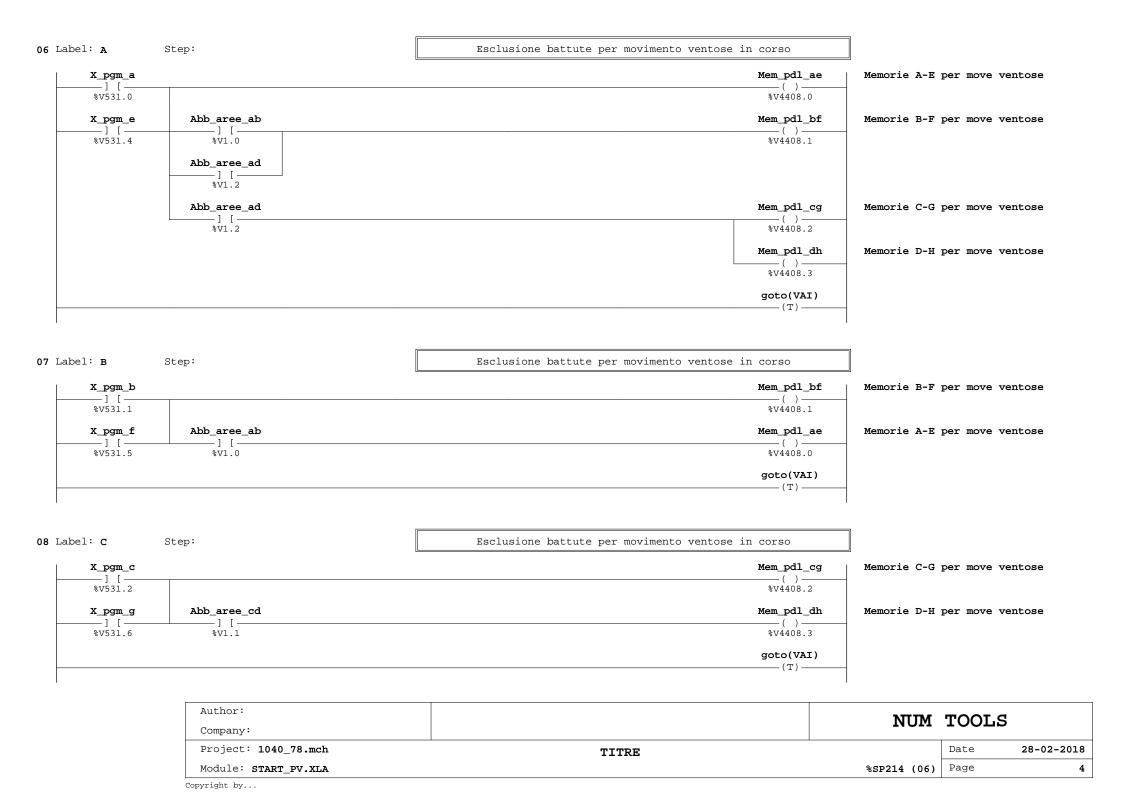
Step:

App_setupa, App_setupb, App_setupc, App_setupd	Raz_pv == 5	Mem_pdl_ae 	Memorie A-E per move ventose
\$V4034.0, \$V4034.1, \$V4034.2, \$V4034.3	%M58.W == 0x5	%V4408.0	
	Movimento_pv	Mem_pdl_bf	Memorie B-F per move ventose
	%V4032.0	(R) %V4408.1	
		Mem_pdl_cg	Memorie C-G per move ventose
		(R)	
		Mem_pdl_dh	Memorie D-H per move ventose
		(R)———(R)———— %V4408.3	

05 Label: Step:

App_setupa	goto(A) (T)
%V4034.0	
App_setupb	goto(B) (T)
%V4U34.1 App_setupc	goto(C)
] [(T)
App_setupd	goto(D)
%V4034.3	\-,'
	goto(VAI)(T)

Author:		NITIM	TOOLS	
Company:		INOM	тоопр	
Project: 1040_78.mch	TITRE		Date	28-02-2018
Module: START_PV.XLA		%SP214 (04)	Page	3



09 Label: D Step: Esc.

Copyright by...

Esclusione battute per movimento ventose in corso

X_pgm_d		Mem_pdl_dh	Memorie D-H per move ventose
%V531.3		%V4408.3	
X_pgm_h] [%V531.7	Abb_aree_cd] [%V1.1	Mem_pdl_cg () %V4408.2	Memorie C-G per move ventose
	Abb_aree_ad] [
	Abb_aree_ad] [%V1.2	Mem_pdl_ae () %V4408.0	Memorie A-E per move ventose
		Mem_pdl_bf ()	Memorie B-F per move ventose

10 Label: VAI Step:

Msg_184	Setup_a	V211_2.3		App_setupa	appoggio start setup area A
%V3037.0	%I5201.2	F_T- %V211.3		(R) %V4034.0	
	Setup_b			App_setupb	appoggio start setup area B
	%I5201.3			(R)	
	Setup_c			App_setupc	appoggio start setup area C
	%I5201.4	-		(R)	
	Setup_d			App_setupd	appoggio start setup area D
	%I5201.5	-		(R)	
	Ps_f2			Test_pgm = 0	
	%V202b.1			(T) %M48.W = 0x0	
				No_setup	bit setup non programmato
			L	(R)————————————————————————————————————	-

Author:		NTTM	TOOLS	2
Company:		NOM	TOOLS	•
Project: 1040_78.mch	TITRE		Date	28-02-2018
Module: START_PV.XLA		%SP214 (09)	Page	5

11 Label: Step: Start setup area A X pgm a (1) Raz_pv == 0 Emer_move == 0 Statoa_x == 8 (2) Setupa_x Setup area A _]>[_ _]>[_ _] > [___ -(S)-M58.W == 0x0%M46.W == 0x0 %V52e.3 %V531.0 %V518.B == 0x8 X pgm_e —][— %V531.4 X_setupa appoggio start setup area A App_setupa —(S)-—] [— %V1150.0 %V4034.0 Setupa_x Setup area A — (R)-%V52e.3 (1) %V5b4.0, %V5b4.4, %V5b5.0, %V5b5.4, %V4034.0, %V4034.1, %V4034.2, %V4034.3 : X_exec_a, X_next_a, X_next_e, App_setupa, App_setupb, App_setupc, App_setupd (2) %I5201.2, %I4101.4, %I4000.6 : Setup_a, Sel_man_aut, Emer_gen **12** Label: Step: Start setup area B X pgm b (1) $Raz_pv == 0$ Emer move == 0 Statob x == 8(2) Setupb_x Setup area B —] **>** [— —] > [— —] **>** [— —(S)— —][— %M58.W == 0x0 %M46.W == 0x0 %V519.B == 0x8 %V52e.4 %V531.1 X pgm f — 1 [— %V531.5 X_setupb App_setupb appoggio start setup area B —][— —(S)-%V1150.1 %V4034.1 Setupb x Setup area B

(1) %V5b4.1, %V5b4.5, %V5b5.1, %V5b5.5, %V4034.0, %V4034.1, %V4034.2, %V4034.3 : X_exec_b, X_exec_f, X_next_b, X_next_f, App_setupa, App_setupa, App_setupd

(2) %I5201.3, %I4101.4, %I4000.6 : Setup_b, Sel_man_aut, Emer_gen

Author:		NITIM	TOOLS	
Company:		NOM	тоопр	
Project: 1040_78.mch	TITRE		Date	28-02-2018
Module: START_PV.XLA		%SP214 (11)	Page	6

— (R)— %V52e.4 13 Label: Step: Start setup area C X pgm c (1) Raz_pv == 0 Emer_move == 0 Statoc_x == 8 (2) Setupc_x Setup area C _]>[_ _]>[_ __]>[__ -(S)-M58.W == 0x0%M46.W == 0x0 %V525.2 %V531.2 V51a.B == 0x8X pgm g —][— %V531.6 X_setupc appoggio start setup area C App_setupc —(S)— —] [— %V1150.7 %V4034.2 Setupc_x Setup area C — (R)-%V525.2 (1) %V5b4.2, %V5b4.6, %V5b5.2, %V5b5.6, %V4034.0, %V4034.1, %V4034.2, %V4034.3 : X_exec_c, X_exec_g, X_next_g, App_setupa, App_setupb, App_setupc, App_setupd (2) %I5201.4, %I4101.4, %I4000.6 : Setup_c, Sel_man_aut, Emer_gen **14** Label: Step: Start setup area D X pgm d (1) $Raz_pv == 0$ Emer move == 0 Statod x == 8(2) Setupd_x Setup area D —] **>** [— —] **>** [— —] **>** [— —(S)— —][— %M58.W == 0x0 %M46.W == 0x0 %V51b.B == 0x8 %V525.3 %V531.3 X pgm h — 1 [— %V531.7 X_setupd App_setupd appoggio start setup area D —][— —(S)-%V1151.0 %V4034.3 Setupd x Setup area D

- (1) %V5b4.3, %V5b5.3, %V5b5.7, %V4034.0, %V4034.1, %V4034.2, %V4034.3 : X_exec_d, X_exec_h, X_next_h, App_setupa, App_setupb, App_setupd
- (2) %I5201.5, %I4101.4, %I4000.6 : Setup_d, Sel_man_aut, Emer_gen

Copyright by...

15 Label: END Step:

Author:		NUM TOOLS		
Company:		INOM	1001	10
Project: 1040_78.mch	TITRE		Date	28-02-2018
Module: START_PV.XLA		%SP214 (13)	Page	7

— (R)— %V525.3