VISVESVARAYA TECHNOLOGICAL UNIVERSITY
"JnanaSangama", Belagavi-590018

FINAL YEAR PROJECT ZEROTH PHASE
REPORT ON
"AUTOMATIC LICENSE PLATE RECOGNITION SYSTEM"

Submitted in partial fulfillment of the requirements for the award of the degree of

BACHELOR OF ENGINEERING
IN
COMPUTER SCIENCE AND ENGINEERING

Submitted by

PRATEEK	**1KS15CS070**
PRITAM RAJ	**1KS15CS072**
SUNIL KUMAR SHARMA	**1KS15CS108**
VIKASH KUMAR SINGH	**1KS15CS120**

Department of Computer Science &Engineering
K.S.I.T.

Department of Computer Science &Engineering
K.S. INSTITUTE OF TECHNOLOGY
#14, Raghuvanahalli, Kanakapura Road, Bengaluru – 560109
2018-2019