Computer Network Lab

Exp 07: CRC Computatin

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Ex07 Resources

- References:
  - http://www.ross.net/crc/crcpaper.html
  - http://www.repairfaq.org/filipg/LINK/F_crc_v3.html
  - http://www.ross.net/crc/download/crc_v3.txt
    - –contains the program
  - –http://srecord.sourceforge.net/crc16-ccitt.html
  - –https://www.slideshare.net/sandeep101026/crc-java-code

Exp10 Description

- Program 07 (Java)
  - Write a program for error detecting code using CRC-CCITT.

Cyclic Concepts

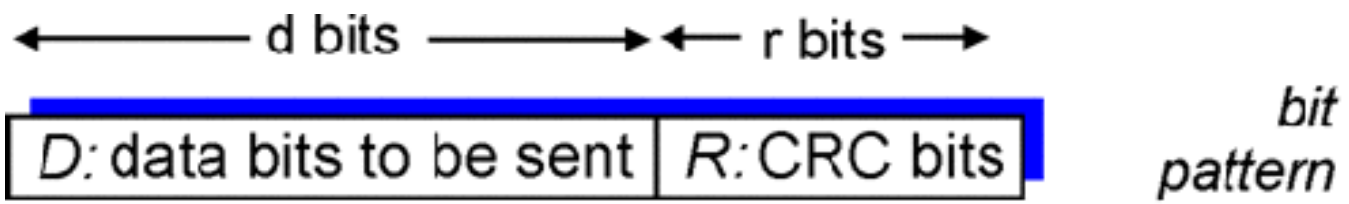
- CRC Codes known as polynomial codes
  - Each bit is taken as coefficient of polynomial
- Using module 2 i.e. bits `0,1`
  - Consider when we ignore carries or borrows
  - What would be difference between add & subtract
    - Can be achieved by XOR operation
    - Examples

```
1011 + 0101 = 1110
1011 - 0101 = 1110

1001 + 1101 = 0100
1001 - 1101 = 0100
```

Cyclic redundancy check

- More powerful error-detection coding
- View data bits, D, as a binary number
- Choose `r+1` bit pattern (generator), G
- Goal: choose `r` CRC bits, R, such that
  - <D,R> exactly divisible by G (modulo 2)
  - Receiver knows G, divides <D,R> by G. If non-zero remainder: error detected!
  - Can detect all burst errors less than `r+1` bits
- Widely used in practice (Ethernet, 802.11 WiFi, ATM)



$D * 2^r \quad XOR \quad R$

mathematical formula

CRC example

Want:

$D \cdot 2^r$ XOR $R = nG$

Equivalently:

$D \cdot 2^r = nG$ XOR R

Equivalently:

if we divide $D \cdot 2^r$ by G, want remainder R to satisfy:

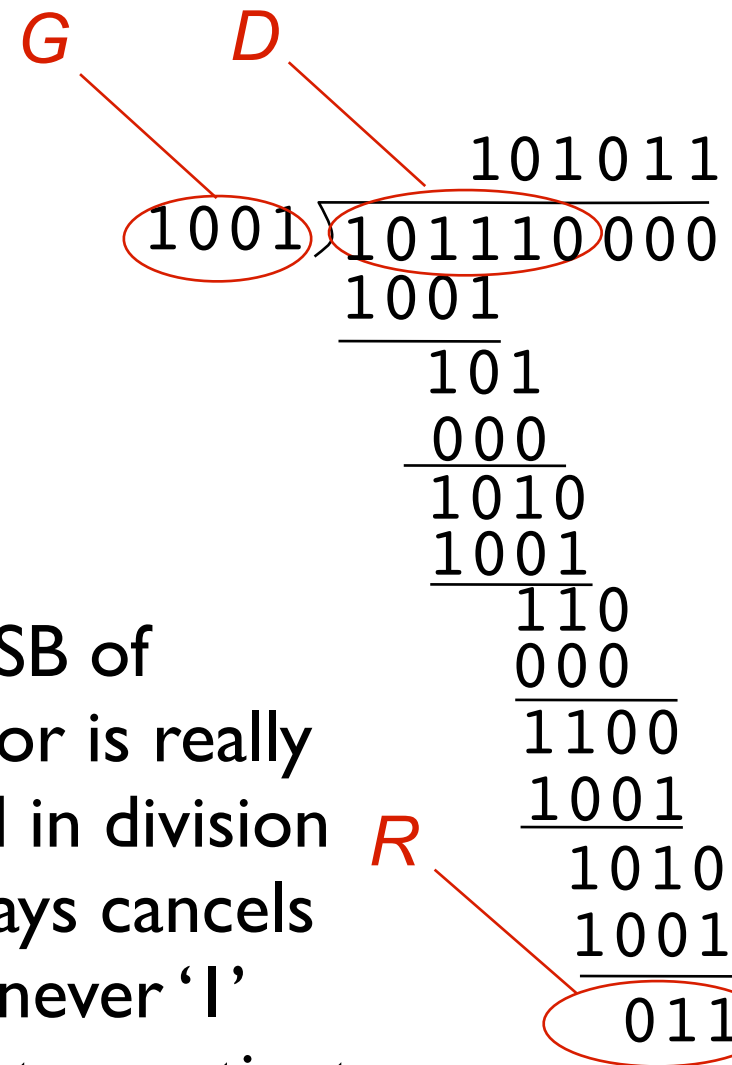
$$R = \text{remainder}\ \left[\frac{D \cdot 2^r}{G}\right]$$

Example:

D = 101110

G = 1001

r = 3



Note: MSB of Generator is really not used in division as it always cancels out whenever '1' is added to quotient

Cyclic redundancy check

- International CRC Standards defined for
  - 8, 12, 16 and 32 bit generators
  - 16 bit generator (CRC-CCITT)

    $x^{16} + x^{12} + x^5 + x^0$, i.e.

    1 0001 0000 0010 0001
    - Polynomial in actual computation
      - 0x1021
- CRC Detects bursts errors of less than r+1 bits
  - Consecutive error of r bits or fewer will be detected
  - Under some appropriate assumptions
    - burst of error >r+1 bits detected
  - Can detect any odd number of bit errors

Exercises

- Given
  - G=`10011` (CRC-4-ITU Standard)
  - D=`1010101010`
- Question:
  - What is the value of `R`
  - Divide `10011` into `1010101010 0000`
    - `R = 0100`

Lab Program:

- Lab program expectation:
  -

Program Template:

- Read message from command line input