RITURAJ RAMAN

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Embedded Electronics Professional with 4+ years of experience on Linux based systems, with hands-on experience on full product development cycle of embedded systems from development to integration and testing on ARM-based microcontrollers and processors for Automotive and Defence Industries. Passionate about developing robust firmware/software for embedded systems.

SKILLS:

- C, C++, Python
- Experienced in use of oscilloscope and logic analyzer for debugging
- UART, SPI, I2C protocols
- Energia (TIVA C series Launchpad); Arduino IDE (Arduino)
- Linux, QNX, Greenhills Integrity
- Data structures and algorithms
- GNU GDB; GNU Make; Valgrind

WORK EXPERIENCE:

Qualcomm (Mar 2018- Present)

A. Designation: Senior Engineer, Automotive graphics
 B. Designation: Engineer, Automotive graphics
 Jan 2020- Present
 Mar 2018-Dec 2019

- POC for the graphics module for RTOS systems with Snapdragon SoC Chipsets, working on C/C++
- Working on OpenGL APIs and user mode drivers for the graphics module on Linux, Android and RTOS systems
- Worked closely with customer team to work on software design and architecture
- Collaborated with testing and platform teams for development and debugging
- Developed and enhanced testing scripts (bash and python) for more efficient testing, helping to avoid regression errors.
- Recognized for individual and team contributions in multiple successful customer releases

Tonbo Imaging Pvt Ltd

(Jan 2015- Mar 2018)

A. Designation: Member of Technical Staff

July 2015-Mar 2018

- As a firmware lead, was responsible for development of a complete fusion system (day and thermal camera)
 with geolocation
- Worked on developing Linux based systems for thermal and fusion based systems with UART, I2C and SPI sensors for defence applications including kernel level development and debugging. For this, worked primarily on C/C++
- Designed a testing setup for SPI, I2C and UART sensors on a TIVA TM4C123GXL board using energia.
- B. Designation: Embedded Electronics Intern

Jan 2015-June 2015

- Worked on complete system integration for a fusion system with geolocation and pan-tilt
- Worked on Das U-Boot for modifying registers in TI processors for configuring initial state of GPIOs

PUBLICATION

A microwave dielectric material for patch antenna substrate for WLAN applications http://www.hindawi.com/journals/ijmst/2014/703181/

(Oct 2014)

Analysed various materials for their efficiency as a substrate in microstrip patch antennas for WLAN
applications. The multiple attribute decision making approach was used for this purpose.

ACADEMIC QUALIFICATIONS:			
Bachelor of Engineering (Hons) (Electronics and Instrumentation)	2015	Birla Institute of Technology and Science, Pilani	CGPA: 8.61/10.0
		(BITS, Pilani)	

ACHIEVEMENTS:

- **Silver Medal, Powerlifting**: Won the Silver medal in full powerlifting qualifiers (South India) by World powerlifting Federation, India chapter in 2019
- **Silver Medal, Powerlifting**: Won the Silver medal in Powerlifting event in BITS Open Sports Meet in 2014.