J@LC JLCDFM

RagulRaj101.zip

Units: Metric (cm/mm)

PCB DFM	SMT DFM

DFM check

Description | All

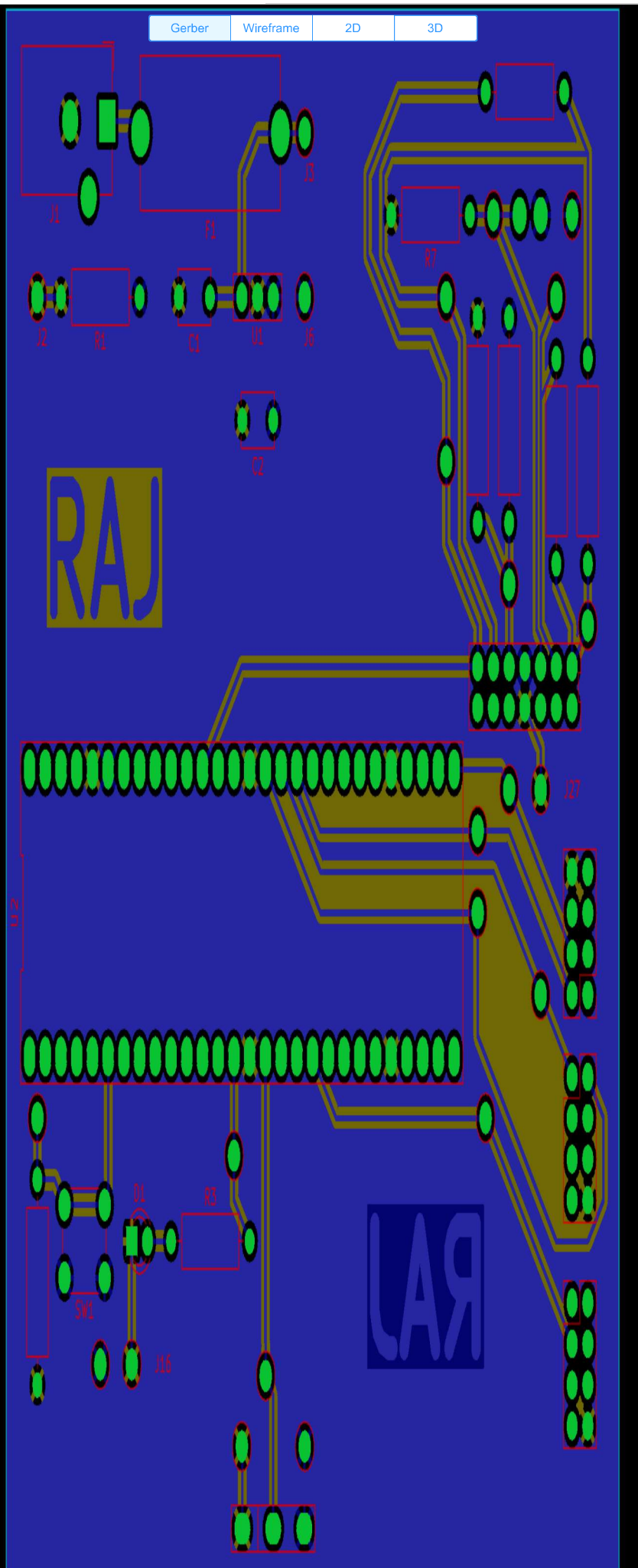| Analyze project	Statistics	Operat
Routing layer analysis		
Sharp trace corner	0, 2, 0	Details
BGA pad	0, 0, 0	Details
Via placed within a pad	0, 0, 0	Details
Trace to board edge	0, 0, 0	Details
Trace spacing	0, 0, 0	Details
Unconnected trace end	0, 10, 0	Details
Trace width	0, 0, 0	Details
Fiducial	0, 0, 0	Details
Pad to board edge	0, 0, 0	Details
Pad spacing	0, 0, 0	Details
Plated through-hole to trace clearance	0, 0, 0	Details
Annular ring	0, 0, 0	Details
tht to smd	0, 0, 0	Details
Via to pad	0, 0, 0	Details
Soldermask layer analysis		
Soldermask bridge	0, 0, 0	Details
Solder mask opening exposing trace	0, 0, 0	Details
Soldermask opening with multiple segments	0, 0, 0	Details
Negative soldermask expansion	0, 0, 0	Details
Silkscreen layer analysis		
Silkscreen to pad	4, 6, 28	Details
Silkscreen to hole	0, 0, 0	Details
Silkscreen line width	0, 50, 0	Details
Drill layer analysis		
Unconnected via	Unanalyzed	Details
Missing plated through-hole	Unanalyzed	Details
Unconnected via	Unanalyzed	Details
Plated through-hole spacing	Unanalyzed	Details
Short slot detection	Unanalyzed	Details
Slot width check	Unanalyzed	Details
Via to PTH spacing	Unanalyzed	Details
Unconnected via	Unanalyzed	Details

Gerber	Wireframe	2D	3D

◢ Show layer controls

	Edit	Layer type
👁		Top silkscreen
👁		Top soldermask
👁		Top copper
👁		Bottom copper
👁		Bottom soldermask
👁		Board outline layer

X: 207.95mm   Y: -99.29mm