

ViperBoard User Guide

**Nano River Technologies
October 2010**

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Congratulations on the purchase of the ViperBoard from Nano River Technologies! We trust that this board will be a valuable asset in the development or test of your own electronics.

... Nano River Technologies development team

ABBREVIATIONS

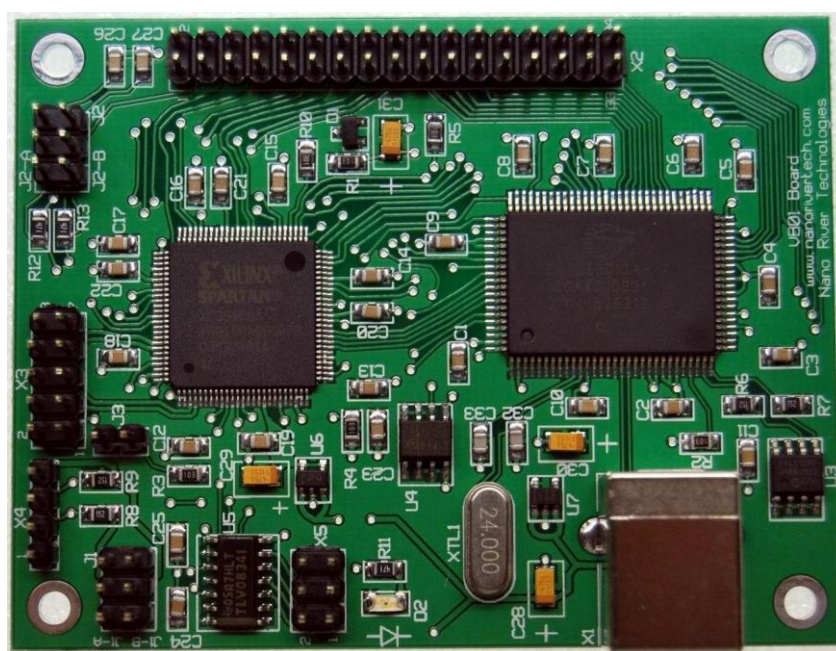
API	Application Programming Interface
EEPROM	Electrically Erasable Programmable Read Only Memory
G++	C++ compiler for Linux (part of GCC)
GCC	GNU Compiler Collection
GPIO	General Purpose IO
GPIOA	ViperBoard GPIO Port A (advanced GPIO interface)
GPIOB	ViperBoard GPIO Port B (digital IO interface)
GUI	Graphical User Interface
I2C	Inter-Integrated Circuit
IIC	Inter-Integrated Circuit (same as I2C)
IO	Input / Output
LED	Light Emitting Diode
Master	An interface which supplies the clock like the SPI master or I2C master on ViperBoard
NRT	Nano River Technologies
Slave	An interface which receives the clock like the SPI slave or I2C slave on ViperBoard
SPI	Serial Peripheral Interface
USB	Universal Serial Bus

1. Preliminaries

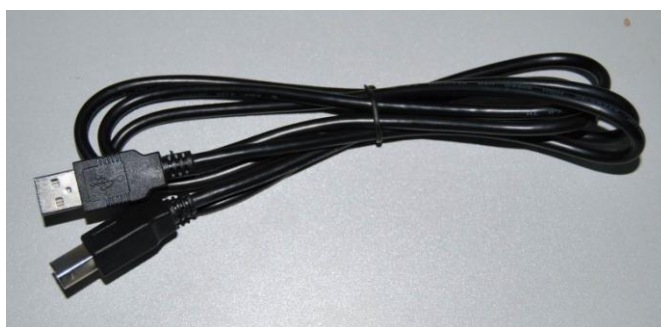
1.1. Packaging List

Your ViperBoard comes standard with the following items.

- One ViperBoard circuit board assembly



- One USB cable – type A to type B



PC software, example applications and documentation for use with this product is available downloadable from our web-site.

www.nanorivertech.com

1.2. Getting Help

Most questions relating to installation and usage of the ViperBoard are available from our web-site.

www.nanorivertech.com

If however you have further questions, then please do not hesitate to contact us via email.

support@nanorivertech.com

For any sales related questions, please contact:

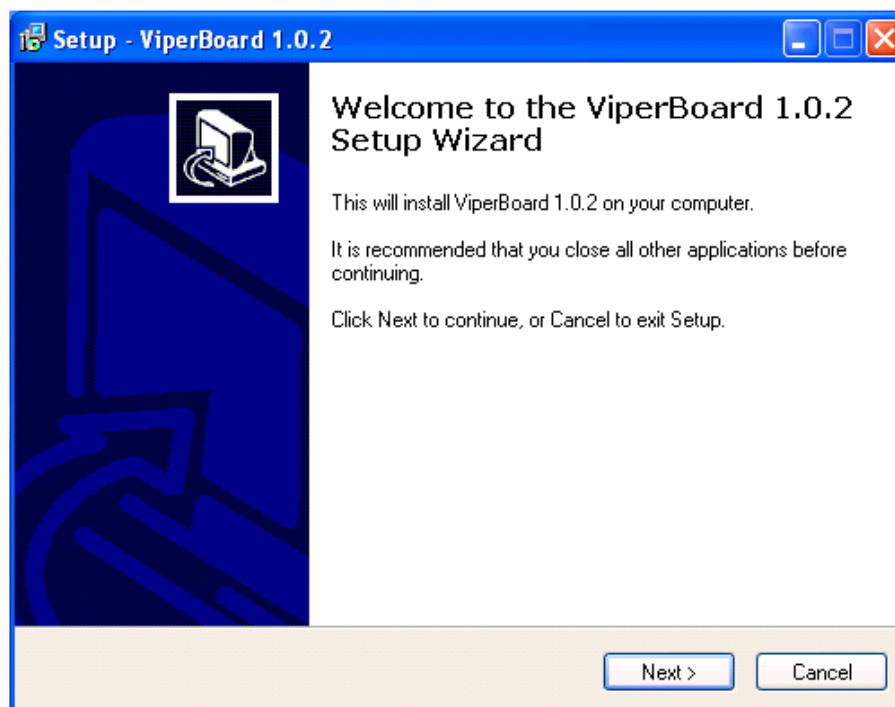
sales@nanorivertech.com

2. Installation (Windows 2000 and XP)

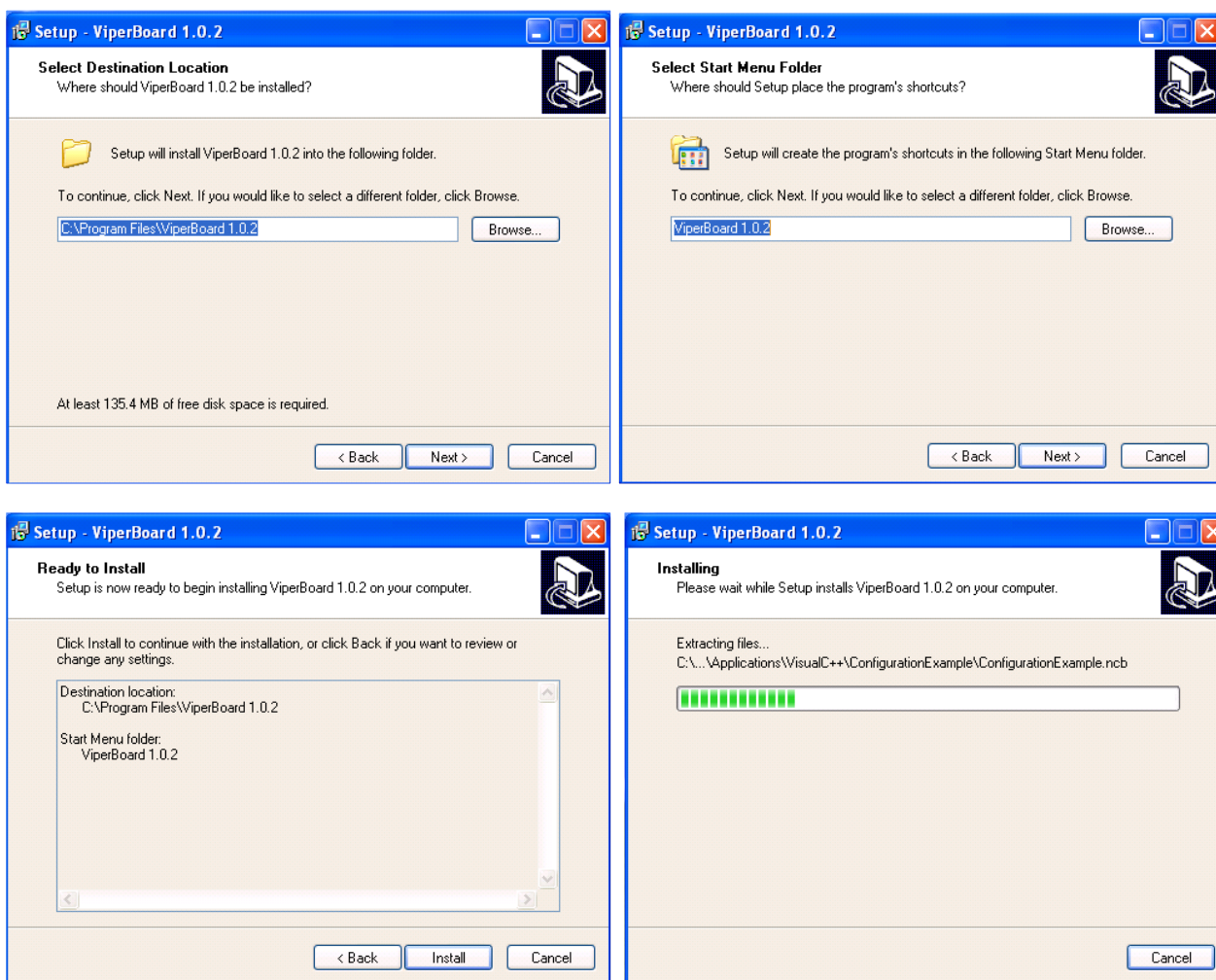
2.1. ViperBoard Installer

The ViperBoard installer activates from the software download page available from:

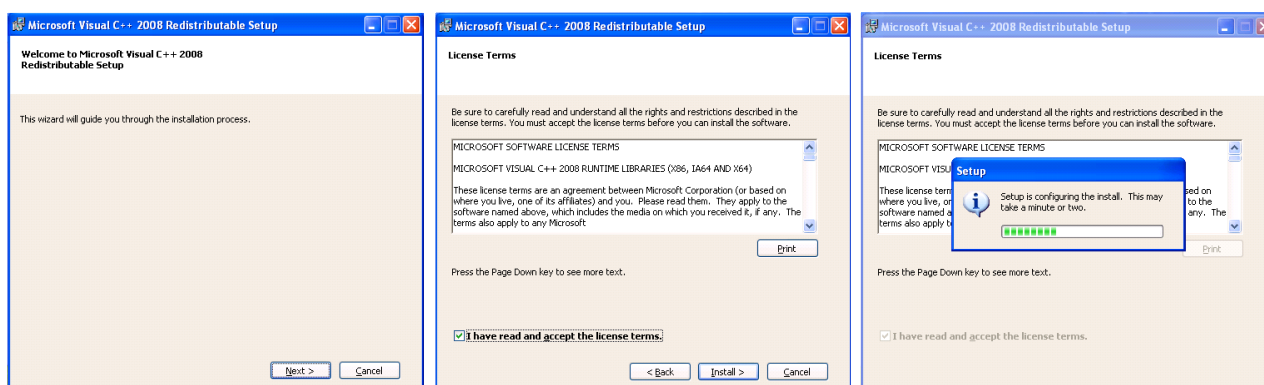
www.nanorivertech.com



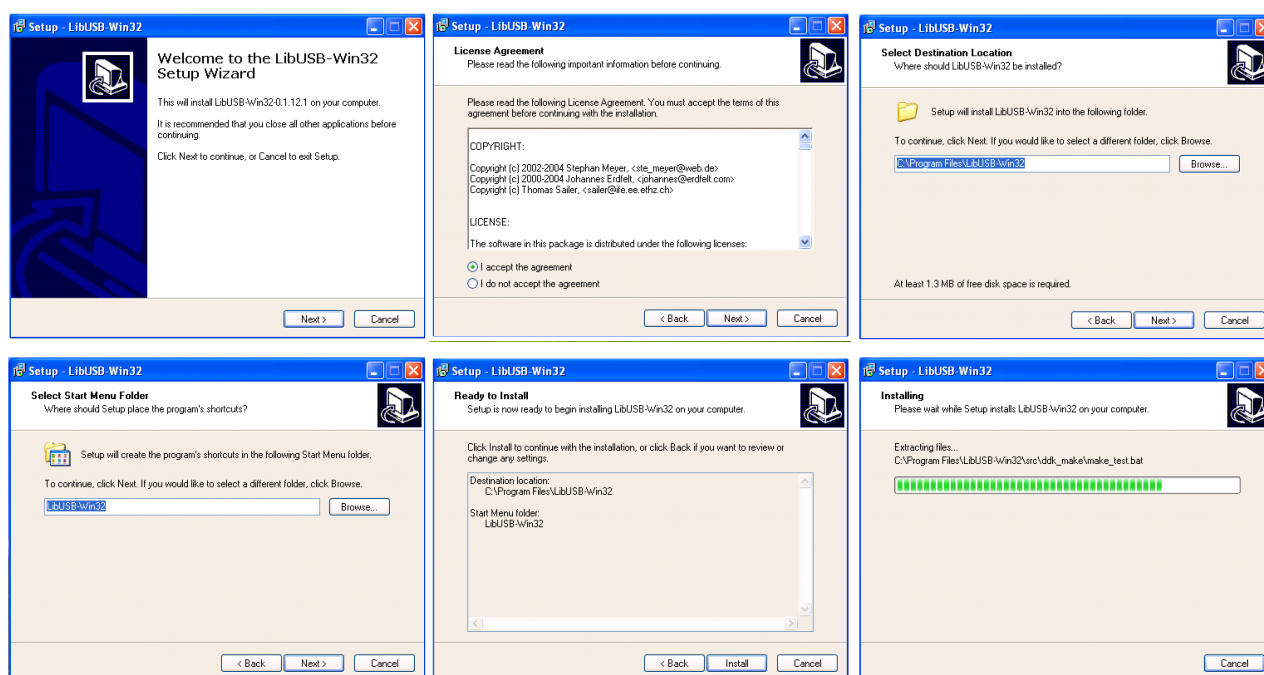
The installer starts by downloading all ViperBoard documentation and software examples.



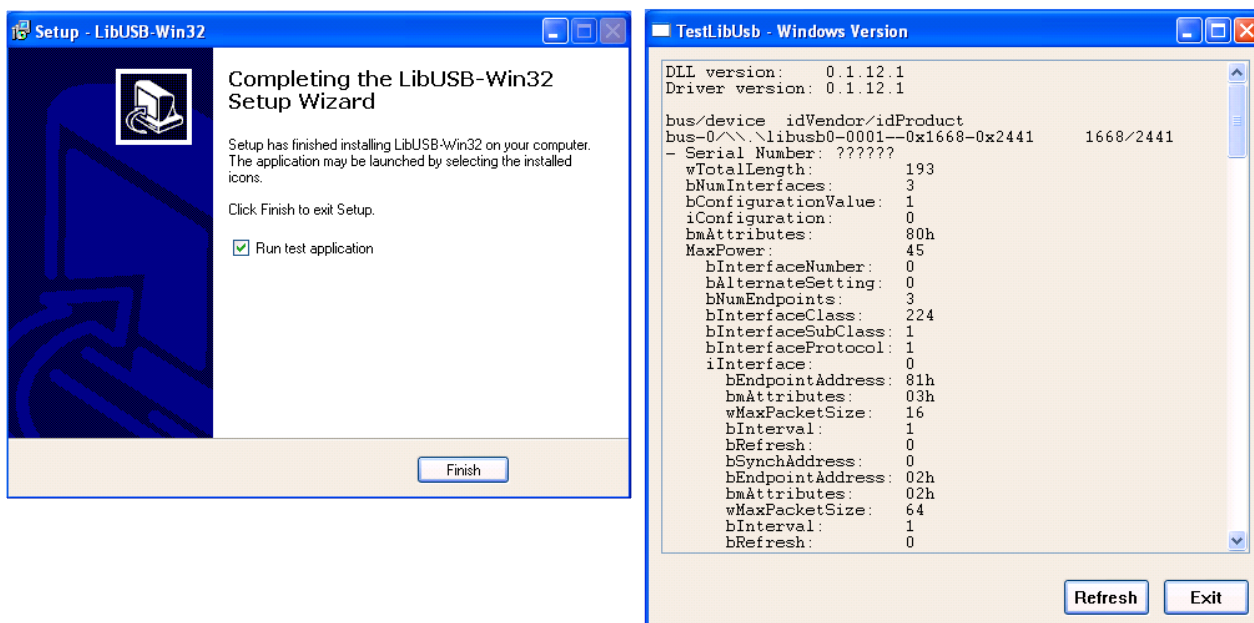
To run the applications without MicroSoft Visual C++ installed the Microsoft Visual C++ 2008 redistribution pack is required. This is installed automatically next. Allow all default directories and file names.



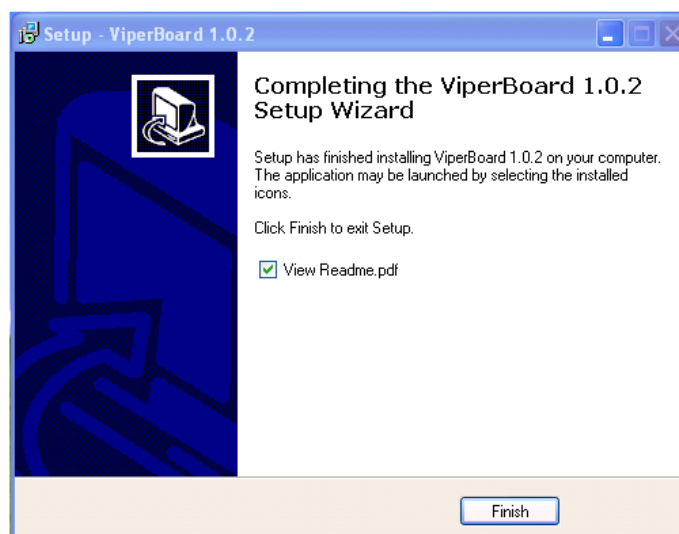
Next the open source LibUSB-Win USB driver is installed. Allow all default directories and file names.



At the conclusion of the LibUSB-Win USB driver installation there will be the opportunity to run a test program to ensure it installed properly. Running this is optional.



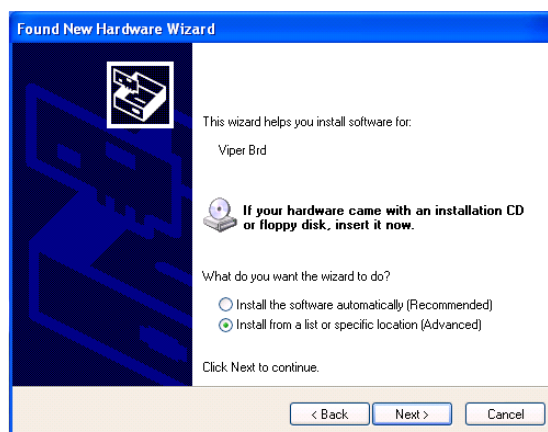
At the conclusion of the driver install, a Readme file will be displayed containing an overview of the ViperBoard product.



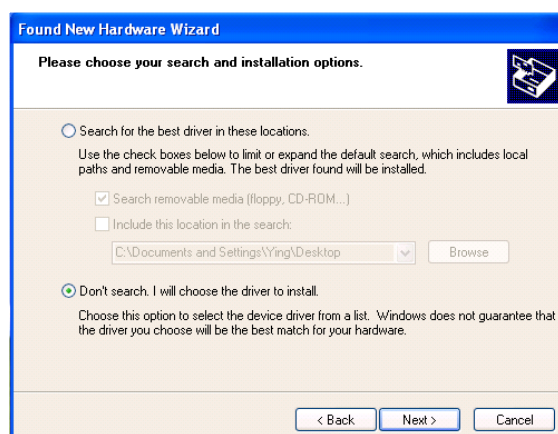
2.2. Plugging in the ViperBoard

When the driver is installed and the ViperBoard plugged into a USB port for the first time, the “Found New Hardware” wizard will appear.

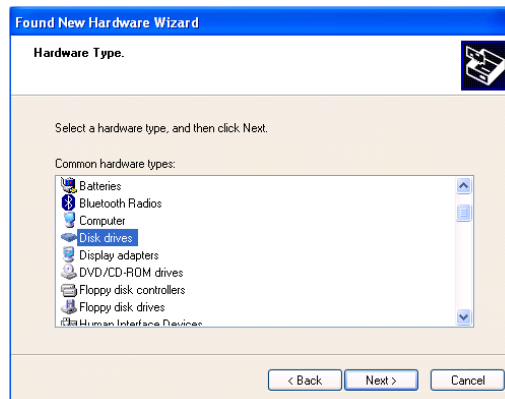
Select “Install from a list or specific location”.



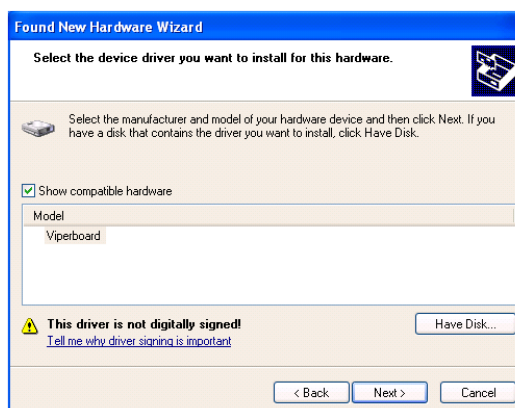
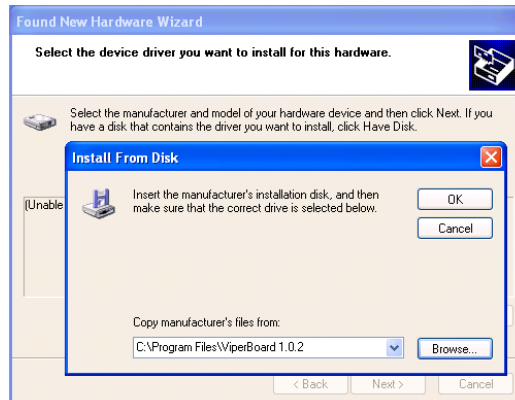
Select “Don’t search. I will choose the driver to install”.



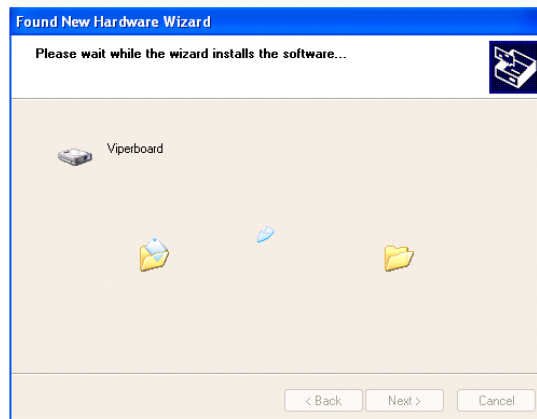
Navigate to the hard disk.



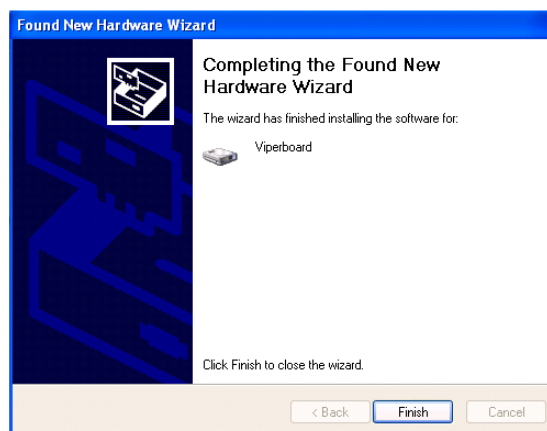
Navigate to the install directory for ViperBoard eg `C:\Program Files\ViperBoardInstaller_1.0.2.`



The Wizard will then find the driver setup file and start to link to the ViperBoard driver.



Installation is then complete. The PC can now talk to a ViperBoard.



3. Installation (Linux)

Installation of ViperBoard under Linux consists of two steps. First OpenSource LibUsb for Linux is installed. Secondly the ViperBoard example applications and documentation is installed.

3.1. Step 1: LibUsb Installation

LibUsb is an OpenSource USB driver available for usage for commercial applications. The ViperBoard class library will make low level USB calls to LibUsb to implement the desired high level functions available through the ViperBoard API.

The LibUsb installation is contained within "libusb-0.1.12.tar.gz".

Installation consists of the following:

a) One should first ensure that GNU g++ is available on the Linux machine. This will be needed during the installation.

b) In a working directory untar the file using
`linux%> gzip -cd libusb-0.1.12.tar.gz | tar xvf -`

c) Move to the created directory using
`linux%> cd libusb-0.1.12`

d) Run the configure script
`linux%> ./configure`

e) Run make (become root if necessary) to build LibUsb
`linux%> make`

f) Install LibUsb
`linux%> make install`

At this point you should have installed LibUsb for Linux. Unfortunately in most cases it seems that the library can only be seen by the root user. This means that applications would only work as root. To fix this one needs to edit one of the rule files in UDEV.

g) Change to be root

```
linux%> sudo bash
```

h) Edit for example your `/etc/udev/rules.d/40-basic-permissions.rules` to include the following two new lines (in red). This makes the ViperBoard USB connection able to be seen by someone other than root.

```
# USB devices (usbfs replacement)
SUBSYSTEM=="usb", ENV{DEVTYPE}=="usb_device", MODE="0664"
SUBSYSTEM=="usb_device", MODE="0664"
#New lines for Viperboard
SUBSYSTEM=="usb", ENV{DEVTYPE}=="usb_device",SYSFS{idVendor}=="04b4",
SYSFS{idProduct}=="1004", MODE="0666"
SUBSYSTEM=="usb", ENV{DEVTYPE}=="usb_device",SYSFS{idVendor}=="2058",
SYSFS{idProduct}=="1005", MODE="0666"
```

The above change to permissions shows how to change USB permissions for Ubuntu Linux. For other kernels permissions may need to be changed in other files. For example in Fedora 11, one needs to make the modification to `/lib/udev/rules.d/50-udev-default.rules`.

i) You can return from being root now and you are ready to install the ViperBoard examples and documentation.

3.2. Step 2: ViperBoard Examples and Documentation Installation

a) Documents and the example applications are all contained in ViperBoard_Installation_1.0.2.tar.gz. Untar this in some suitable place.

```
linux%> tar xvfz ViperBoard_Installation_1.0.2.tar.gz
```

b) Move to the created directory.

```
linux%> cd ViperBoard_Installation_1.0.2
```

c) You will see the following documents to read:

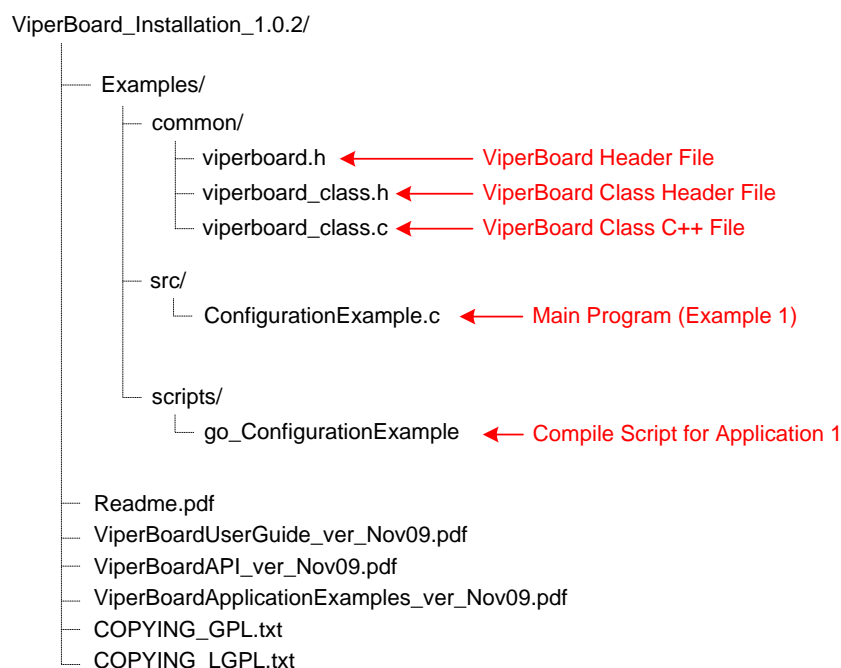
Readme.pdf

ViperBoardUserGuide_ver_Nov09.pdf

ViperBoardAPI_ver_Nov09.pdf

ViperBoardApplicationExamples_ver_Nov09.pdf

d) You will find the application examples in the Examples/ directory. Read **ViperBoardApplicationExamples_ver_Nov09.pdf** to learn about running these. The file structure should appear as below.



4. Installation (MacOS)

Installation of ViperBoard for MacOS consists of two steps. First OpenSource LibUsb for MacOS is installed. Secondly the ViperBoard example applications and documentation is installed.

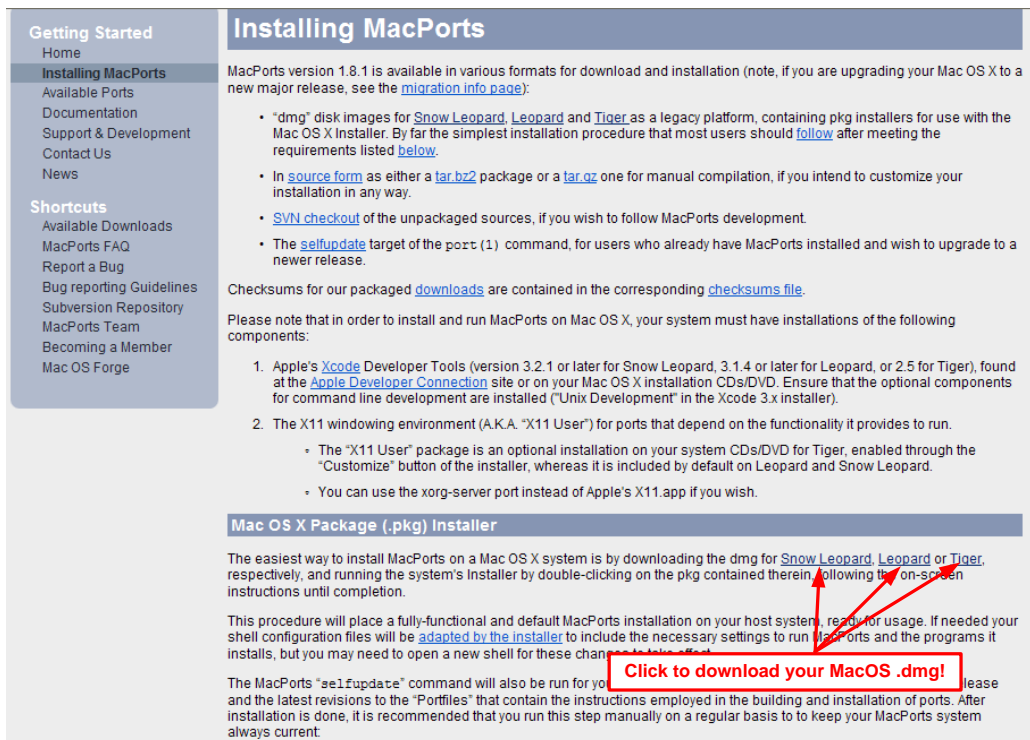
4.1. Step 1: LibUsb Installation

LibUSB 0.1.12 must be first installed under MacOS. The simplest way to achieve this is to make use of the installation already included in MacPorts.

First visit the MacPorts installation page:

<http://www.macports.org/install.php>

Download the MacPorts installation for your version of MacOS by selecting Snow Leopard, Leopard or Tiger. A .dmg file will be downloaded (it will be called something like `MacPorts-1.8.1-10.6-SnowLeopard.dmg`).



Getting Started
Home
Installing MacPorts
Available Ports
Documentation
Support & Development
Contact Us
News

Shortcuts
Available Downloads
MacPorts FAQ
Report a Bug
Bug reporting Guidelines
Subversion Repository
MacPorts Team
Becoming a Member
Mac OS Forge

Installing MacPorts

MacPorts version 1.8.1 is available in various formats for download and installation (note, if you are upgrading your Mac OS X to a new major release, see the [migration info page](#)):

- "dmg" disk images for [Snow Leopard](#), [Leopard](#) and [Tiger](#) as a legacy platform, containing pkg installers for use with the Mac OS X Installer. By far the simplest installation procedure that most users should [follow](#) after meeting the requirements listed [below](#).
- In [source form](#) as either a [tar.bz2](#) package or a [tar.gz](#) one for manual compilation, if you intend to customize your installation in any way.
- [SVN checkout](#) of the unpackaged sources, if you wish to follow MacPorts development.
- The [selfupdate](#) target of the `port (1)` command, for users who already have MacPorts installed and wish to upgrade to a newer release.

Checksums for our packaged [downloads](#) are contained in the corresponding [checksums file](#).

Please note that in order to install and run MacPorts on Mac OS X, your system must have installations of the following components:

1. Apple's [Xcode Developer Tools](#) (version 3.2.1 or later for Snow Leopard, 3.1.4 or later for Leopard, or 2.5 for Tiger), found at the [Apple Developer Connection](#) site or on your Mac OS X installation CDs/DVD. Ensure that the optional components for command line development are installed ("Unix Development" in the Xcode 3.x installer).
2. The X11 windowing environment (A.K.A. "X11 User") for ports that depend on the functionality it provides to run.
 - The "X11 User" package is an optional installation on your system CDs/DVD for Tiger, enabled through the "Customize" button of the installer, whereas it is included by default on Leopard and Snow Leopard.
 - You can use the xorg-server port instead of Apple's X11.app if you wish.

Mac OS X Package (.pkg) Installer

The easiest way to install MacPorts on a Mac OS X system is by downloading the dmg for [Snow Leopard](#), [Leopard](#) or [Tiger](#), respectively, and running the system's Installer by double-clicking on the pkg contained therein, following the on-screen instructions until completion.

This procedure will place a fully-functional and default MacPorts installation on your host system, ready for usage. If needed your shell configuration files will be [adapted by the installer](#) to include the necessary settings to run MacPorts and the programs it installs, but you may need to open a new shell for these changes to take effect.

The MacPorts `selfupdate` command will also be run for you after installation. Please see the [FAQ](#) for more details. After installation is done, it is recommended that you run this step manually on a regular basis to keep your MacPorts system always current.

Click to download your MacOS .dmg!

Double click on the downloaded file to mount it and execute the package file (.pkg) to install it.

In a command tool install version 0.1.12 of LibUsb using the following command:

```
% sudo port install libusb-legacy
```

At this point you should have installed LibUsb for MacOS and you are ready to install the ViperBoard examples and documentation.

4.2. Step 2: ViperBoard Examples and Documentation Installation

a) Documents and the example applications are all contained in ViperBoard_Installation_1.0.2.tar.gz which comes from the ViperBoard software downloads page for MacOS. Untar this in some suitable place.

```
macos %> tar xvfz ViperBoard_Installation_1.0.2.tar.gz
```

b) Move to the created directory.

```
macos %> cd ViperBoard_Installation_1.0.2
```

c) You will see the following documents to read:

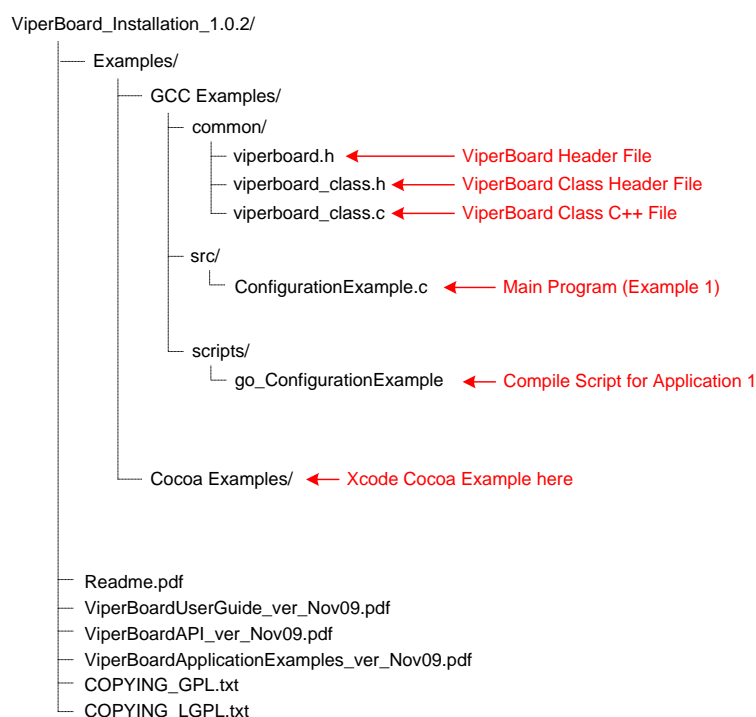
Readme.pdf

ViperBoardUserGuide_ver_Nov09.pdf

ViperBoardAPI_ver_Nov09.pdf

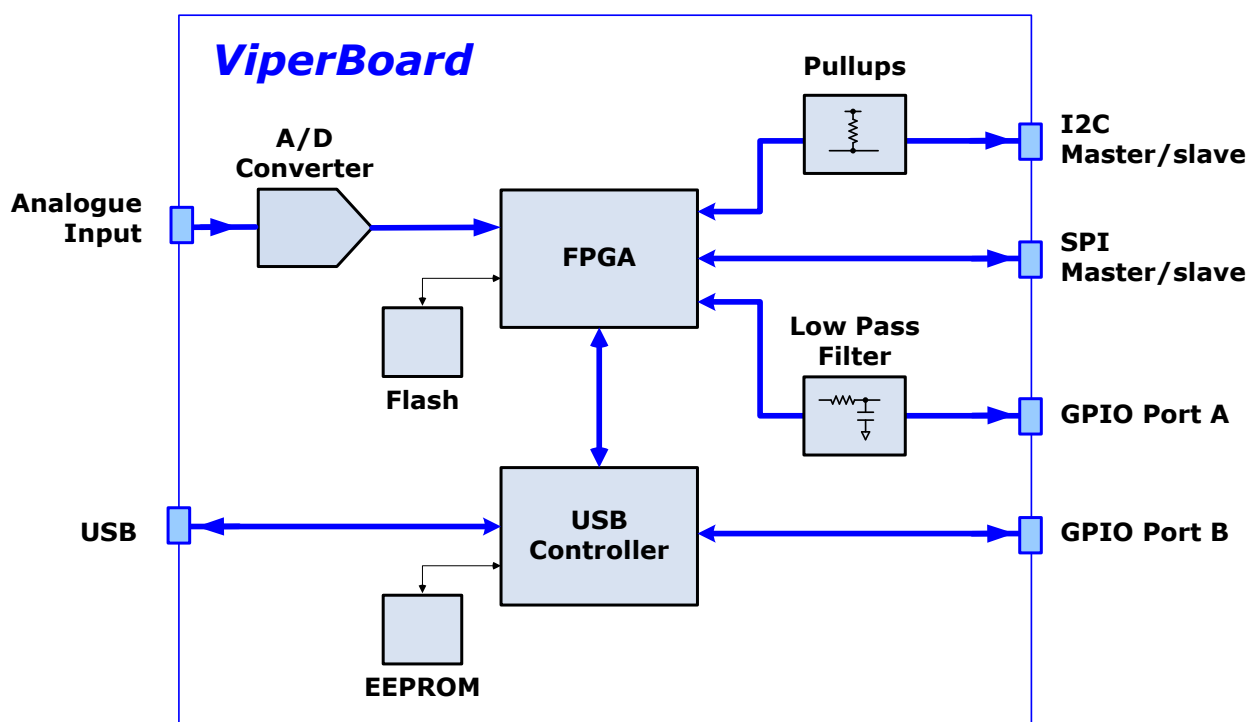
ViperBoardApplicationExamples_ver_Nov09.pdf

d) You will find the application examples in the Examples/ directory. Read **ViperBoardApplicationExamples_ver_Nov09.pdf** to learn about running these. The file structure should appear as below.



5. ViperBoard Hardware

5.1. Overview



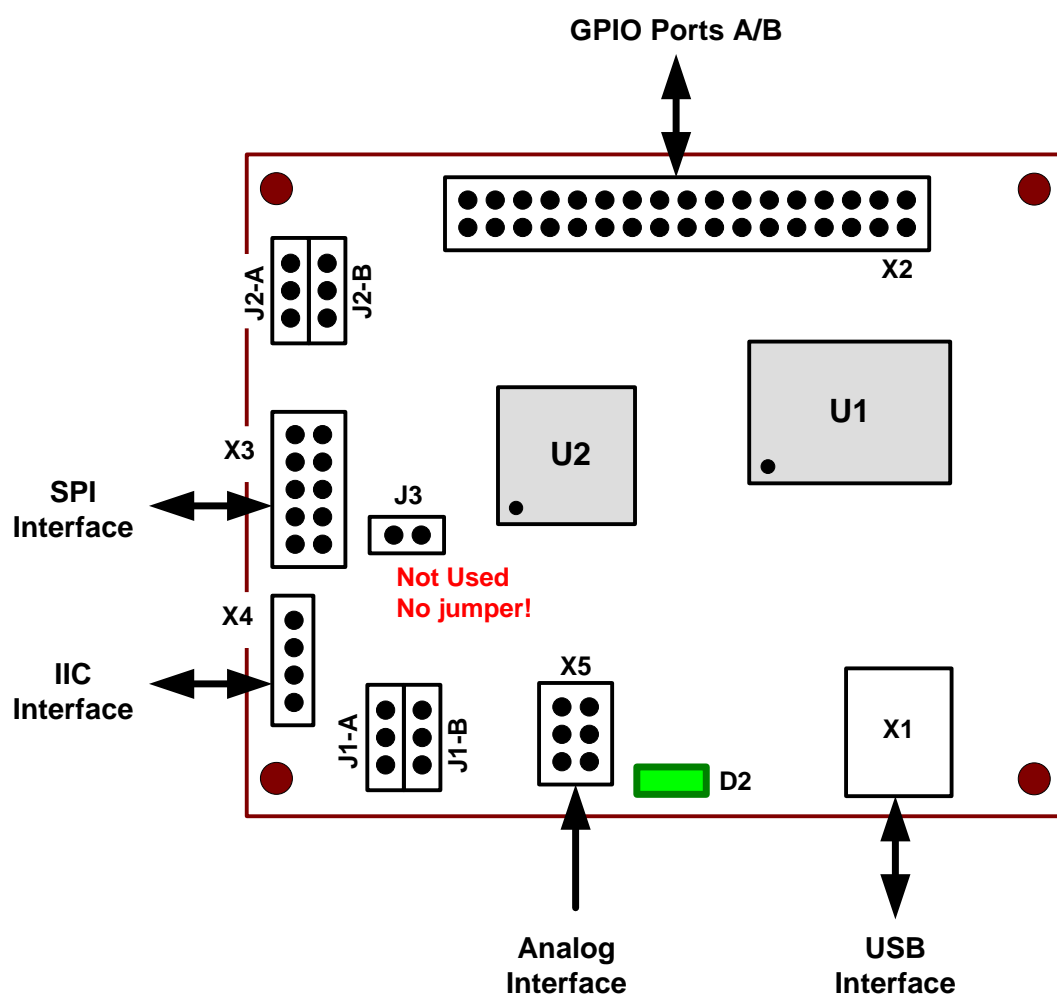
Viperboard is built around a Cypress USB controller chip and FPGA. It accepts commands across the USB bus and then translates these into I2C, SPI and GPIO bus protocols as required. Key features of the Viperboard include:

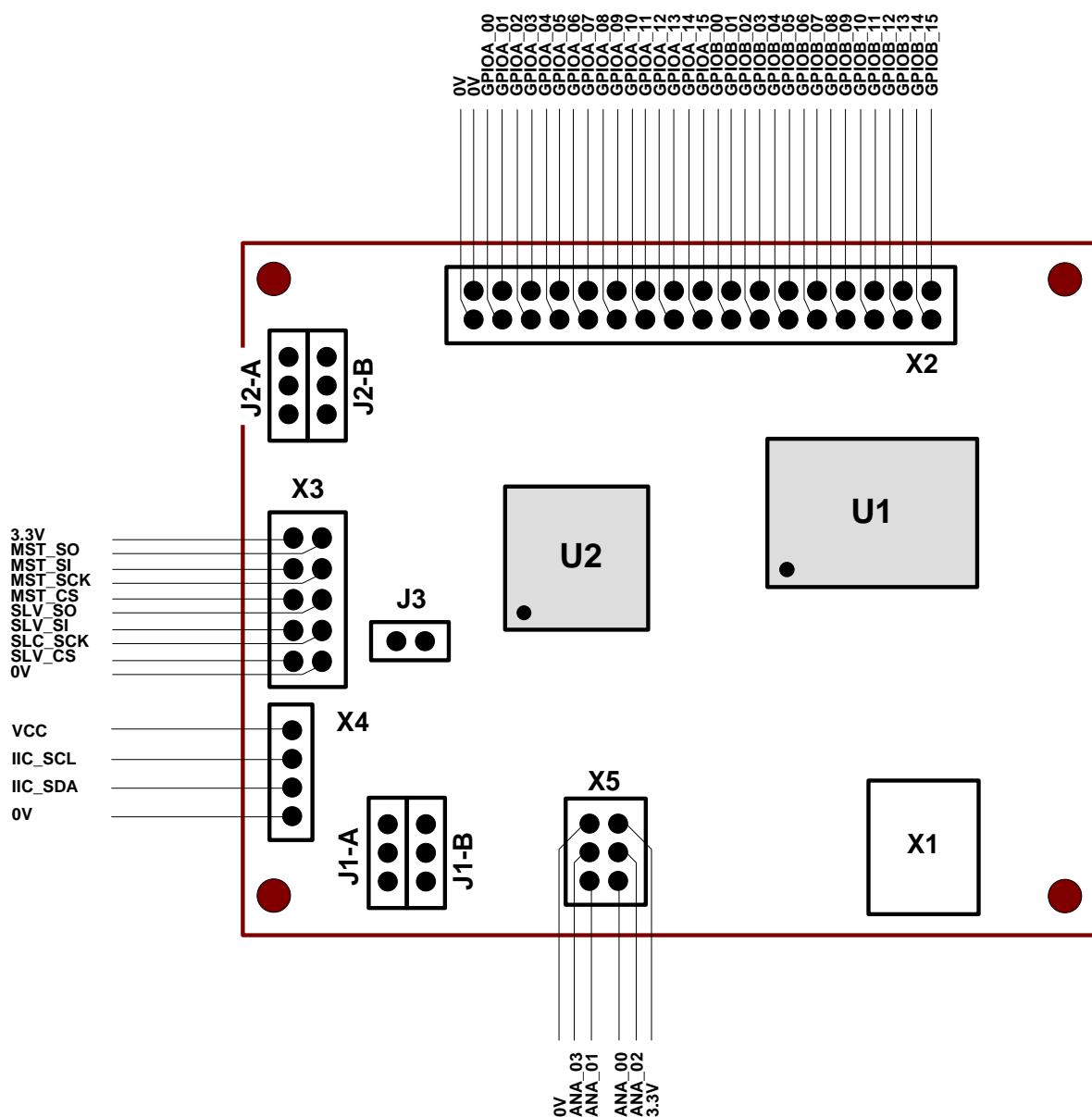
- high speed SPI master and slave (up to 17 channels)
- high speed I2C master and slave with optional pull-ups
- 16 bit GPIO Port A capable of digital I/O, interrupts, PWM
- optional low pass filtering for 2 GPIO Port A pins for analogue output
- 16 bit GPIO Port B capable of digital I/O
- 4 bit analogue input

A full schematic for the Viperboard can be seen in Appendix A.

5.2. Interfaces

Separate connectors are provided for each of the I2C, SPI, GPIO and Analogue interfaces. With pins as specified in the following diagrams.





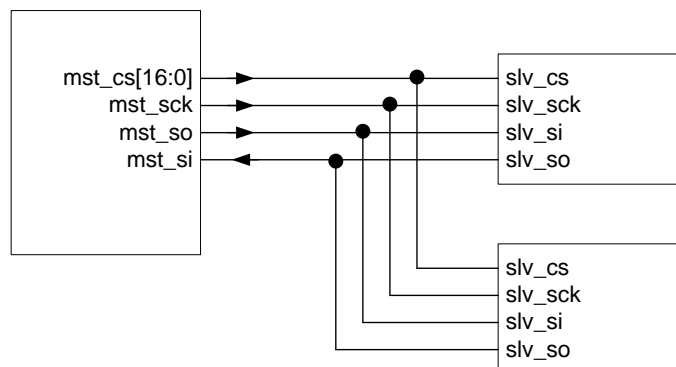
5.3. SPI Interface

Connector X3 contains the SPI interface.

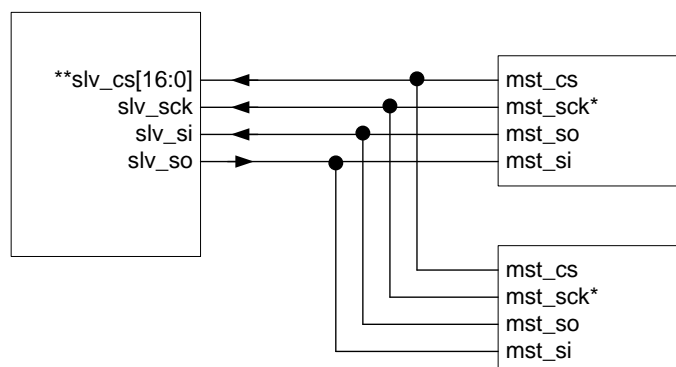
MST_CS[0]	Output	SPI master chip select output (channel 0).
MST_SCK	Output	SPI master serial clock output.
MST_SO	Output	SPI master serial data output.
MST_SI	Input	SPI master serial data input.
SLV_CS[0]	Input	SPI slave chip select input (channel 0).
SLV_SCK	Input	SPI slave serial clock input.
SLV_SO	Output	SPI slave serial data output.
SLV_SI	Input	SPI slave serial data input.
3.3V	Power	Supply
0V	Power	Ground

The interface includes separate master and slave SPI interfaces. Pins of the GPIOA port can be sacrificed to either SPI master or SPI slave chip selects. These then form the remaining bits of the chip select buses `mst_cs[16:0]` or `slv_cs[16:0]`. In general three usages of the SPI interface become possible, as shown over:

Master Mode



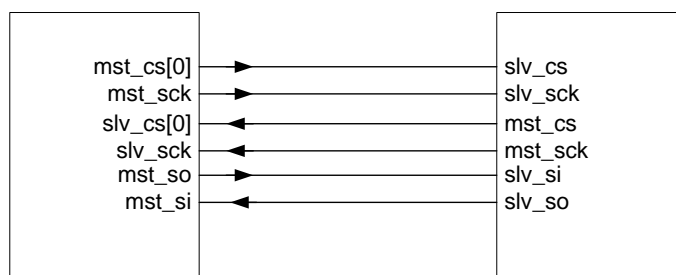
Slave Mode



*) When connecting multiple slaves, care needs to be taken to make sure the clock outputs can be connected. For example they could be externally gated or made open drain.

**) Always make sure that for channels configured to be SPI slaves always have their chip select inputs driven and not left floating.

Hybrid Master-Slave Mode

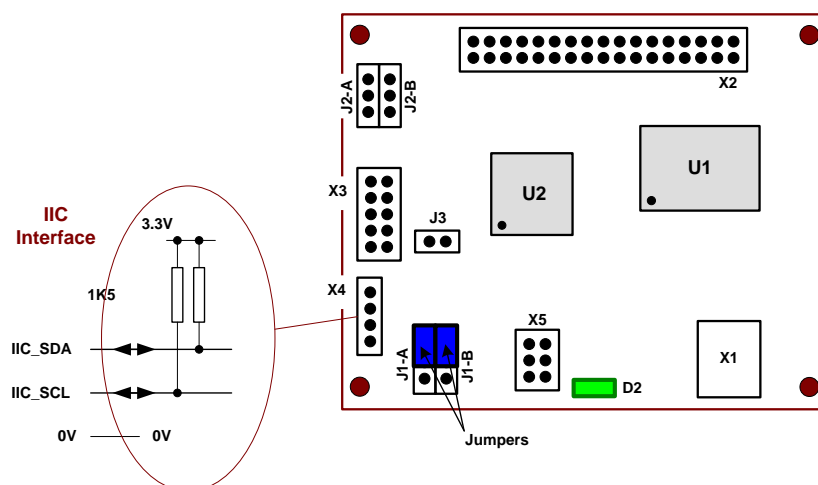
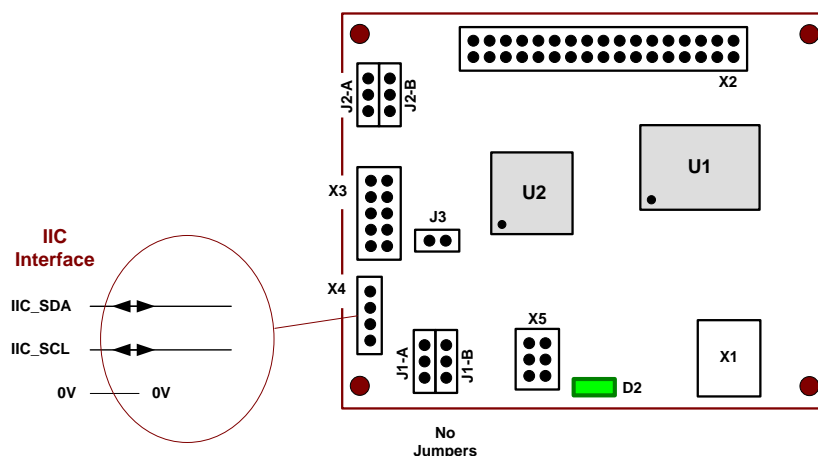
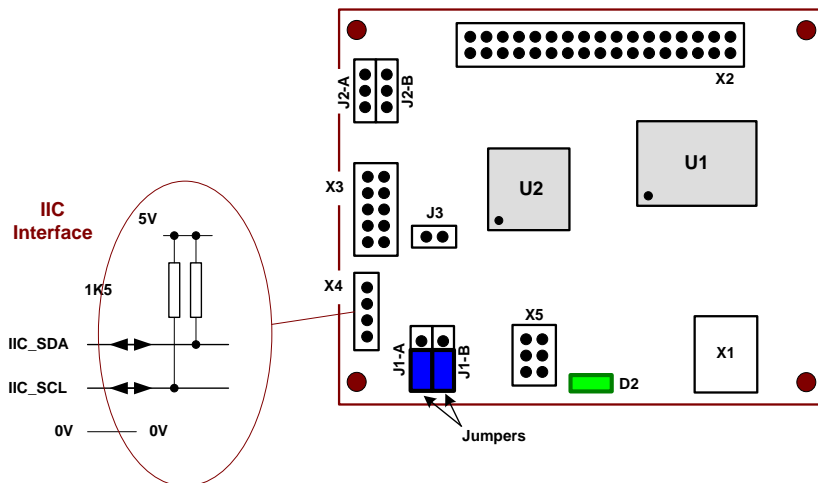


5.4. IIC Interface

Connector X4 contains the I2C interface.

IIC_SCL	Input/Output	IIC serial clock.
IIC_SDA	Input/Output	IIC serial clock.

Configuration jumpers J1-A and J1-B can be used to switch in/out on-board pull-up resistors for the I2C lines up to either 3.3 volts or 5 volts as per the following drawings.



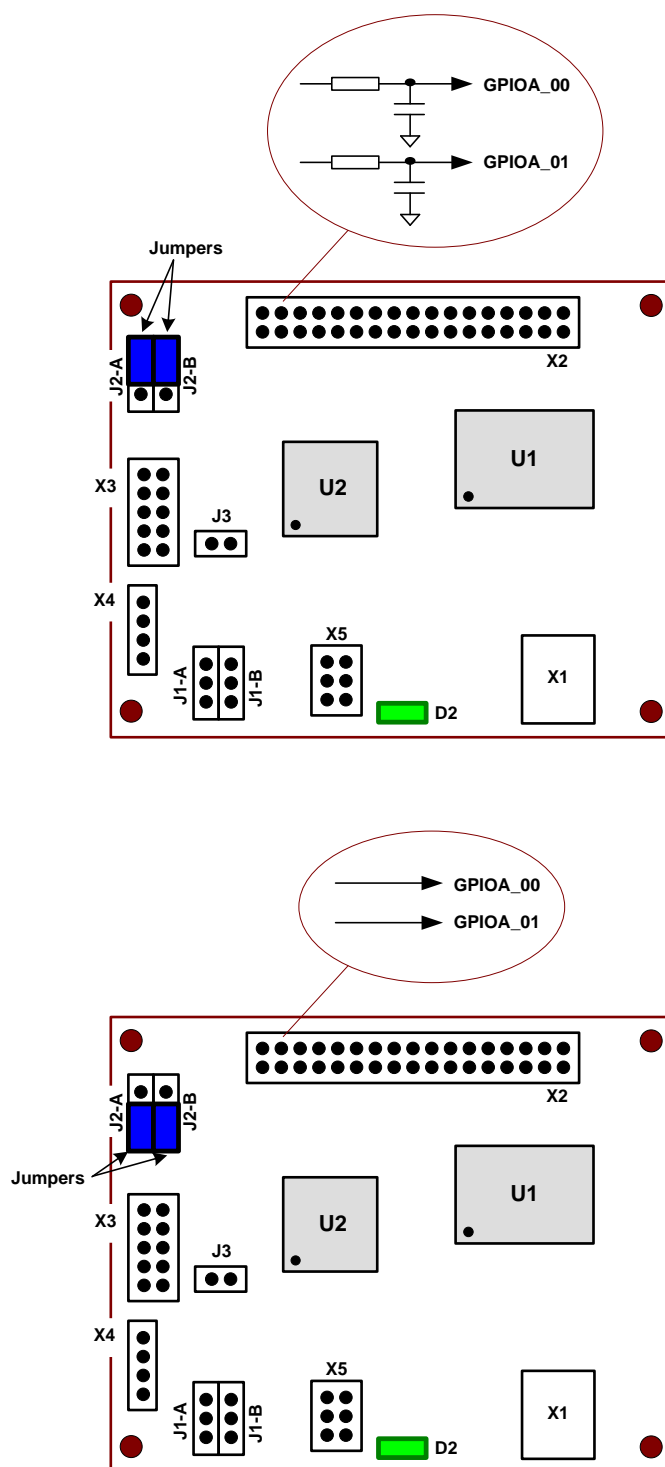
5.5. GPIO Port A Interface

Connector X2 contains GPIO Port A interface.

GPIOA_00	Input/Output	Digital I/O, PWM, interrupt or pulsed. Can be filtered to form an analogue output.
GPIOA_01	Input/Output	Digital I/O, PWM, interrupt or pulsed. Can be filtered to form an analogue output.
GPIOA_02	Input/Output	Digital I/O, PWM, interrupt or pulsed.
GPIOA_03	Input/Output	Digital I/O, PWM, interrupt or pulsed.
GPIOA_04	Input/Output	Digital I/O, PWM, interrupt or pulsed.
GPIOA_05	Input/Output	Digital I/O, PWM, interrupt or pulsed.
GPIOA_06	Input/Output	Digital I/O, PWM, interrupt or pulsed.
GPIOA_07	Input/Output	Digital I/O, PWM, interrupt or pulsed.
GPIOA_08	Input/Output	Digital I/O, PWM, interrupt or pulsed.
GPIOA_09	Input/Output	Digital I/O, PWM, interrupt or pulsed.
GPIOA_10	Input/Output	Digital I/O, PWM, interrupt or pulsed.
GPIOA_11	Input/Output	Digital I/O, PWM, interrupt or pulsed.
GPIOA_12	Input/Output	Digital I/O, PWM, interrupt or pulsed.
GPIOA_13	Input/Output	Digital I/O, PWM, interrupt or pulsed.
GPIOA_14	Input/Output	Digital I/O, PWM, interrupt or pulsed.
GPIOA_15	Input/Output	Digital I/O, PWM, interrupt or pulsed.
0V	Supply	

The GPIO pins can all be separately configured as digital input, digital output, PWM outputs, pulsed output or interrupt input. These pins can also be sacrificed as SPI chip selects as either a SPI master or SPI slave as described in section 5.3.

For GPIOA bits 0 and 1 in PWM mode the output can be low pass filtered to form an analogue output using jumpers J2-A and J2-B.



5.6. GPIO Port B Interface

Connector X2 contains GPIO Port B interface.

GPIOB_00	Input/Output	Digital I/O.
GPIOB_01	Input/Output	Digital I/O.
GPIOB_02	Input/Output	Digital I/O.
GPIOB_03	Input/Output	Digital I/O.
GPIOB_04	Input/Output	Digital I/O.
GPIOB_05	Input/Output	Digital I/O.
GPIOB_06	Input/Output	Digital I/O.
GPIOB_07	Input/Output	Digital I/O.
GPIOB_08	Input/Output	Digital I/O.
GPIOB_09	Input/Output	Digital I/O.
GPIOB_10	Input/Output	Digital I/O.
GPIOB_11	Input/Output	Digital I/O.
GPIOB_12	Input/Output	Digital I/O.
GPIOB_13	Input/Output	Digital I/O.
GPIOB_14	Input/Output	Digital I/O.
GPIOB_15	Input/Output	Digital I/O.
0V	Supply	

5.7. Analogue Interface

Connector X5 the analogue interface.

ANA_00	Input	Analogue input.
ANA_01	Input	Analogue input.
ANA_02	Input	Analogue input.
ANA_03	Input	Analogue input.
3.3V	Supply	
0V	Supply	

5.8. LED Indicator

The ViperBoard includes a green LED indicator to indicate that the circuit is powered correctly.

6. Getting Started

Use of the ViperBoard typically involves writing a C/C++ console application or a Visual C++ Windows GUI application which calls functions and tasks in the ViperBoard API provided by Nano River Technologies. A full definition of the API is provided in the **“ViperBoard API Specification”**, provided on the web-site.

In order to rapidly build up applications and see how to link to the API, eight example applications have been built up. The example applications provided are described below. These are all fully documented in the **“ViperBoard Example Applications”** document, provided on the web-site.

Configuration Example (for Windows)

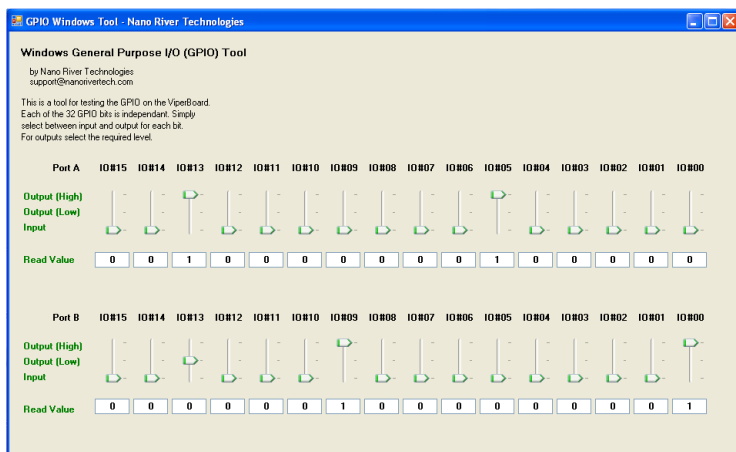
```

*****
** NANO RIVER TECHNOLOGIES                               **
** CONFIGURATION EXAMPLE FOR VIPERBOARD (05 Nov 2009)    **
*****
Open Device...
-> ViperBoard Connected!!!
*****
Initialisation
*****
*****
GPIO A Test
*****
Write AA
Write 55
Make bit 0 pulsed
*****
*****
GPIO B Test
*****
Write AA
Write 55
*****
*****
Analogue Input Test
*****
Analogue Channel #00 : E2
Analogue Channel #01 : 00
Analogue Channel #02 : FA
Analogue Channel #03 : CA
*****
*****
SPI Test
*****
SPI Configure Channel 0...
SPI Set Frequency ...
SPI Slave Data (Written) : AABBCDD
SPI Master Data (To send) : 11223344
SPI Master Send Channel 0...
SPI Master Data (Read) : AABBCDD
SPI Slave Data (Read) : 11223344
*****
*****
I2C Test
*****
I2C Devices Connected ... 0x2B
Master I2C write...
Master Buffer : AABBCDDFF
Slave Buffer : AABBCDDFF
Master I2C read...
Slave Buffer : 1122334455
Master Buffer : 1122334455
*****
*****
TEST COMPLETE ....
*****
<<<<< PRESS any KEY >>>>>

```

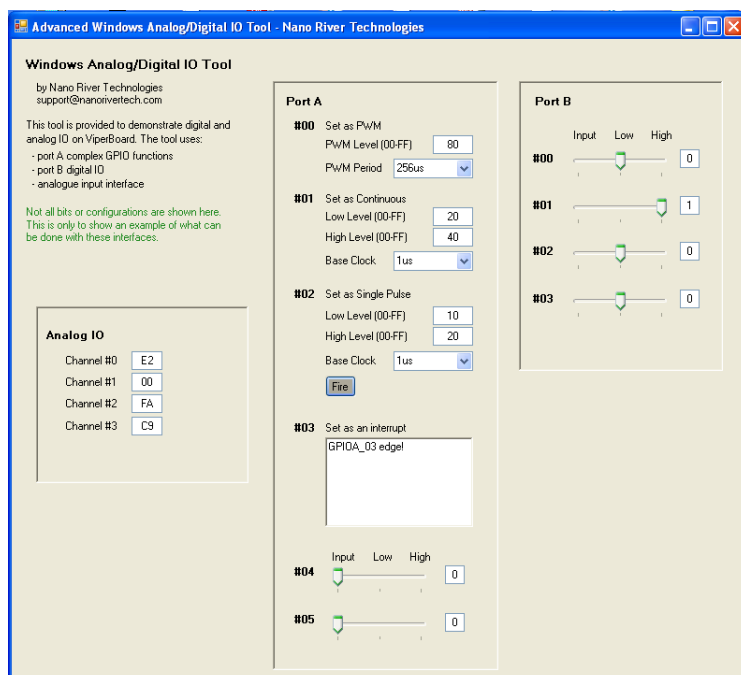
This example is very simple C/C++ console application for Windows showing how to call basic functions associated with SPI, I2C, GPIO and analogue IO. The example is a good starting point if you want to try the ViperBoard for the first time and want to see a simple console application.

Windows GPIOTool (for Windows)



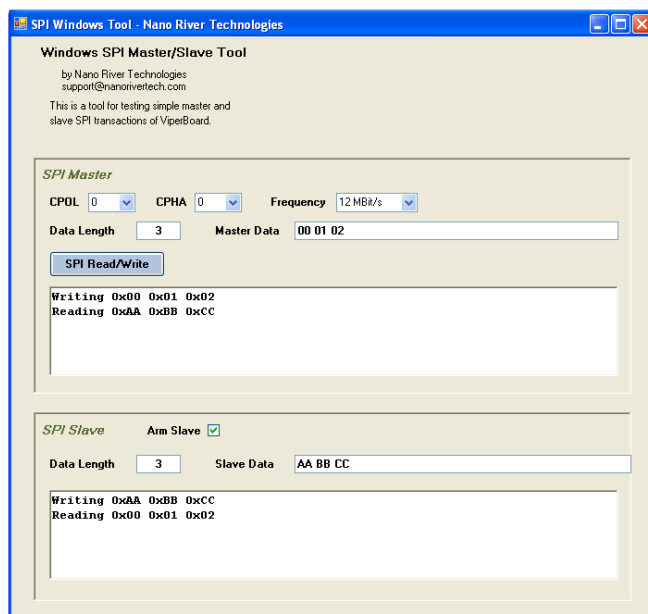
This example application is a very simple Visual C++ GUI application showing how to interface to the general purpose IO (GPIO) pins. This includes both port A and port B GPIOs.

Advanced Windows Analog/Digital IO Tool



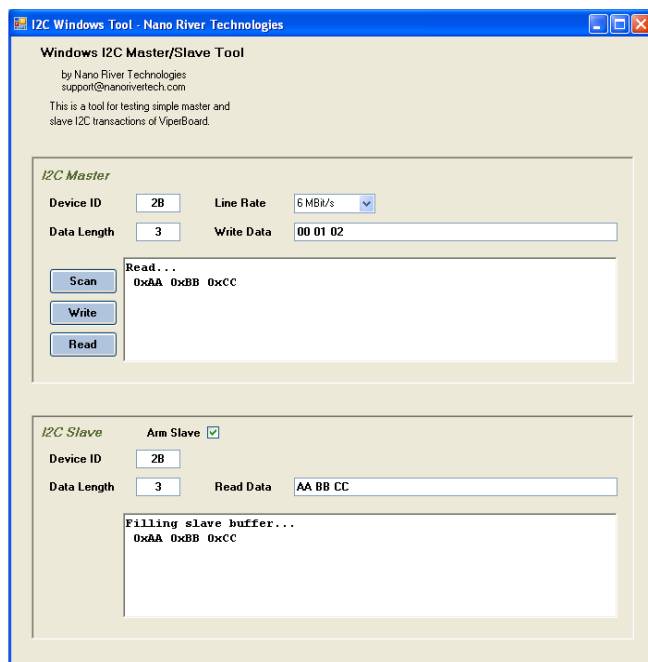
This is a more complex Visual C++ GUI application showing some of the more complex GPIO and analog input functions.

Windows SPI Tool



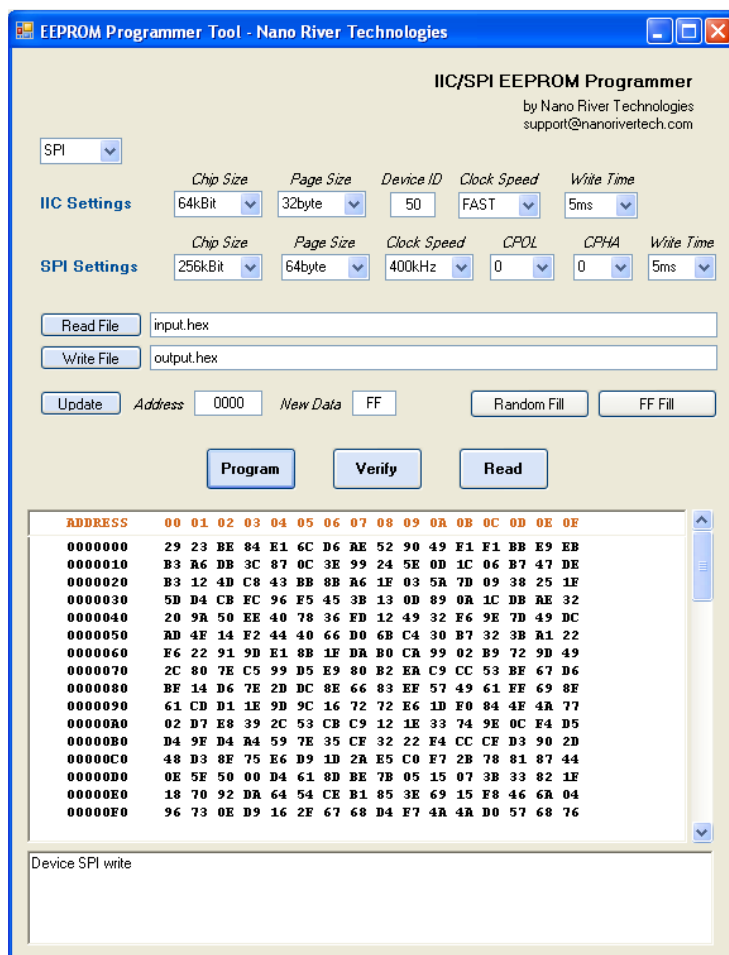
The Windows SPI tool is a windows GUI application in Visual C++ used to demonstrate both master and slave operation of the SPI interface for ViperBoard.

Windows I2C Tool



The Windows I2C tool is a windows GUI application in Visual C++ used to demonstrate both master and slave operation of the I2C interface for ViperBoard.

Windows EEPROM Programmer



The screenshot shows the 'EEPROM Programmer Tool - Nano River Technologies' window. It features a 'IIC/SPI EEPROM Programmer' section with settings for SPI and IIC. The SPI settings are currently selected, showing a chip size of 256kBit, page size of 64byte, clock speed of 400kHz, and a write time of 5ms. There are buttons for 'Read File' (input: input.hex), 'Write File' (output: output.hex), 'Update' (address: 0000, new data: FF), 'Random Fill', and 'FF Fill'. Below these are 'Program', 'Verify', and 'Read' buttons. A large hex dump table displays memory addresses from 00000000 to 000000F0 with corresponding data values. At the bottom, there is a 'Device SPI write' status area.

ADDRESS	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F
00000000	29	23	BE	84	E1	6C	D6	AE	52	90	49	F1	F1	BB	E9	EB
00000010	B3	A6	DB	3C	87	0C	3E	99	24	5E	0D	1C	06	B7	47	DE
00000020	B3	12	4D	C8	43	BB	8B	A6	1F	03	5A	7D	09	38	25	1F
00000030	5D	D4	CB	FC	96	F5	45	3B	13	0D	89	0A	1C	DB	AE	32
00000040	20	9A	50	EE	40	78	36	ED	12	49	32	F6	9E	7D	49	DC
00000050	AD	4E	14	F2	44	40	66	D0	6B	C4	30	B7	32	3B	A1	22
00000060	F6	22	91	9D	E1	8B	1F	DA	B0	CA	99	02	B9	72	9D	49
00000070	2C	80	7E	C5	99	D5	E9	80	B2	EA	C9	CC	53	BE	67	D6
00000080	BE	14	D6	7E	2D	DC	8E	66	83	EF	57	49	61	FF	69	8F
00000090	61	CD	D1	1E	9D	9C	16	72	72	E6	1D	F0	84	4F	4A	77
000000A0	02	D7	E8	39	2C	53	CB	C9	12	1E	33	74	9E	0C	F4	D5
000000B0	D4	9F	D4	A4	59	7E	35	CF	32	22	F4	CC	CF	D3	90	2D
000000C0	48	D3	8F	75	E6	D9	1D	2A	E5	C0	F7	2B	78	81	87	44
000000D0	0E	5F	50	00	D4	61	8D	BE	7B	05	15	07	3B	33	82	1F
000000E0	18	70	92	DA	64	54	CE	B1	85	3E	69	15	F8	46	6A	04
000000F0	96	73	0E	D9	16	2F	67	68	D4	F7	4A	4A	D0	57	68	76

This is a simple I2C and SPI EEPROM programmer written as a windows GUI application in Visual C++. It illustrates how to build a more complex application and also how to link to both I2C and SPI functions in the API.

Configuration Example (for Linux and MacOS)

```

+++++
++ NANO RIVER TECHNOLOGIES                               ++
++ CONFIGURATION EXAMPLE FOR VIPERBOARD (05 Nov 2009)      ++
+++++
Open Device...
-> ViperBoard Connected!!!
+++++

+++++
GPIO A Test
+++++
Write AA

Write 55

Make bit 0 pulsed
+++++
GPIO B Test
+++++
Write AA

Write 55
+++++

+++++
Analogue Input Test
+++++
Analogue Channel #00 : FF
Analogue Channel #01 : FF
Analogue Channel #02 : FF
Analogue Channel #03 : FF
+++++

+++++
SPI Test
+++++
SPI Configure Channel 0...
SPI Set Frequency ...

SPI Slave Data (Written) : AABBCDD
SPI Master Data (To send) : 11223344

SPI Master Send Channel 0...

SPI Master Data (Read) : AABBCDD
SPI Slave Data (Read) : 11223344

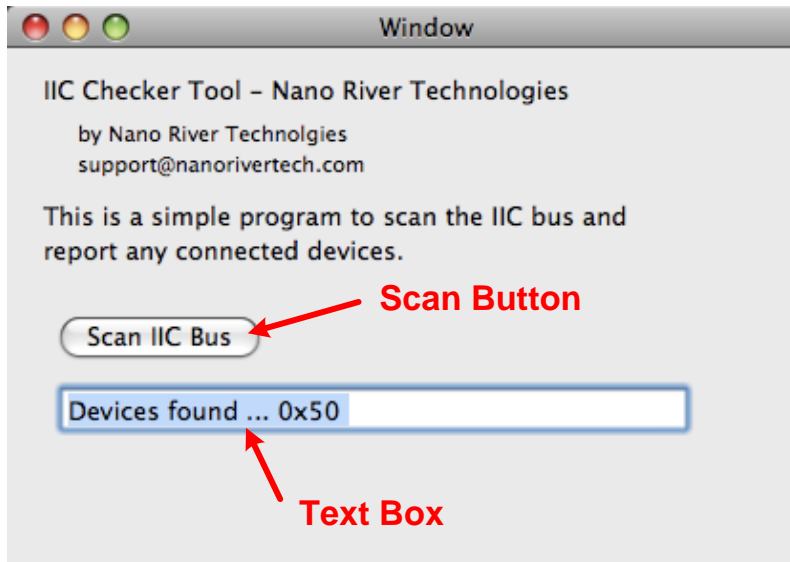
+++++
I2C Test
+++++
I2C Devices Connected ... 0x2B
Master I2C write...
Master Buffer : AABBCDDFF
Slave Buffer : AABBCDDFF

Master I2C read...
Slave Buffer : 1122334455
Master Buffer : 1122334455
+++++

```

This is a simple GCC program for Linux and MacOS to show basic setup of GPIO, I2C, SPI and Analogue IO interfaces.

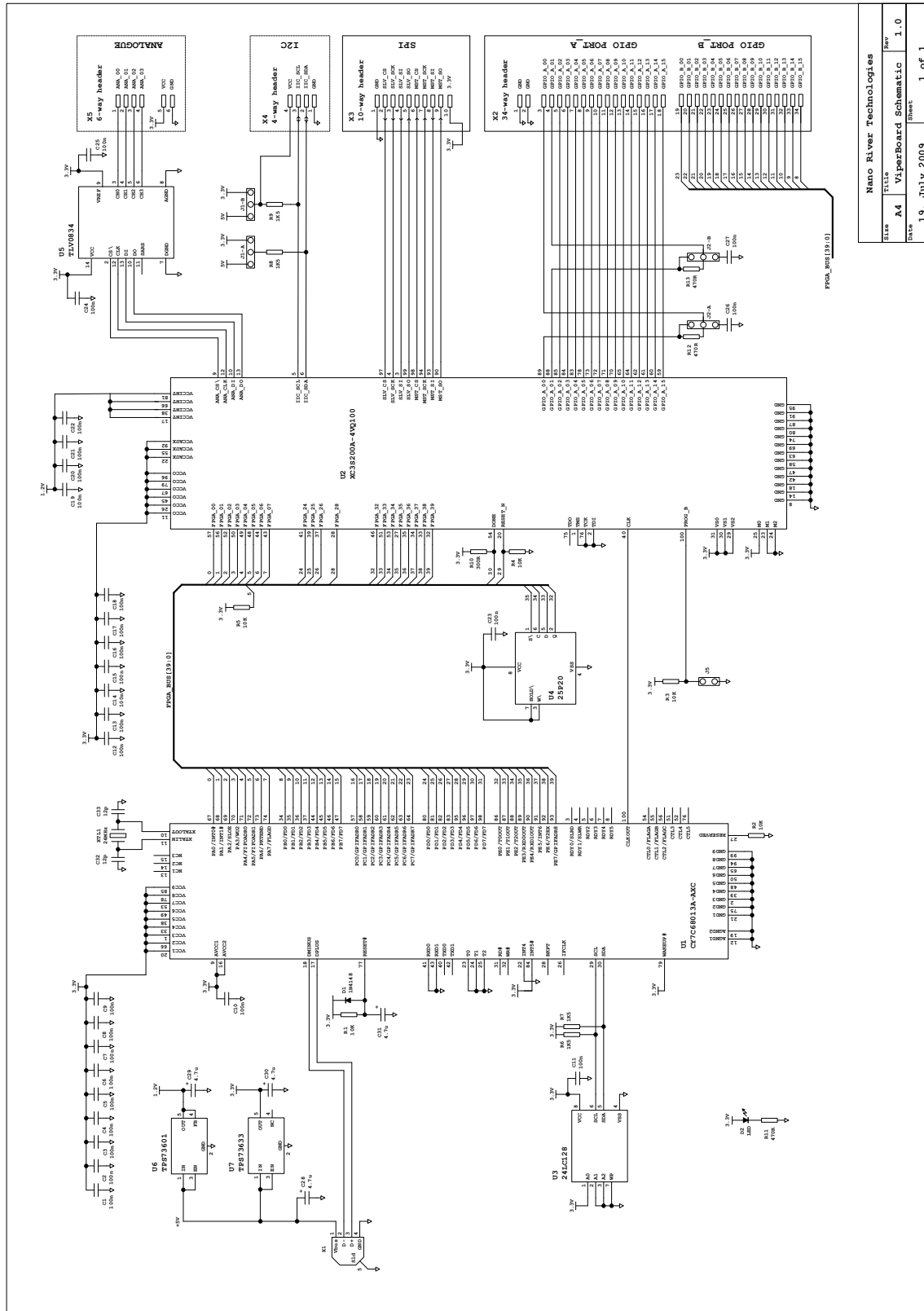
I2C Checker Tool *(for MacOS Xcode Cocoa)*



This is a simple GUI program for MacOS showing how to use ViperBoard to scan IIC devices from Xcode Cocoa.

7. **Appendix A : Schematic**

See over.



Nano River Technologies			
Rev	Title	Rev	
A4	ViperBoard Schematic	1.0	
19. July 2009	1 of 1		