

Program Counter
Instruction Register

00000000

00100001

Register 0
Register 1
Register 2
Register 3

00000101

00000010

00000111

00000000

Central Processing Unit (CPU)

Location (Binary)	(Base 10)	Contents
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00000000

0

00100001

00000001

1

00000000

00000010

2

00001010

00000011

3

...

...

...

11111111

255

Memory