R S HARIPRIYA

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Objective

Desirous to work in Research and Development in the domain of Digital VLSI Testing and Reliability. To work in the field of designing reliable AI hardware accelerators.

Education

Indian Institute of Technology, Tirupati

PhD-Electrical Engineering (Spl: Digital VLSI Testing), CPGA: 9.5/10 *2022-Present*

Indian Institute of Information Technology, Design and Manufacturing, Kancheepuram

MTech-ECE(Spl: Electronics Systems Design), CPGA: 9.34/10 (Distinction) *2019-2021*

Academic Proficiency Certificate for securing second highest CGPA in 1st year

Thesis: Power efficient current mode Flash ADC with error correction and indirect digital variable sampling.

Panimalar Engineering College (Affiliated to Anna university)

BE(ECE), CGPA: 8.83/10 (First Class with Distinction) *2015-2019*

Project: Approximate Hybrid High Radix Encoding for Energy-Efficient Inexact Multiplier using 4:2 Compressor.

Publications

Posit Multiplier: Lakshmi Bhanuprakash Reddy Konduru, R S Haripriya, Keerthija Puli, Subba Ramkumar Reddy Annapalli and Vikramkumar Pudi, **"Design of Energy Efficient and Low Delay Posit Multiplier,"** in VLSID 2023 January 10, 2023.

Licenses and Certifications

- Online Internship on VLSI covering Analog and Digital Flow.
  Organised Jointly by NIELIT Calicut and NIT Calicut during 17th August 2020–4 th September 2020.
- Verilog HDL programming with practical approach. Udemy Date: July 18, 2021.

Tools Known

- Cadence- Virtuoso, Assura, Genus, Innovus, Modus
- ANSYS Electronics
- Xilinx Vivado
- LTSpice and Multisim
- Keil uVision4, ARM Cortex processorTM4C123GH6PM

Courses

- Test and Verification of VLSI systems
- Computer System Architecture
- VLSI circuits for signal processing
- Digital VLSI Design
- Pattern Recognition
- Re-configurable computing