

A Hierarchical Method of Noise Analysis for the Design of Negative-Feedback Amplifiers

Arturo Sarmiento-Reyes*, Roberto Castañeda Sheissa*, L. Hernández Martínez*

Abstract

The design of high-performance negative-feedback amplifiers is aimed to design with a systematic and hierarchical method [1], where the nullor is the starting point for the synthesis of the amplifier. In this context, this paper shows how to improve the noise performance at the negative-feedback amplifiers through equivalent input noise source. This equivalent noise source is obtained with a method which allows to show the main noise sources and key parameters that determine the noise behaviour of the amplifier. So the noise will be evaluated and techniques will be applied to improve the noise performance.

1 Introduction

In a negative-feedback amplifier (NFA) two main blocks can be distinguished, the feedback network and the active part [2]. There are two main steps in the design of high performance negative-feedback network [3], (a) the design of the feedback network, under the assumption that the nullor is the active part, and (b) the design of an active part which constitute the synthesis of the nullor. Furthermore, in this design strategy three fundamental signal-processing quality aspects are of importance, *noise*, *bandwidth* and *distortion*. These aspects are optimized under the assumption that they are orthogonal.

Particular, for a noisy amplifier, the noise determines the minimum signal that the amplifier can process. It is possible to obtain an equivalent input noise source [4, 5] of a noisy amplifier, and the amplifier is considered to be noise-free as shown in figure 9. This article shows how to obtain the equivalent input noise source through by four transformations [6], everything in the context of the design strategy of high-performance negative-feedback amplifiers. The nullor and the synthesis of the feedback network are revised in section 2. Section 3 revises the noise. Section 4 revises the transformations. Section 5 shows an example and results. Section 6 gives conclusions.

2 Nullor And Amplifiers

The nullor is composed of two elements: *the nullator* connected at its input port where $u_i = i_i = 0$ and *the norator* connected at its output port where $u_o = i_o = \text{arbitrary}$, as shown in figure 1. This ideal active element has zero at its ABCD matrix [7, 8], as shown in equation (1). Therefore the nullor has infinite voltage gain (μ), current gain (α), trans-conductance (γ) and trans-impedance (ζ). Furthermore, the nullor does not produce noise, distortion, and does not limit the overall bandwidth.

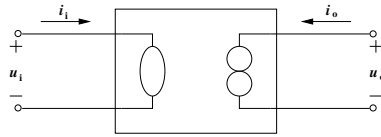


Figure 1: The nullor.

$$\begin{pmatrix} u_i \\ i_i \end{pmatrix} = \begin{pmatrix} 1/\mu & 1/\gamma \\ 1/\zeta & 1/\beta \end{pmatrix} \begin{pmatrix} u_o \\ i_o \end{pmatrix} = \begin{pmatrix} 0 & 0 \\ 0 & 0 \end{pmatrix} \begin{pmatrix} u_o \\ i_o \end{pmatrix} \quad (1)$$

Four negative-feedback amplifiers are obtained when the feedback network is synthesized with passive components, and the active part is a nullor, as shown in figure 2. For each amplifier the feedback network determines the transfer function, and then their ABCD matrix has only one element non zero as shown in equations (2),(3),(4),(5)

$$M_{ABCD} = \begin{pmatrix} \frac{1}{1+R_1/R_2} & 0 \\ 0 & 0 \end{pmatrix}, \text{ for NFA V-V} \quad (2)$$

$$M_{ABCD} = \begin{pmatrix} 0 & -R_1 \\ 0 & 0 \end{pmatrix}, \text{ for NFA V-C} \quad (3)$$

$$M_{ABCD} = \begin{pmatrix} 0 & 0 \\ \frac{-1}{R_1} & 0 \end{pmatrix}, \text{ for NFA C-V} \quad (4)$$

$$M_{ABCD} = \begin{pmatrix} 0 & 0 \\ 0 & \frac{1}{1+R_2/R_1} \end{pmatrix}, \text{ for NFA C-C} \quad (5)$$

*National Institute for Astrophysics, Optics and Electronics Electronics Department, CAD Group, P.O. Box 51, 72000, Puebla, Pue., Mexico, Email: jarocho@inaoep.mx

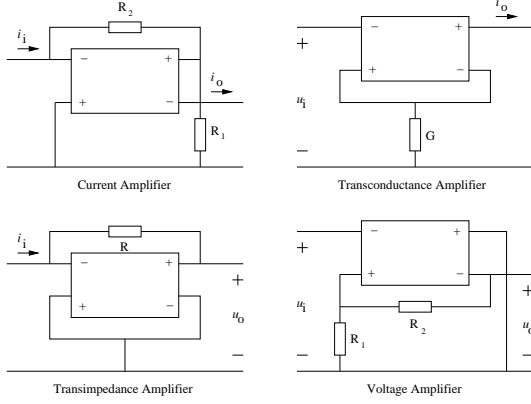


Figure 2: Negative-feedback amplifiers.

3 Noise In Amplifiers

The power spectral density of the output $S_{n,y}(w)$ of a LTI system, in response to a wide-sense stationary input noise $S_{n,x}(w)$ is given by [9, 10]: $S_{n,y} = |H(w)|^2 S_{n,x}(w)$, where $H(w)$ is the transfer function. This method is a straight-forward way to evaluate noise in amplifiers, by evaluating the circuit's equations of the amplifier. However it does not yield much insight about the dominant noise contribution, and hides the key design parameters that are essential to optimize the noise behavior. In this article, four transformations are used to obtain the equivalent input noise source, this method allows to show the dominant factors and the key parameters that determine the noise behavior of the amplifier. The power spectral density of the equivalent input noise source $S_{n,eq,in}(w)$ can be expressed as ¹

$$S_{n,eq,in}(w) = \sum_{i=1}^n |H_{2i}(w)|^2 S_{u_{n,2i}}(w) + \sum_{k=0}^m |H_{2k+1}(w)|^2 S_{i_{n,2k+1}}(w) \quad (6)$$

where $S_{u_{n,2i}}(w)$ and $S_{i_{n,2k+1}}(w)$ are the power spectral densities of the noise sources of voltage and current, respectively.

The figure 3 shows the noisy amplifiers. Where $u_{n,n}$ and $i_{n,n}$ model completely the noise in a two-port system [11] ², and each noise source of the resistors ³ is shown in the table 1. The sort source

¹It is assumed that the noise sources are uncorrelated and each source has mean square value equal to zero.

²Despite that the two-port system is a nullor, the two noisy sources are considered for the analysis because they appear later as a result of the nullor synthesis

³The noise source of the load is not considered in order to simplify the nullor synthesis

Elements	NFA			
	V-V	V-C	C-V	C-C
R_S	u_{nS}	u_{nS}	u_{nS}	u_{nS}
R	—	i_{nR}	u_{nR}	—
R_1	u_{nR_1}	—	—	i_{nR_1}
R_2	u_{nR_2}	—	—	i_{nR_2}

Table 1: Resistor's noise sources

(voltage or current) is selected such that in the section 4 the transformations are performed easily.

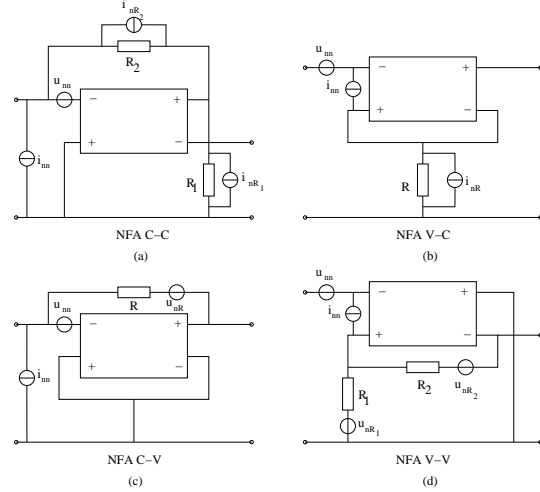


Figure 3: Noisy Negative-Feedback Amplifiers.

4 Transformations

The goal of the transformations is to move noisy sources to the input or output of the amplifier.

- **Transformation 1 V-Shift.** Let N be a network as shown in figure 4(1). The transformation V-shift on u_n is obtained to remove it through node b as shown in figure 4(2). After the transformation is performed the nodes a and b are clustered.
- **Transformation 2 I-Shift.** Let N be a network as shown in figure 5(1). The transformation I-shift on i_n is obtained by splitting it into two occurrence of i_n , as shown if figure 5(2)
- **Transformation 3 Norton-Thevenin.** The theorems of Thevenin and Norton are used to transform voltage sources into current sources and viceversa.

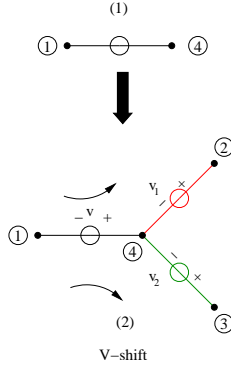


Figure 4: V-Shift Transformation.

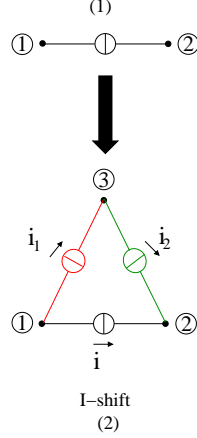


Figure 5: I-Shift Transformation.

- **Transformation 4** *Two-port shift.* Let N be a two-port network as shown in figure 6(1), where u_o and i_o are the (noise) voltage and (noise) current output for the two-port network; then, the shift from the output to the input port is shown in figure 6(2) such that u_o and i_o are accounted in the input.

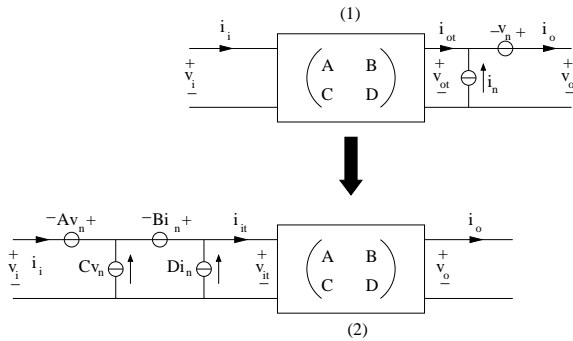


Figure 6: Two-Port Shift.

5 Example and Conclusions

The amplifier V-C is used to show how to perform transformations. The result is the equivalent input

noise source.

- **Step 1** u_{nn} and u_{ns} of the figure 7 are added, the result is $u_{n,eq,1}$ as shown in figure 8.

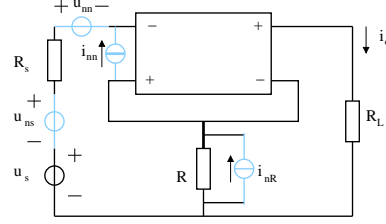


Figure 7: Transadmittance Amplifier with Noise Sources Added.

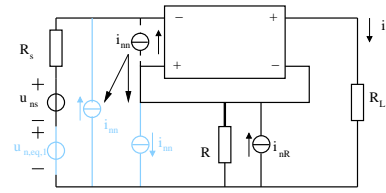


Figure 8: Voltage Noise Sources Added and I-Shift Applied.

- **Step 2** The I-shift is used on i_{nn} as shown in figure 8. The Norton-Thevenin transformation is applied on i_{nn} and R_s , the result is added to $u_{n,eq,1}$, this equals to $u_{n,eq,2}$ as shown in figure 9. The figure 8 shows i_{nn} and i_{nR} in parallel with R , these noise sources are added and the result is $i_{n,eq,1}$ as depicted in figure 9.

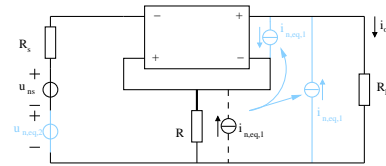


Figure 9: Voltage Noise Sources Added and I-Shift Applied.

- **Step 3** The I-shift is applied on $i_{n,eq,1}$ as shown in figure 9. For the current noise source $i_{n,eq,1}$ located directly in parallel to the output port of the nullor, if we apply the two-port transformation to the nullor it will show us that this source will not yield any equivalent input noise source, since all chain parameters of the nullor equal zero.

- **Step 4** The two-port transformation is used on the source $i_{n,eq,1}$ which is located at the amplifier output. The result is added with $u_{n,eq,2}$ and the result becomes the equivalent input noise source $u_{n,eq,in}$ as shown in figure 10. Then a noiseless-amplifier is obtained.

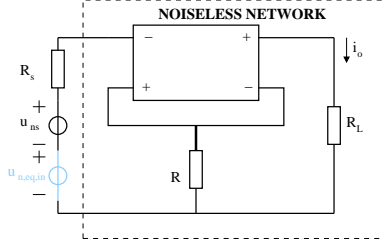


Figure 10: Voltage Noise Sources Added and I-Shift Applied.

The result from the previous steps is shown in equation (7) which is the equivalent input noise source. Similarly, it is possible to obtain the equivalent input noise sources for the V-V, C-V and C-C amplifiers. These are shown in equations (8), (9) and (10), respectively.

$$u_{n,eq,in} = u_{ns} + u_{nn} + (R_s + R)i_{nn} + Ri_{nR} \quad (7)$$

$$u_{n,eq,in} = u_{ns} + u_{nn} + \left(R_s + \frac{R_1 R_2}{R_1 + R_2} \right) i_{nn} + \frac{R_2}{R_1 + R_2} u_{nR_1} + \frac{R_1}{R_1 + R_2} u_{nR_2} \quad (8)$$

$$i_{n,eq,in} = i_{ns} + i_{nn} + \left(\frac{1}{R_s} + \frac{1}{R} \right) u_{nn} + \frac{1}{R} u_{nR} \quad (9)$$

$$u_{n,eq,in} = i_{ns} + i_{nn} + \left(\frac{1}{R_s} + \frac{1}{R_1 + R_2} \right) i_{nn} + \frac{R_2}{R_1 + R_2} i_{nR_2} + \frac{R_1}{R_1 + R_2} i_{nR_1} \quad (10)$$

6 Conclusions

A hierarchical noise analysis has been performed on the four basic negative-feedback amplifiers. The equivalent input noise source is obtained using four simple transformations, this allows us to understand the contribution of the main noise source. This main source is comprised of the source's resistance, active part and the feedback network. The largest noise contribution is usually identified by

being from the source resistance. The results show that feedback network adds noise to the active part, so we can perform techniques to reduce noise in both the active part and feedback network. In particular u_{nn} is increased for the V-V and V-C amplifiers as shown in equations 8 and 7; i_{nn} is increased in the case of C-V and C-C amplifiers, as shown in equations 9 and 10.

References

- [1] E.H. Nordholt, *Design of High-Performance Negative-Feedback Amplifiers*, Ph.D. thesis, Delft University of Technology, The Netherlands, 1980.
- [2] P.R. Gray and R.G. Meyer, *Analysis and Design of Analog Integrated Circuits*, John Wiley and Sons, Inc, 1993.
- [3] E.H. Nordholt, *Design of High-Performance Negative-Feedback Amplifiers*, Elsevier Scientific, 1983.
- [4] Y. Netzer, "The design of low-noise amplifiers," in *Proc. IEEE*, June 1981, vol. 69, pp. 728–742.
- [5] Z.Y. Chang and W.M.C. Sansen, *Low-Noise Wide-Band Amplifiers in Bipolar and CMOS Technologies*, Kluwer Academic, 1991.
- [6] George Moschytz, Chris Toumazou, and Barrie Gilbert, *Trade-Offs in Analog Circuit Design - The Designer's Companion*, Kluwer Academic, 2002.
- [7] H.J. Carlin, "Singular network elements," *IEEE Trans. Circuit Theory*, vol. 11, pp. 67–72, March 1964.
- [8] G.S. Moschytz, "Linear integrated networks: Fundamentals," in *Bell Laboratories Series*. Bell Laboratories, 1974.
- [9] W.B. Davenport and W.L. Root, *An Introduction to the Theory of Random Signals and Noise*, IEEE Press, 1987.
- [10] P.Z. Peebles, *Probability, Random Variables, and Random Signal Principles*, McGraw-Hill, 1980.
- [11] H.A. Haus, "Representation of noise in linear two-ports," in *Proc. IRE*, January 1960, vol. 48, pp. 69–74.