GOVERNMENT OF INDIA
MINISTRY OF ELECTRONICS AND INFORMATION TECHNOLOGY
LOK SABHA
UNSTARRED QUESTION NO. 571
TO BE ANSWERED ON: 01/12/2021

ATMANIRBHAR BHARAT

571. SHRI PATEL HASMUKHBHAI SOMABHAI:

Will the Minister of ELECTRONICS AND INFORMATION TECHNOLOGY be pleased to state:

(a). whether Government announced "Swadeshi Microprocessor Challenge" to realize the ambition of self-reliance and a momentous stride towards Atmanirbhar Bharat; and
(b) if so, the details thereof; and
(c) whether the Government has allocated funds for the above said scheme for the next five years and if so, the details thereof?

ANSWER

MINISTER OF STATE FOR ELECTRONICS AND INFORMATION TECHNOLOGY
(SHRI RAJEEV CHANDRASEKHAR)

(a):   Yes, Sir. MeitY has announced Swadeshi Microprocessor Challenge.

(b): Swadeshi Microprocessor Challenge was launched by MeitY on 18th August 2020 towards strengthening the capacity and capability in India's semiconductor ecosystem by developing systems and solutions around two indigenous microprocessors namely SHAKTI and VEGA processors.

Under the Challenge, 6,170 Teams (having about 10,000 participants) had registered as Start-ups, Students from Education Institutions & Innovators from across the country. Hundred (100) teams were provided with financial support as well as the FPGA Hardware Boards ported with Swadeshi Microprocessors for developing innovative Proof-of-Concept innovative solutions around Swadeshi Microprocessors. Besides this, mentorship support to Top 10 winners of the Challenge has been included for incubating their start-ups at identified Incubation Centre across the country for translating their Hardware Prototypes developed using Swadeshi Microprocessors.

(c):   The total allocation for the Swadeshi Microprocessor Challenge is Rs. 5.0 Crore.

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