SOEN331: Introduction to Formal Methods for Software Engineering
Assignment 1 on extended finite state machines

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1 Washing Machine formal specification

The EFSM of the washing machine is the tuple $S = (Q, \Sigma_1, \Sigma_2, q_0, V, \Lambda)$, where

$Q = \{\text{off, on}\}$

$\Sigma_1 = \{\text{turn on, turn off}\}$

$\Sigma_2 = \{\text{beep, turn light off}\}$

q_0 : off

Λ: Transition specifications

1. $\rightarrow off$
2. $off \xrightarrow{\text{turn on}} on$
3. $on \xrightarrow{\text{turn off / (beep; turn light off)}} off$

As *on* is a composite state, it is defined as the tuple $S = (Q, \Sigma_1, \Sigma_2, q_0, V, \Lambda)$, where

$Q = \{\text{operating, servicing}\}$

$\Sigma_1 = \{\text{after (10 s), service signal [idle], machine fixed}\}$

$\Sigma_2 = \{\text{blinking, long beep}\}$

q_0 : operating

Λ: Transition specifications

1. $\xrightarrow{\text{after (10 s) / (blinking; long beep)}} operating$
2. $operating \xrightarrow{\text{service signal [idle]}} service$
3. $service \xrightarrow{\text{machine fixed}} operating$

As *operating* is a composite state, it is defined as the tuple $S = (Q, \Sigma_1, \Sigma_2, q_0, V, \Lambda)$, where

$Q = \{\text{idle, standby, active}\}$

$\Sigma_1 = \{\text{light on, start signal or finish button, power off, power on, completion, cancel, cancel [setting]}\}$

$\Sigma_2 = \{\text{turn light on, clear settings, unlock door}\}$

q_0 : idle

Λ: Transition specifications

1. $\xrightarrow{\text{light on / turn light on}} idle$
2. $idle \xrightarrow{\text{start signal or finish button}} active$
3. $active \xrightarrow{\text{cancel}} idle$
4. $active \xrightarrow{\text{completion / unlock door}} idle$
5. $active \xrightarrow{\text{cancel [setting] / clear settings}} idle$
6. $active \xrightarrow{\text{power off}} standby$
7. $standby \xrightarrow{\text{power on}} active$

The UML state diagram is shown in Figure 1.

As *active* is a composite state, it is defined as the tuple $S = (Q, \Sigma_1, \Sigma_2, q_0, V, \Lambda)$, where

The UML state diagram is shown in Figure 2.

2 UML state diagrams

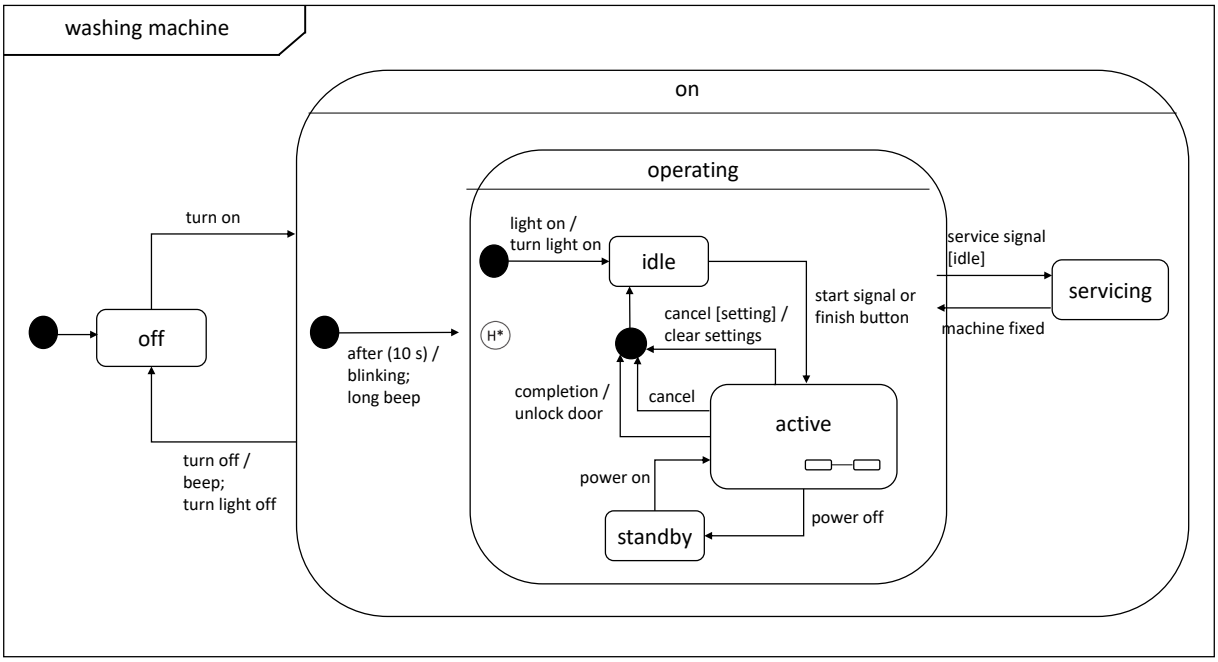

Figure 1: Washing Machine

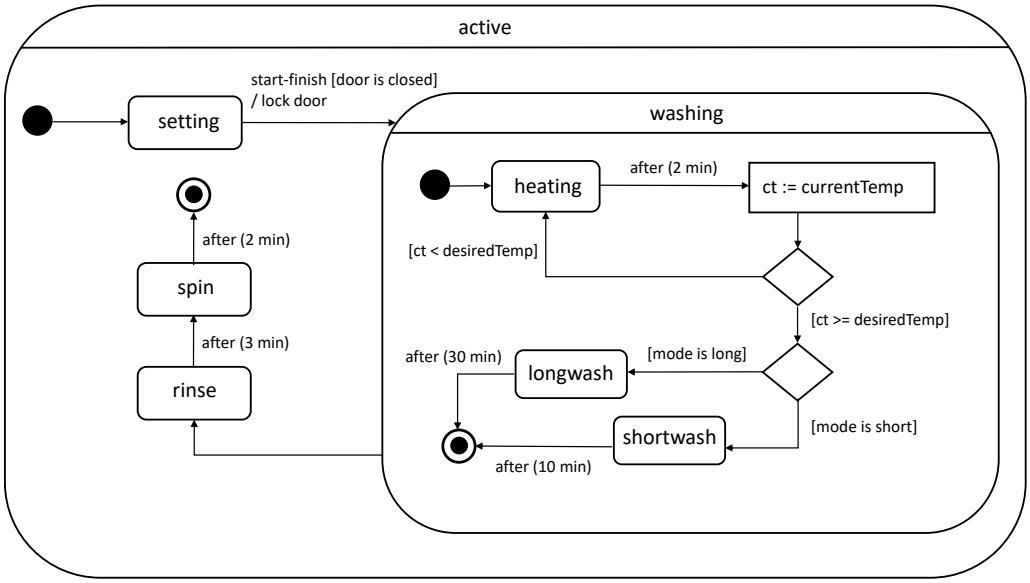

Figure 2: Washing Machine (Active state)