

QIHUA-X733

核心板接口定义

核心板 引脚编号	CPU 引脚编号	功能 1	功能 2	功能 3	功能 4	功能 5	功能 6	功能 7	功能 8	功能 9	功能 14
1	DCIN （5V/2A 核心板主电源输入）										
2	PS										
3	DCIN （5V/2A 核心板主电源输入）										
4	PS										
5	DCIN （5V/2A 核心板主电源输入）										
6	PS										
7	DCIN （5V/2A 核心板主电源输入）										
8	GND										
9	DCIN （5V/2A 核心板主电源输入）										
10	GND										
11	-										
12	GND										
13	-										
14	GND										
15	-										
16	AP-RESET（复位信号输入，外接按钮到 GND）										
17	D2	PD0	LCD0-D2	LVDS0-D0P	DSI0-D0P	EINK-D0	-	PWM0-0	-	-	PD-EINT0
18	AP-NMI（复位信号输出）										
19	E2	PD1	LCD0-D3	LVDS0-D0N	DSI0-D0N	EINK-D1	-	PWM0-1	-	-	PD-EINT1
20	PWU-PWRON（电源开关机信号输入，外接按钮到 GND）										
21	F2	PD2	LCD0-D4	LVDS0-D1P	DSI0-D1P	EINK-D2	-	PWM0-2	-	-	PD-EINT2
22	FEL（升级信号输入，外接按钮到 GND）										
23	F1	PD3	LCD0-D5	LVDS0-D1N	DSI0-D1N	EINK-D3	-	PWM0-3	-	-	PD-EINT3
24	AK1	PH0	TWI0-SCK	I2S2-DOUT2	I2S2-DIN2	RGMIIO-RXD1/RMII0-RXD1	TWI3-SCK	SPI1-HOLD	-	-	PH-EINT0
25	G1	PD4	LCD0-D6	LVDS0-D2P	DSI0-CKP	EINK-D4	-	PWM0-4	-	-	PD-EINT4
26	AK2	PH1	TWI0-SDA	I2S2-DOUT3	I2S2-DIN3	RGMIIO-RXD0/RMII0-RXD0	TWI3-SDA	SPI1-WP	-	-	PH-EINT1
27	G2	PD5	LCD0-D7	LVDS0-D2N	DSI0-CKN	EINK-D5	-	PWM0-5	-	-	PD-EINT5
28	1AA2	PH2	TWI1-SCK	-	I2S2-MCLK	RGMIIO-RXCTL/RMII0-CRS-DV	-	SPI1-CS1	-	-	PH-EINT2
29	J1	PD6	LCD0-D10	LVDS0-CKP	DSI0-D2P	EINK-D6	-	PWM0-6	-	-	PD-EINT6
30	1AA1	PH3	TWI1-SDA	-	I2S2-BCLK	RGMIIO-CLKIN/RMII0-RXER	-	SPI1-MISO	-	-	PH-EINT3
31	J2	PD7	LCD0-D11	LVDS0-CKN	DSI0-D2N	EINK-D7	-	PWM0-7	-	-	PD-EINT7
32	AL1	PH4	UART3-TX	SPI2-CS3	I2S2-LRCK	RGMIIO-TXD1/RMII0-TXD1	-	SPI1-CS0	-	-	PH-EINT4
33	K1	PD8	LCD0-D12	LVDS0-D3P	DSI0-D3P	EINK-D8	-	PWM0-8	-	-	PD-EINT8
34	AM2	PH5	UART3-RX	I2S2-DIN1	I2S2-DOUT0	RGMIIO-TXD0/RMII0-TXD0	-	SPI1-MOSI	-	-	PH-EINT5
35	K2	PD9	LCD0-D13	LVDS0-D3N	DSI0-D3N	EINK-D9	-	PWM0-9	-	-	PD-EINT9
36	AN1	PH6	UART3-RTS	I2S2-DOUT1	I2S2-DIN0	RGMIIO-TXCK/RMII0-TXCK	-	SPI1-CLK	-	-	PH-EINT6
37	1J2	PD15	LCD0-D21	LVDS1-D2N	DSI1-CKN	EINK-D15	SPI1-CS1	UART3-CTS	-	-	PD-EINT15
38	AL2	PH7	UART3-CTS	SPI2-CS2	DSI-TRIG-LCD-TE1	RGMIIO-TXCTL/RMII0-TXEN	-	-	-	-	PH-EINT7
39	1J1	PD14	LCD0-D20	LVDS1-D2P	DSI1-CKP	EINK-D14	SPI1-HOLD/DBI-DCX/DBI-WR X	UART3-RTS	-	-	PD-EINT14
40	AP1	PH8	DMIC-CLK	SPI2-CLK	-	RGMIIO-MDC	-	-	-	-	PH-EINT8
41	N1	PD19	LCD0-DE	LVDS1-D3N	DSI1-D3N	EINK-STH	PWM1-5	UART4-CTS	-	-	PD-EINT19
42	1AC1	PH9	DMIC-DATA0	SPI2-CS0	-	RGMIIO-MDIO	-	-	-	-	PH-EINT9
43	N2	PD18	LCD0-CLK	LVDS1-D3P	DSI1-D3P	EINK-CKH	SPI1-WP/DBI-TE	UART4-RTS	-	-	PD-EINT18
44	1A3	PE10	TWI4-SCK	TCON-FSYNC1<TC CON-LCD1>	I2S3-DIN3<MUX-HDMI-ED P>	I2S3-DOUT3<MUX-HDMI-EDP>	-	NCSI0-D6	UART1-RTS	-	PE-EINT10
45	M1	PD17	LCD0-D23	LVDS1-CKN	DSI1-D2N	EINK-LEH	TWI2-SDA	UART3-RX	-	-	PD-EINT17
46	B7	PK24	MCSIC-CKN	MCSI0-MCLK	PWM0-6	TWI12-SCK	-	-	TWI10-SDA	-	PK-EINT24
47	M2	PD16	LCD0-D22	LVDS1-CKP	DSI1-D2P	EINK-OEH	TWI2-SCK	UART3-TX	-	-	PD-EINT16
48	AP2	PH13	UART5-TX	-	-	RGMIIO-TXD3/RMII0-NUL	TWI7-SDA	-	-	-	PH-EINT13
49	1G1	PD13	LCD0-D19	LVDS1-D1N	DSI1-D1N	EINK-D13	SPI1-MISO/DBI-SDI/DBI-TE /DBI-DCX	PWM1-3	-	-	PD-EINT13
50	AR2	PH14	UART5-RX	-	-	RGMIIO-TXD2/RMII0-NUL	TWI7-SCK	-	-	-	PH-EINT14
51	1G2	PD12	LCD0-D18	LVDS1-D1P	DSI1-D1P	EINK-D12	SPI1-MOSI/DBI-SDO	PWM1-2	-	-	PD-EINT12
52	1AC2	PH11	DMIC-DATA2	SPI2-MISO	-	RGMIIO-RXD2/RMII0-NUL	-	-	-	-	PH-EINT11
53	1J3	PD11	LCD0-D15	LVDS1-D0N	DSI1-D0N	EINK-D11	SPI1-CLK/DBI-SCLK	PWM1-1	-	-	PD-EINT11
54	AT2	PH12	DMIC-DATA3	SPI2-CS1	-	RGMIIO-RXCK/RMII0-NUL	-	-	-	-	PH-EINT12
55	1J4	PD10	LCD0-D14	LVDS1-D0P	DSI1-D0P	EINK-D10	SPI1-CS0/DBI-CSX	PWM1-0	-	-	PD-EINT10
56	AT1	PH15	UART5-RTS	LEDC	DSI-TRIG-LCD-TE1	RGMIIO-EPHY-25/50M	-	-	-	-	PH-EINT15
57	B9	PK22	MCSIC-D1N	TWI3-SCK	UART3-RTS	PWM1-6	-	NCSI1-HSYNC	UART1-TX	-	PK-EINT22
58	1AE1	PH10	DMIC-DATA1	SPI2-MOSI	-	RGMIIO-RXD3/RMII0-NUL	-	-	-	-	PH-EINT10

核心板 引脚编号	CPU 引脚编号	功能 1	功能 2	功能 3	功能 4	功能 5	功能 6	功能 7	功能 8	功能 9	功能 14
59	A10	PK20	MCSIC-DON	TWI2-SCK	UART3-TX	PWM0-1	-	NCSI1-PCLK	UART1-RTS	-	PK-EINT20
60	AU2	PH16	UART5-CTS	-	-	IR-TX	-	-	-	-	PH-EINT16
61	1L3	PD20	LCD0-HSYNC	DSI-TRIG-LCD- TE1	TWI0-SCK	EINK-CKV	PCIE-CLKREQN	UART4-TX	TWI3-SCK	PWM0-2	PD-EINT20
62	AT1	PH15		UART5-RTS	LEDC	DSI-TRIG-LCD-TE1	RGMII0-EPHY-25/50M	-	-	-	-
63	1L2	PD21	LCD0-VSYNC	-	TWI0-SDA	EINK-MODE	PCIE-WAKEN	UART4-RX	TWI3-SDA	PWM0-3	PD-EINT21
64	1AB24	PC3	NAND-CE1	-	-	SPI0-CS0	SPIF0-CS0	-	-	-	PC-EINT3
65	1L1	PD22	PWM0-4	-	-	EINK-STV	PCIE-PERSTN	TWI0-SCK	TWI2-SCK	PWM1-4	PD-EINT22
66	1AB26	PC12	NAND-DQS	-	-	SPI0-CLK	SPIF0-CLK	-	-	-	PC-EINT12
67	P2	PD23	PWM0-5	-	-	-	PCIE-CLKREQN	TWI0-SDA	TWI2-SDA	PWM1-5	PD-EINT23
68	1AB23	PC2	NAND-CLE	-	-	SPI0-MOSI	SPIF0-MOSI	-	-	-	PC-EINT2
69	ALD02（以太网 IO 电源输出）										
70	1Y22	PC4	NAND-CEO	-	-	SPI0-MISO	SPIF0-MISO	-	-	-	PC-EINT4
71	SPOLP										
72	-										
73	SPOLN										
74	-										
75	SPORP										
76	-										
77	SPORN										
78	-										
79	HPOUTL										
80	B10	PK21	MCSIC-DOP	TWI2-SDA	UART3-RX	PWM0-2	-	NCSI1-MCLK	UART1-CTS	-	PK-EINT21
81	HPOUTFB										
82	A9	PK23	MCSIC-D1P	TWI3-SDA	UART3-CTS	PWM1-7	-	NCSI1-VSYNC	UART1-RX	-	PK-EINT23
83	HPOUTR										
84	B8	PK25	MCSIC-CKP	MCSI1-MCLK	PWM0-7	TWI12-SDA	-	-	TWI10-SCK	-	PK-EINT25
85	1Y23	PC7	NAND-RB1	-	-	SPI0-CS1	SPIF0-DQS	-	-	-	PC-EINT7
86	1B26	PL10	S-UART0-TX	S-TWI2-SCK	S-UART1-TX		S-PWM0-8	-	-	-	PL-EINT10
87	MBIAS（MIC 电源输出，默认 1.8V）										
88	1E3	PJ26	-	PWM1-8	UART4-RTS	UART2-TX	-	TWI5-SCK	SPI3-MISO	LCD0-D16	PJ-EINT26
89	MIC1P										
90	1E4	PJ27	-	PWM1-9	UART4-CTS	UART2-RX	DSI-TRIG-LCD-TE1	TWI5-SDA	SPI3-CS0	LCD0-D17	PJ-EINT27
91	MIC1N										
92	B21	HTX0P									
93	MIC2P/MIC3P										
94	A21	HTX0N									
95	MIC2N/MIC3N										
96	B19	HTX1P									
97	LINE1N/MIC4N										
98	B20	HTX1N									
99	LINE1NR/MIC4P										
100	B18	HTX2P									
101	VCCIO-WIFI（1.8V WIFI IO 电源输出）										
102	A18	HTX2N									
103	VCC-3V3（3.3V/400mA 电源输出）										
104	A22	HTXCP									
105	VCC-3V3（3.3V/400mA 电源输出）										
106	B22	HTXCN									
107	U36	AP-CK32K-OUT（32.768K 时钟信号输出）									
108	1A13	HSCL									
109	R1	PG4	SDC1-D2	LCD0-D16	PWM1-5	-	-	-	-	-	PG-EINT4
110	1B13	HSDA									
111	T2	PG5	SDC1-D3	LCD0-D17	PWM1-6	-	-	-	-	-	PG-EINT5
112	1C13	HCEC									
113	1N2	PG1	SDC1-CMD	LCD0-D1	PWM1-2	-	-	-	-	-	PG-EINT1
114	1E13	HHPD									
115	1N3	PG0	SDC1-CLK	LCD0-D0	PWM1-1	-	-	-	-	-	PG-EINT0
116	VCC-CARD（默认 3.3V TF 卡电源输出）										
117	1N1	PG2	SDC1-D0	LCD0-D8	PWM1-3	-	-	-	-	-	PG-EINT2
118	1R3	PF6	-	-	-	-	-	-	-	-	PF-EINT6
119	R2	PG3	SDC1-D1	LCD0-D9	PWM1-4	-	-	-	-	-	PG-EINT3

核心板 引脚编号	CPU 引脚编号	功能 1	功能 2	功能 3	功能 4	功能 5	功能 6	功能 7	功能 8	功能 9	功能 14
120	1U1	PF0	SDC0-D1	JTAG-MS	-	UART5-RTS	TWI2-SCK	-	-	-	PF-EINT0
121	T37	PM0	S-JTAG-MS	S-SPI0-CS0	S-RJTAG-MS	S-TWI1-SCK	S-PWM0-2	S-IR-RX	-	-	PM-EINT0
122	AB2	PF3	SDC0-D0	JTAG-DI	-	UART5-CTS	TWI2-SDA	-	-	-	PF-EINT3
123	R37	PM1	S-JTAG-CK	S-SPI0-CLK	S-RJTAG-CK	S-TWI1-SDA	S-PWM0-3	-	-	-	PM-EINT1
124	AD1	PF2	SDC0-CLK	UART0-TX	-	-	-	-	-	-	PF-EINT2
125	P36	PM2	S-JTAG-D0	S-SPI0-MOSI	S-UART0-TX	S-UART1-TX	S-PWM0-4	-	-	-	PM-EINT2
126	AD2	PF1	SDC0-CMD	JTAG-D0	-	-	-	-	-	-	PF-EINT1
127	R36	PM3	S-JTAG-DI	S-SPI0-MISO	S-UART0-RX	S-UART1-RX	S-PWM0-5	-	-	-	PM-EINT3
128	1R1	PF4	SDC0-D3	UART0-RX	-	UART5-RX	-	-	-	-	PF-EINT4
129	N37	PM4	S-UART0-TX	S-TWI2-SCK	S-TWI1-SCK	S-UART1-TX	S-PWM0-0	S-IR-RX	-	-	PM-EINT4
130	1R4	PF5	SDC0-D2	JTAG-CK	-	UART5-TX	-	-	-	-	PF-EINT5
131	AB1	PG13	I2S1-DOUT0	I2S1-DIN1	TWI6-SCK	-	DMIC-DATA1	HDMI-SDA	-	-	PG-EINT13
132	A24	EDP-AUXP									
133	Y2	PG11	I2S1-BCLK	TWI12-SCK	TWI9-SCK	-	DMIC-DATA3	HDMI-CEC	-	-	PG-EINT11
134	B24	EDP-AUXN									
135	AA1	PG14	I2S1-DIN0	I2S1-DOUT1	TWI6-SDA	-	DMIC-DATA0	-	-	-	PG-EINT14
136	B32	COMBO-TX3P									
137	AA2	PG12	I2S1-LRCK	TWI12-SDA	TWI9-SDA	-	DMIC-DATA2	HDMI-SCL	-	-	PG-EINT12
138	B31	COMBO-TX3N									
139	T1	PG6	UART1-TX	UART4-TX	PWM1-7	-	-	-	-	-	PG-EINT6
140	A30	COMBO-TX2P									
141	V2	PG7	UART1-RX	UART4-RX	PWM1-8	-	-	-	-	-	PG-EINT7
142	B30	COMBO-TX2N									
143	V1	PG8	UART1-RTS	UART4-RTS	UART5-TX	-	TWI0-SCK	-	-	-	PG-EINT8
144	A27	COMBO-TX1P									
145	W2	PG9	UART1-CTS	UART4-CTS	UART5-RX	-	TWI0-SDA	-	-	-	PG-EINT9
146	B27	COMBO-TX1N									
147		-									
148	B25	COMBO-TX0P									
149		-									
150	A25	COMBO-TX0N									
151		-									
152	1A15	DP-HPD									
153		-									
154		GND									
155		-									
156	A36	PCIE1-TX0P/USB2-U3-TX0P									
157		-									
158	B36	PCIE1-TX0N/USB2-U3-TX0N									
159		-									
160	B35	PCIE1-RX0P/USB2-U3-RX0P									
161		-									
162	B34	PCIE1-RX0N/USB2-U3-RX0N									
163		-									
164	A34	COMB1-REF-CLKP									
165		-									
166	A33	COMB1-REF-CLKN									
167		-									
168	1C3	PE13	-	SPI3-CLK	-	-	PCIE-PERSTN	-	UART1-RX	-	PE-EINT13
169		-									
170	C2	PE14	-	SPI3-MOSI	-	PWM1-8	PCIE-CLKREQN	IR-RX	TWI9-SCK	-	PE-EINT14
171		-									
172	B1	PE12	-	SPI3-CS0	I2S3-DOUT0<MUX-HDMI-E DP>	I2S3-DIN1<MUX-HDMI-EDP>	PCIE-WAKEN	-	UART1-TX	-	PE-EINT12
173		-									
174	1D11	PK0	MCSIA-DON	UART6-DCD	I2S4-BCLK<MUX-EDP>	HDMI-CEC	TWI1-SDA	NCSI1-HSYNC	-	-	PK-EINT0
175		-									
176	1C11	PK1	MCSIA-DOP	UART6-DSR	I2S4-MCLK<MUX-EDP>	HDMI-SCL	TWI1-SCK	NCSI1-VSYNC	-	-	PK-EINT1
177		-									
178	1B11	PK2	MCSIA-D1N	UART6-DTR	I2S4-LRCK<MUX-EDP>	HDMI-SDA	TWI5-SDA	NCSI1-PCLK	-	-	PK-EINT2
179		-									
180	1A11	PK3	MCSIA-D1P	UART6-RI	I2S4-DIN0<MUX-EDP>	I2S4-DOUT1<MUX-EDP>	TWI5-SCK	NCSI1-MCLK	-	-	PK-EINT3

核心板 引脚编号	CPU 引脚编号	功能 1	功能 2	功能 3	功能 4	功能 5	功能 6	功能 7	功能 8	功能 9	功能 14
181	D37	USB0-DP									
182	1B7	PK4	MCSIA-CKN	PCIE-WAKEN	I2S4-DOUT0<MUX-EDP>	I2S4-DIN1<MUX-EDP>	SPI3-CS0	NCSI1-D15	-	-	PK-EINT4
183	D36	USB0-DM									
184	1A7	PK5	MCSIA-CKP	PCIE-CLKREQN	PWM1-8	PWM1-9	SPI3-CLK	NCSI1-D14	-	-	PK-EINT5
185	W1	PG10	I2S1-MCLK	CLK-FANOUT0	PWM1-9	-	DMIC-CLK	-	-	-	PG-EINT10
186	1B9	PK6	MCSIA-D2N	TWI2-SCK	UART4-TX	UART2-RTS	SPI3-MOSI	NCSI1-D13	-	-	PK-EINT6
187	N36	PM5	S-UART0-RX	S-TWI2-SDA	S-TWI1-SDA	S-UART1-RX	S-PWM0-1	S-IR-RX	-	-	PM-EINT5
188	1A9	PK7	MCSIA-D2P	TWI2-SDA	UART4-RX	UART2-CTS	SPI3-MISO	NCSI1-D12	-	-	PK-EINT7
189	E36	USB1-DP									
190	1E9	PK8	MCSIA-D3N	MCSIO-MCLK	UART4-RTS	UART2-TX	SPI3-CS1	NCSI1-D11	-	-	PK-EINT8
191	F36	USB1-DM									
192	1D9	PK9	MCSIA-D3P	MCSI2-MCLK	UART4-CTS	UART2-RX	-	NCSI1-D10	-	-	PK-EINT9
193	C36	USB2-DP									
194		-									
195	B37	USB2-DM									
196		-									
197	1B25	PL11	S-UART0-RX	S-TWI2-SDA	S-UART1-RX	S-IR-RX	S-PWM0-9	-	-	-	PL-EINT11
198	B16	PK10	MCSIB-DON	UART6-TX	-	PWM0-3	-	NCSI1-D9	-	-	PK-EINT10
199		-									
200	B17	PK11	MCSIB-DOP	UART6-RX	-	PWM0-4	-	NCSI1-D8	-	-	PK-EINT11
201		-									
202	A15	PK12	MCSIB-D1N	UART6-RTS	-	PWM0-5	-	NCSI1-D7	-	-	PK-EINT12
203		-									
204	A16	PK13	MCSIB-D1P	UART6-CTS	-	PWM0-6	-	NCSI1-D6	-	-	PK-EINT13
205		-									
206	B13	PK14	MCSIB-CKN	PCIE-PERSTN	-	PWM0-7	-	NCSI1-D5	-	-	PK-EINT14
207	1W3	PB10	UART0-RX	I2S0-DIN3	I2S0-DOUT3	PWM1-2	-	LCD0-D17	TWI8-SDA	TWI0-SDA	PB-EINT10
208	B14	PK15	MCSIB-CKP	-	-	PWM0-8	-	NCSI1-D4	-	-	PK-EINT15
209	1W4	PB9	UART0-TX	I2S0-DIN2	I2S0-DOUT2	PWM1-1	WATCHDOG-SIG	LCD0-D16	TWI8-SCK	TWI0-SCK	PB-EINT9
210	A12	PK16	MCSIB-D2N	TWI3-SCK	UART2-TX	PWM0-9	-	NCSI1-D3	-	-	PK-EINT16
211	1U2	PB2	UART2-RTS	-	SPI2-MOSI	HDMI-SCL	LCD0-D8	JTAG-DO	TWI0-SCK	-	PB-EINT2
212	A13	PK17	MCSIB-D2P	TWI3-SDA	UART2-RX	PWM1-0	-	NCSI1-D2	-	-	PK-EINT17
213	1U3	PB3	UART2-CTS	-	SPI2-MISO	HDMI-SDA	LCD0-D9	JTAG-DI	TWI0-SDA	-	PB-EINT3
214	B11	PK18	MCSIB-D3N	MCSI1-MCLK	UART2-RTS	PWM1-1	-	NCSI1-D1	TWI9-SCK	-	PK-EINT18
215	AE2	PB1	UART2-RX	UART0-RX	SPI2-CLK	-	LCD0-D1	JTAG-CK	-	-	PB-EINT1
216	B12	PK19	MCSIB-D3P	MCSI2-MCLK	UART2-CTS	PWM1-2	-	NCSI1-D0	TWI9-SDA	-	PK-EINT19
217	AE1	PB0	UART2-TX	UART0-TX	SPI2-CS0	DSI-TRIG-LCD-TE1	LCD0-D0	JTAG-MS	-	-	PB-EINT0
218		-									
219	1C1	PJ25	-	PWM1-7	UART4-RX	-	-	TWI4-SDA	SPI3-MOSI	-	PJ-EINT25
220		-									
221	1C2	PJ24	-	PWM1-6	UART4-TX	-	-	TWI4-SCK	SPI3-CLK	-	PJ-EINT24
222	A6	PE0	MCSIO-MCLK	CSIO-XHS	-	SPI3-CS0	-	NCSIO-HSYNC	UART6-TX	-	PE-EINT0
223		-									
224	B4	PE3	TWI3-SCK	CSI1-XVS-FSYN C	PWM0-0	SPI3-MISO	-	NCSIO-D1	UART6-CTS	-	PE-EINT3
225		-									
226	A4	PE4	TWI3-SDA	LEDC	PWM0-1	SPI3-CS1	-	NCSIO-D0	UART6-DCD	-	PE-EINT4
227	1E1	PJ23	-	PWM1-5	UART3-RX	UART2-CTS	TWI7-SDA	TWI3-SDA	TWI11-SDA	LCD0-D9	PJ-EINT23
228	1B5	PE5	MCSI1-MCLK	-	-	PWM0-2	-	NCSIO-MCLK	UART6-DSR	-	PE-EINT5
229	1E2	PJ22	-	PWM1-4	UART3-TX	UART2-RTS	TWI7-SCK	TWI3-SCK	TWI11-SCK	LCD0-D8	PJ-EINT22
230	B6	PE1	TWI2-SCK	CSI1-XHS	-	SPI3-CLK	-	NCSIO-VSYNC	UART6-RX	-	PE-EINT1
231		-									
232	B5	PE2	TWI2-SDA	CSIO-XVS-FSYN C	-	SPI3-MOSI	-	NCSIO-PCLK	UART6-RTS	-	PE-EINT2
233		-									
234	J37	PL5	S-JTAG-CK	S-TWI2-SDA	S-SPI0-CLK	-	S-PWM0-3	-	-	-	PL-EINT5
235		-									
236	K37	PL4	S-JTAG-MS	S-TWI2-SCK	S-SPI0-CS0	S-IR-RX	S-PWM0-2	-	-	-	PL-EINT4
237		-									
238	H36	PL6	S-JTAG-DO	S-UART0-TX	S-SPI0-MOSI	S-IR-RX	S-PWM0-4	-	-	-	PL-EINT6
239	G37	PL8	S-TWI1-SCK	S-UART1-TX	S-TWI0-SCK	S-TWI2-SCK	S-PWM0-6	-	-	-	PL-EINT8
240	G36	PL7	S-JTAG-DI	S-UART0-RX	S-SPI0-MISO	-	S-PWM0-5	-	-	-	PL-EINT7

核心板 引脚编号	CPU 引脚编号	功能 1	功能 2	功能 3	功能 4	功能 5	功能 6	功能 7	功能 8	功能 9	功能 14
241	F37	PL9	S-TWI1-SDA	S-UART1-RX	S-TWI0-SDA	S-TWI2-SDA	S-PWM0-7	-	-	-	PL-EINT9
242	L36	PL2	S-UART1-TX	S-UART0-TX	S-TWI1-SDA	-	S-PWM0-0	-	-	-	PL-EINT2
243	A2	PE9	MCSI2-MCLK	TCON-FSYNCO<T CON-LCD0>	I2S3-MCLK<MUX-HDMI-ED P>	-	-	NCSI0-D5	UART6-RI	-	PE-EINT9
244	K36	PL3	S-UART1-RX	S-UART0-RX	S-TWI1-SCK	S-IR-RX	S-PWM0-1	-	-	-	PL-EINT3
245	B3	PE7	CLK-FANOUT2	HDMI-SCL	I2S3-BCLK<MUX-HDMI-ED P>	-	-	NCSI0-D3	TWI11-SCK	-	PE-EINT7
246	-										
247	B2	PE8	CLK-FANOUT3	HDMI-SDA	I2S3-LRCK<MUX-HDMI-ED P>	-	-	NCSI0-D4	TWI11-SDA	-	PE-EINT8
248	-										
249	1A1	PE11	TWI4-SDA	SPI3-MISO	I2S3-DIN2<MUX-HDMI-ED P>	I2S3-DOUT2<MUX-HDMI-EDP>	PCIE-CLKREQN	NCSI0-D7	UART1-CTS	-	PE-EINT11
250	-										
251	1C5	PE6	CLK-FANOUT1	HDMI-CEC	I2S3-DIN0<MUX-HDMI-ED P>	I2S3-DOUT1<MUX-HDMI-EDP>	-	NCSI0-D2	UART6-DTR	-	PE-EINT6
252	1F25	GPADC4									
253	D1	PE15	-	SPI3-CS1	-	PWM1-9	IR-RX	-	TWI9-SDA	-	PE-EINT15
254	1H23	GPADC5									
255	AVCC（默认 1.8V 模拟电源输出）										
256	1H24	GPADC6									
257	1D23	LRADC0									
258	-										
259	-										
260	-										