

CONNECTOR

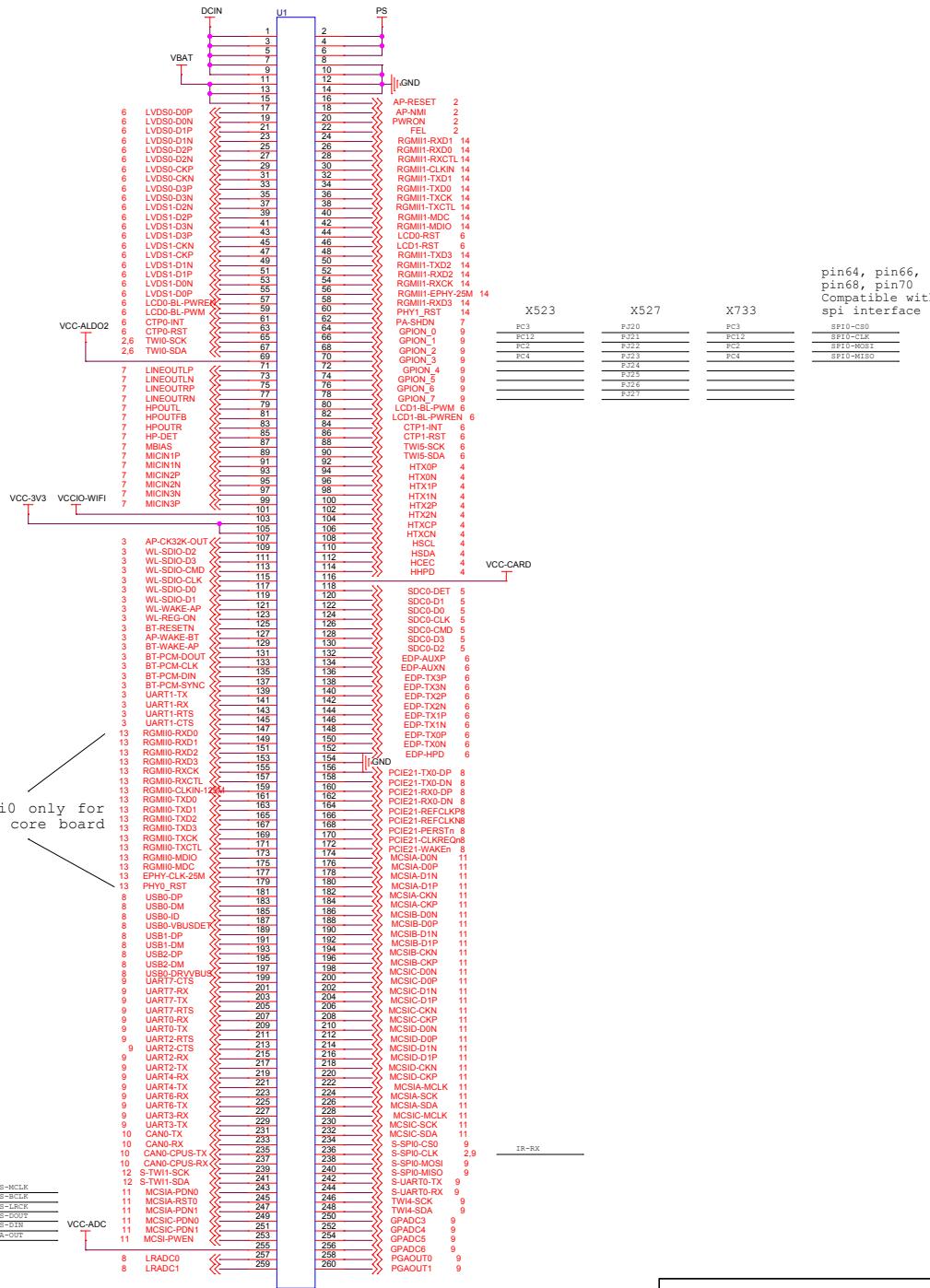
Description

Design by Chengdu qihua information technology co. LTD

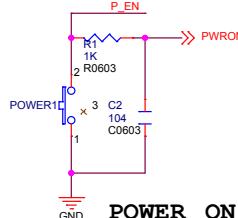
It supports the QIHUA-X523, QIHUA-X527 and QIHUA-X733 core boards simultaneously.

Function

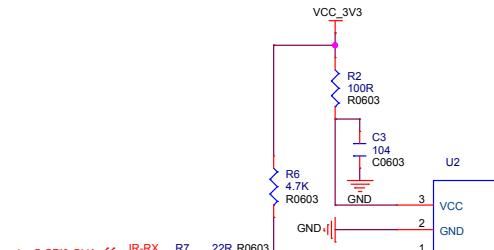
P01: CONNECTOR
P02: POWER (12V INPUT), RTC, IR
P03: WIFI, BT
P04: HDMI
P05: TF CARD
P06: LVDS, MIPI DSI, EDP
P07: HEADSET, MIC, SPK
P08: OTG, USB
P09: UART, RS485, SPI, I2C, ADC, DAC
P10: CAN
P11: MIPI CSI
P12: MINI PCIE 3G/4G
P13: ETHERNET0
P14: ETHERNET1



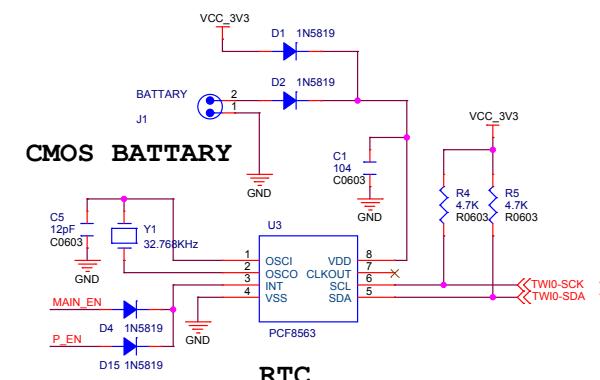
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QIHUA-X227 BOARD		
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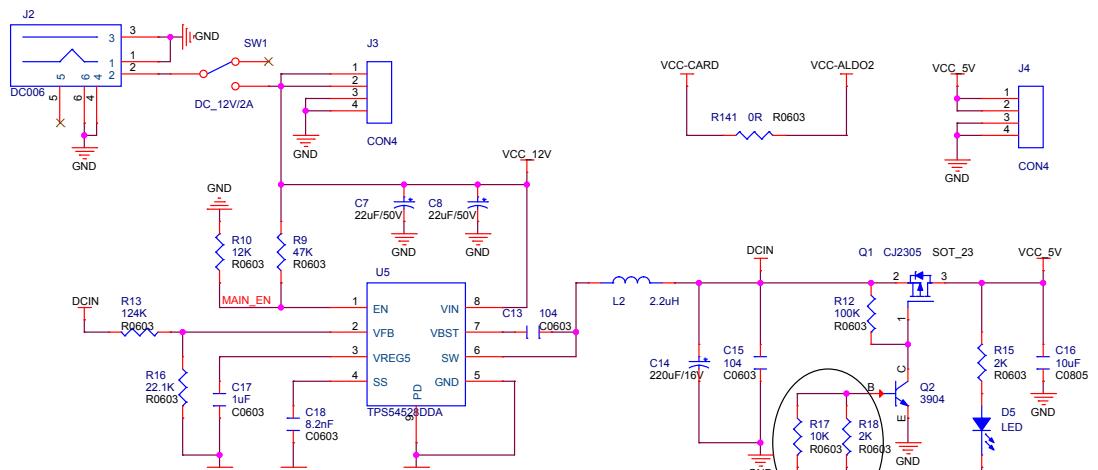
RESET



TR

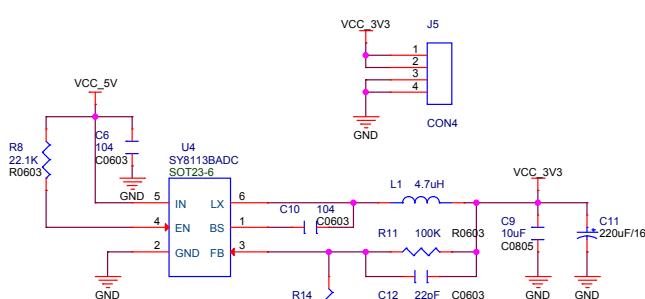


RTC



12V DC POWER INPUT

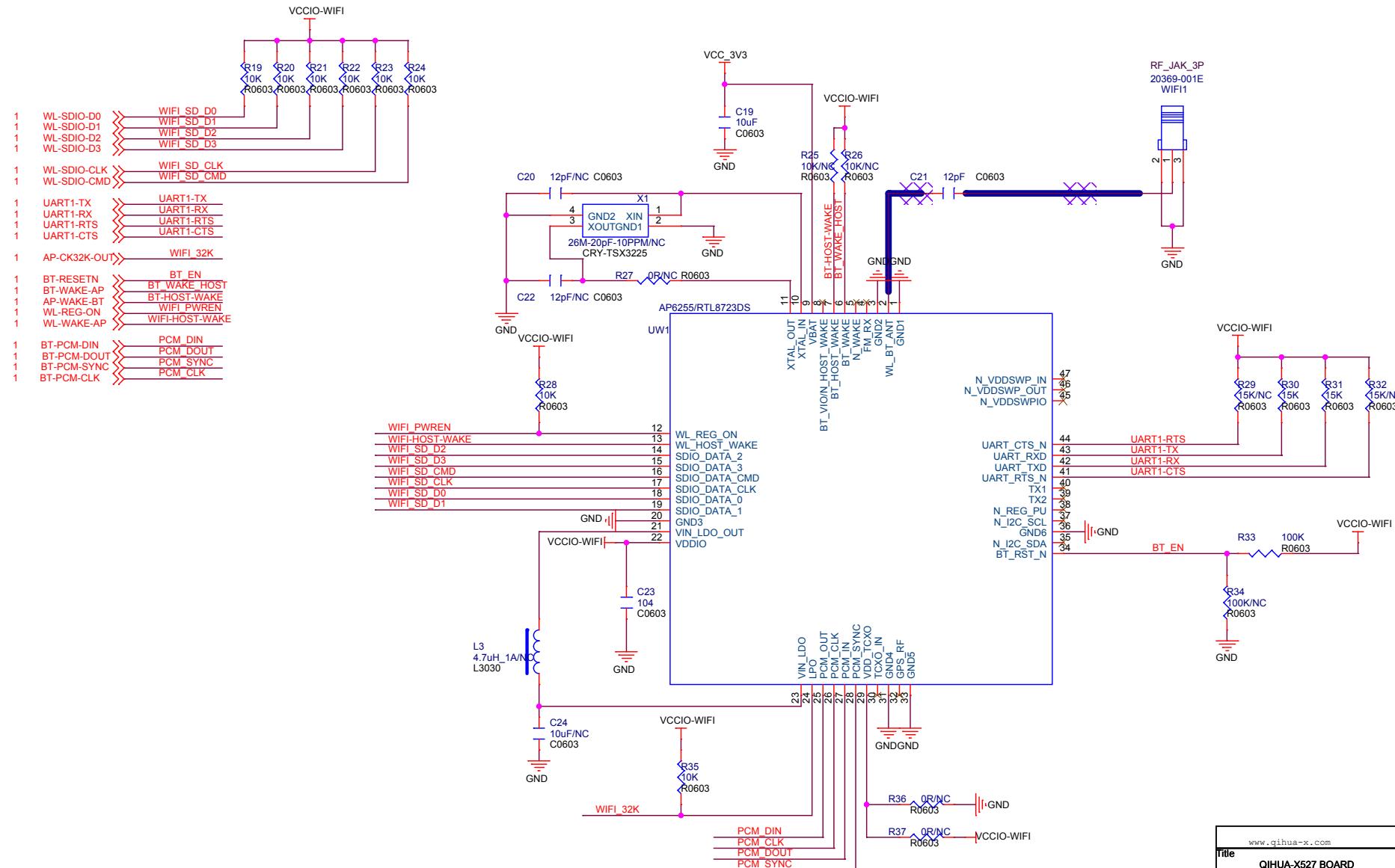
POWER OUTPUT 5v



5V TO 3.3V

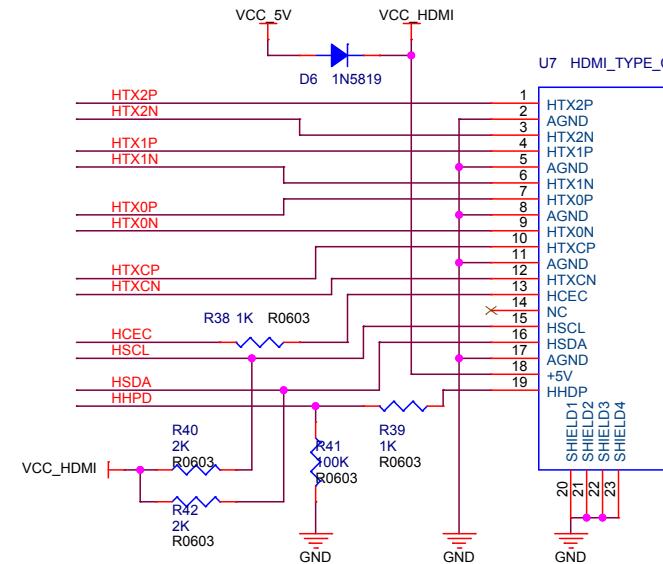
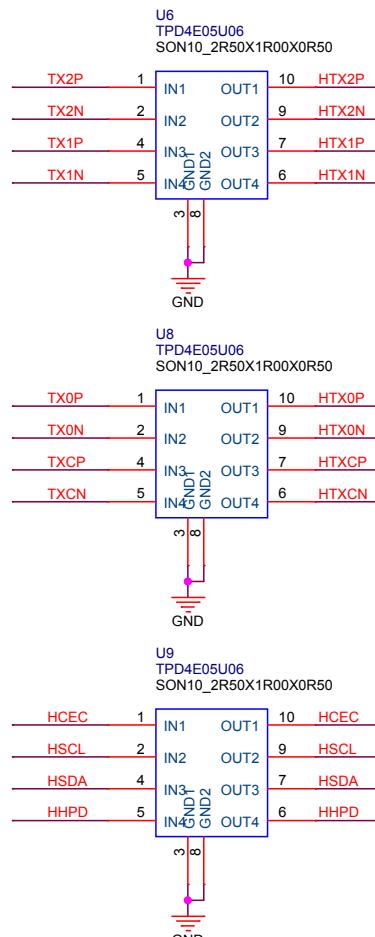
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WIFI + BT



HDMI

1 HTX0P TX0P
 1 HTX0N TX0N
 1 HTX1P TX1P
 1 HTX1N TX1N
 1 HTX2P TX2P
 1 HTX2N TX2N
 1 HTXCP TXCP
 1 HTXCN TXCN
 1 HCEC HCEC
 1 HSCL HSCL
 1 HSDA HSDA
 1 HHPD HHPD

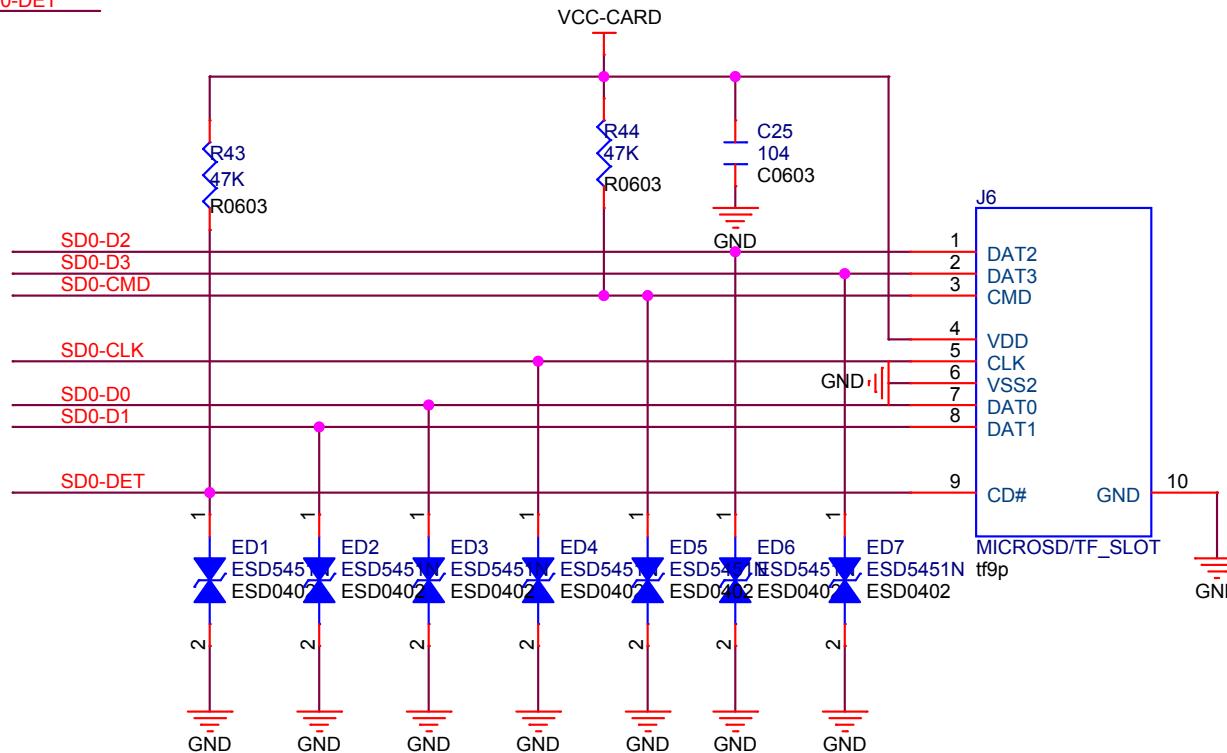


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TF CARD

1 SDC0-D0 SD0-D0
 1 SDC0-D1 SD0-D1
 1 SDC0-D2 SD0-D2
 1 SDC0-D3 SD0-D3
 1 SDC0-CMD SD0-CMD
 1 SDC0-CLK SD0-CLK
 1 SDC0-DET SD0-DET



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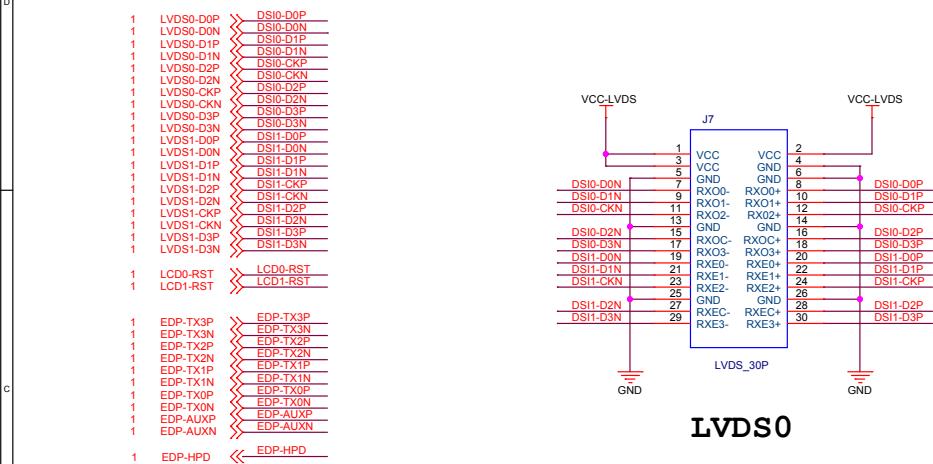
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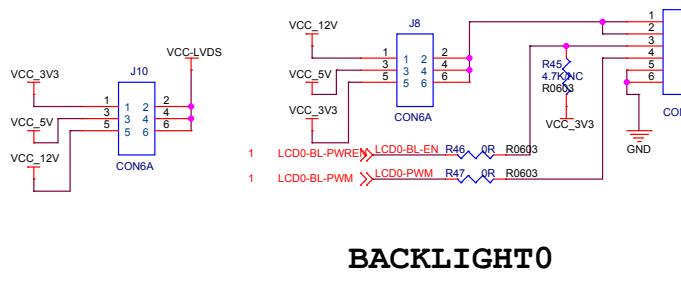
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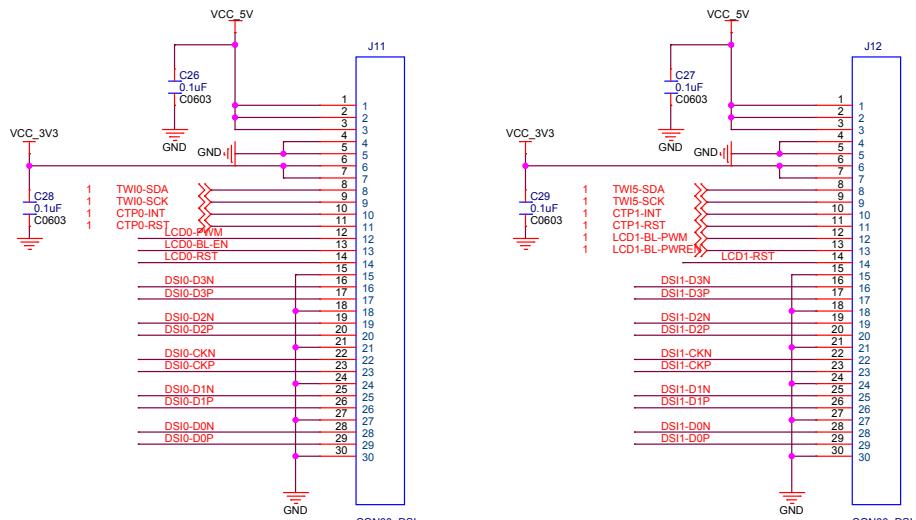
LCD PHONE



LVDS0

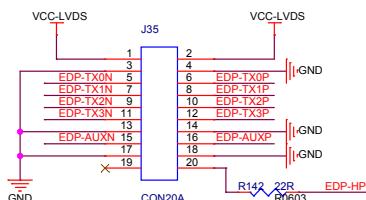


BACKLIGHTC



DS10

DSI1

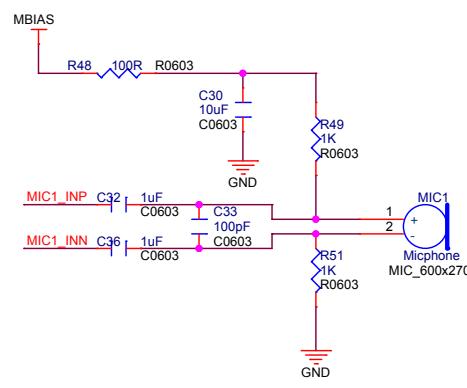
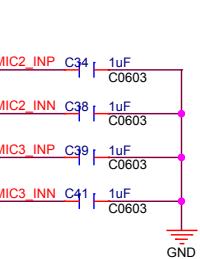


EDP

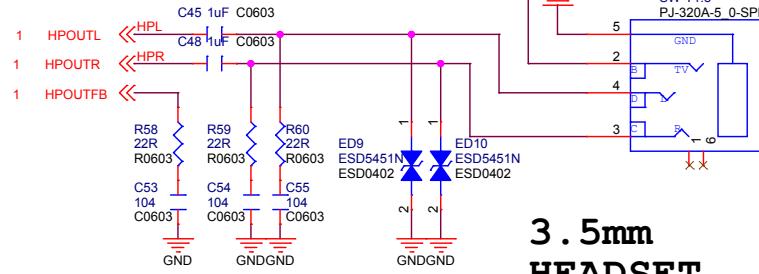
AUDIO

1 LINEOUTLP
1 LINEOUTLN
1 LINEOUTRP
1 LINEOUTRN

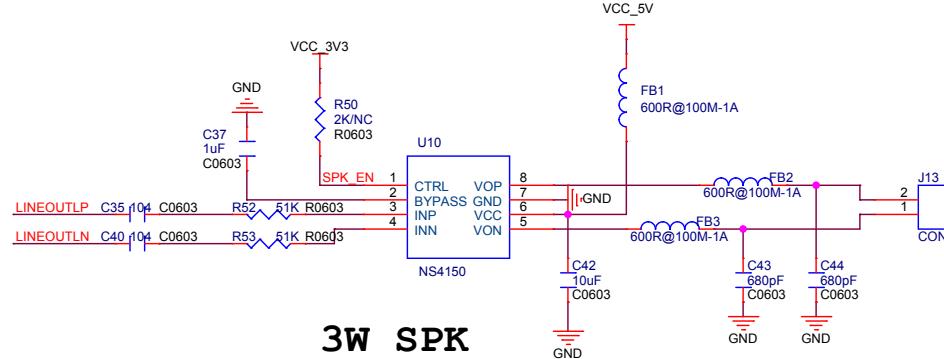
1 MBIAS
1 MICIN1P
1 MICIN1N
1 MICIN2P
1 MICIN2N
1 MICIN3P
1 MICIN3N
1 PA-SHDN



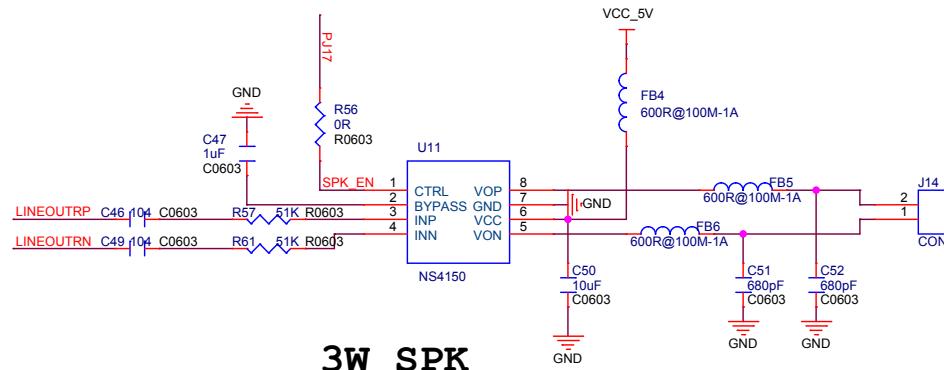
MIC1 IN



**3.5mm
HEADSET**



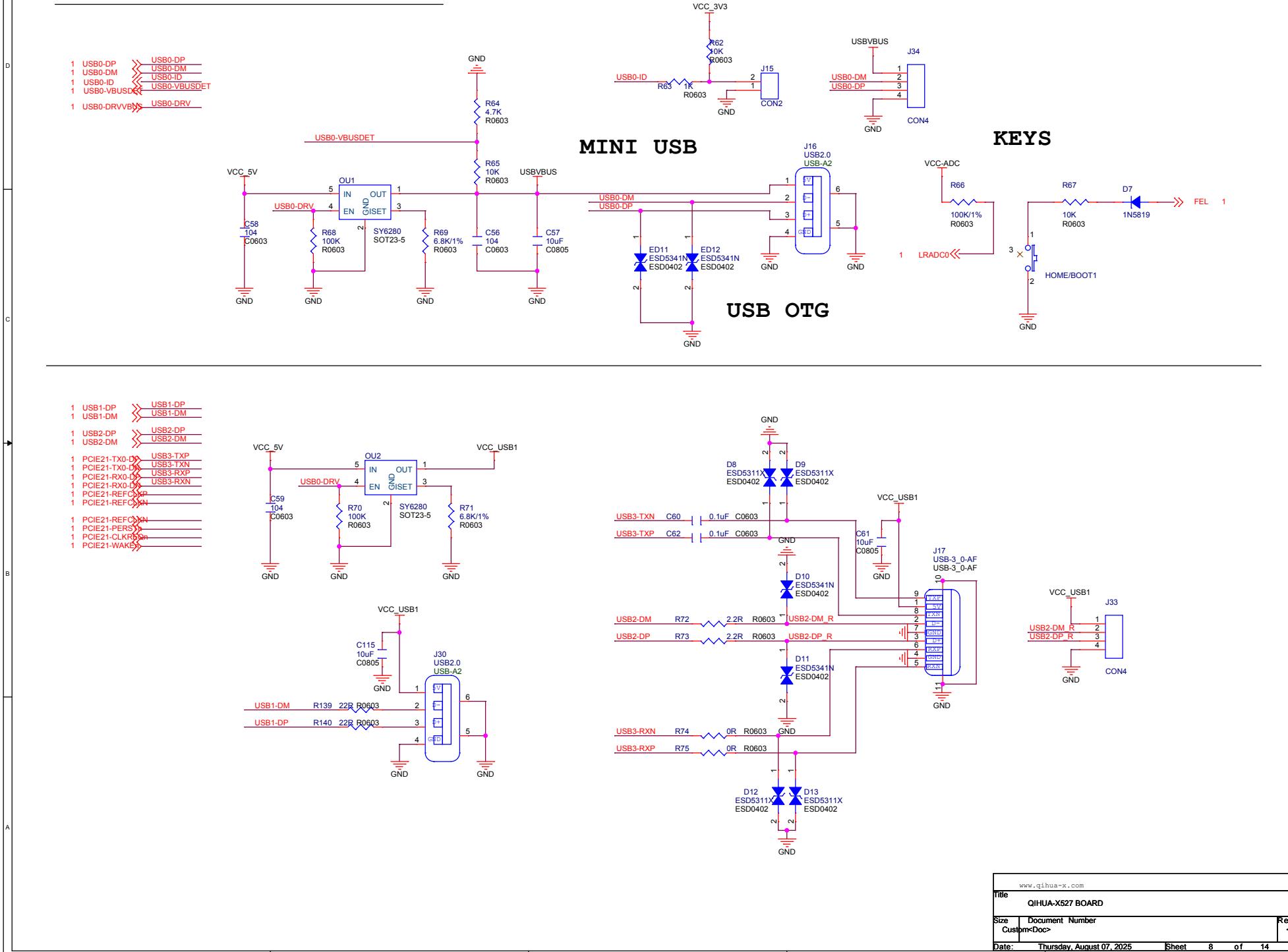
3W SPK



3W SPK

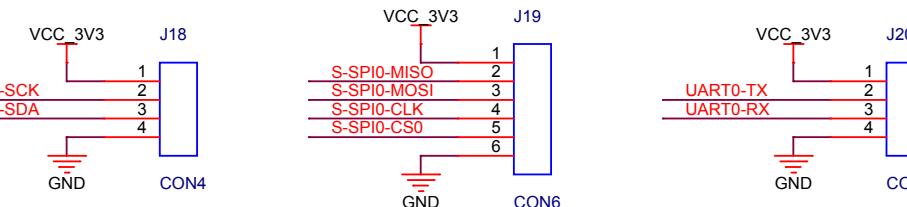
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OTG HUB KEY



UART RS485 I2C SPI ADC GPIO

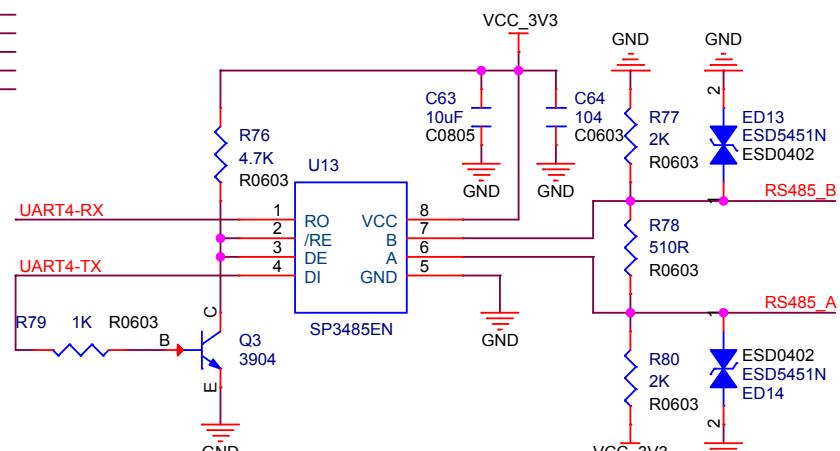
1 S-UART0-TX → S-UART0-TX
 1 S-UART0-RX → S-UART0-RX
 1 UART0-TX → UART0-TX
 1 UART0-RX → UART0-RX
 1 UART2-TX → UART2-TX
 1 UART2-RX → UART2-RX
 1 UART2-RTS → UART2-RTS
 1 UART2-CTS → UART2-CTS
 1 UART3-TX → UART3-TX
 1 UART3-RX → UART3-RX
 1 UART4-TX → UART4-TX
 1 UART4-RX → UART4-RX
 1 UART6-TX → UART6-TX
 1 UART6-RX → UART6-RX
 1 UART7-TX → UART7-TX
 1 UART7-RX → UART7-RX
 1 UART7-RTS → UART7-RTS
 1 UART7-CTS → UART7-CTS
 1 TWI4-SCK → TWI4-SCK
 1 TWI4-SDA → TWI4-SDA
 1 GPADC3 → GPADC3
 1 GPADC4 → GPADC4
 1 GPADC5 → GPADC5
 1 GPADC6 → GPADC6
 1 PGAOUT0 → PGAOUT0
 1 PGAOUT1 → PGAOUT1
 1 S-SPI0-CS0 → S-SPI0-CS0
 1 S-SPI0-MISO → S-SPI0-MISO
 1 S-SPI0-MOSI → S-SPI0-MOSI
 1 S-SPI0-CLK → S-SPI0-CLK
 1 GPIO_0 → GPIO_0
 1 GPIO_1 → GPIO_1
 1 GPIO_2 → GPIO_2
 1 GPIO_3 → GPIO_3
 1 GPIO_4 → GPIO_4



I2C **SPI**



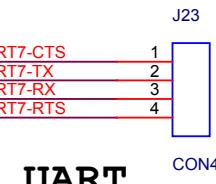
DAC/ADC



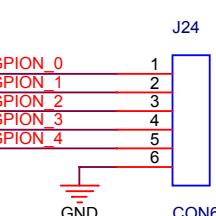
TTL TO RS485



External Connector



UART



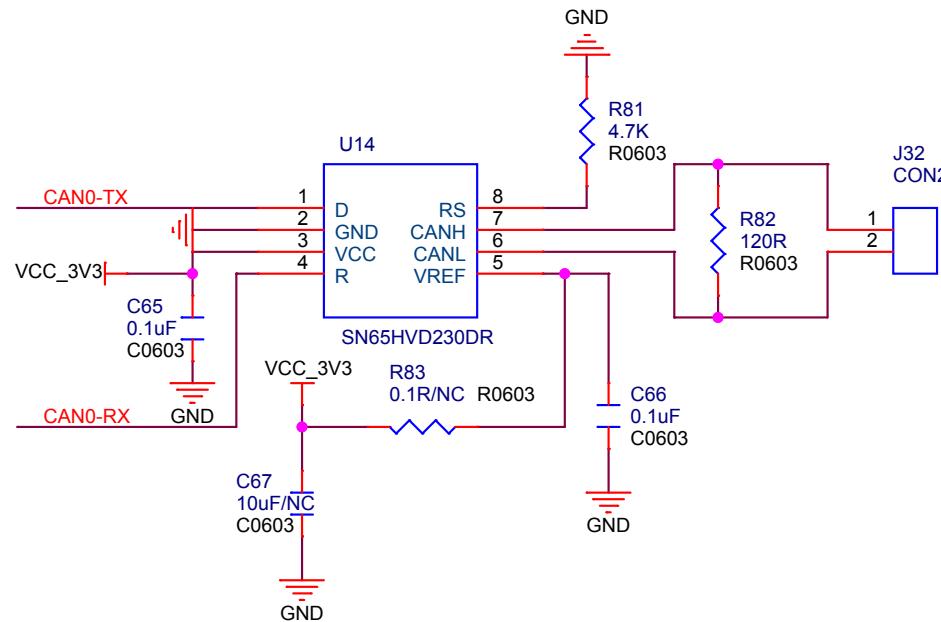
GPIO

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CAN

1 CAN0-TX
1 CAN0-RX
1 CAN0-CPUS-TX
1 CAN0-CPUS-RX

CAN0-TX
CAN0-RX
CAN0-CPUS-TX
CAN0-CPUS-RX



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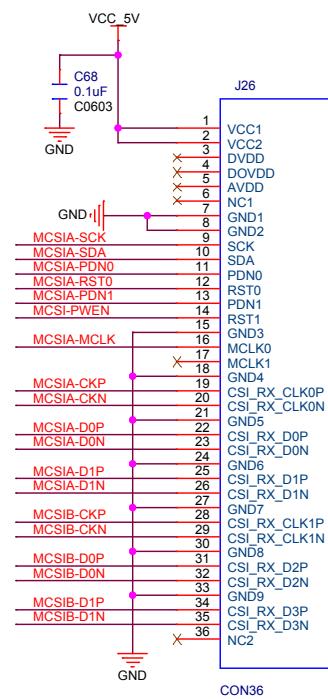
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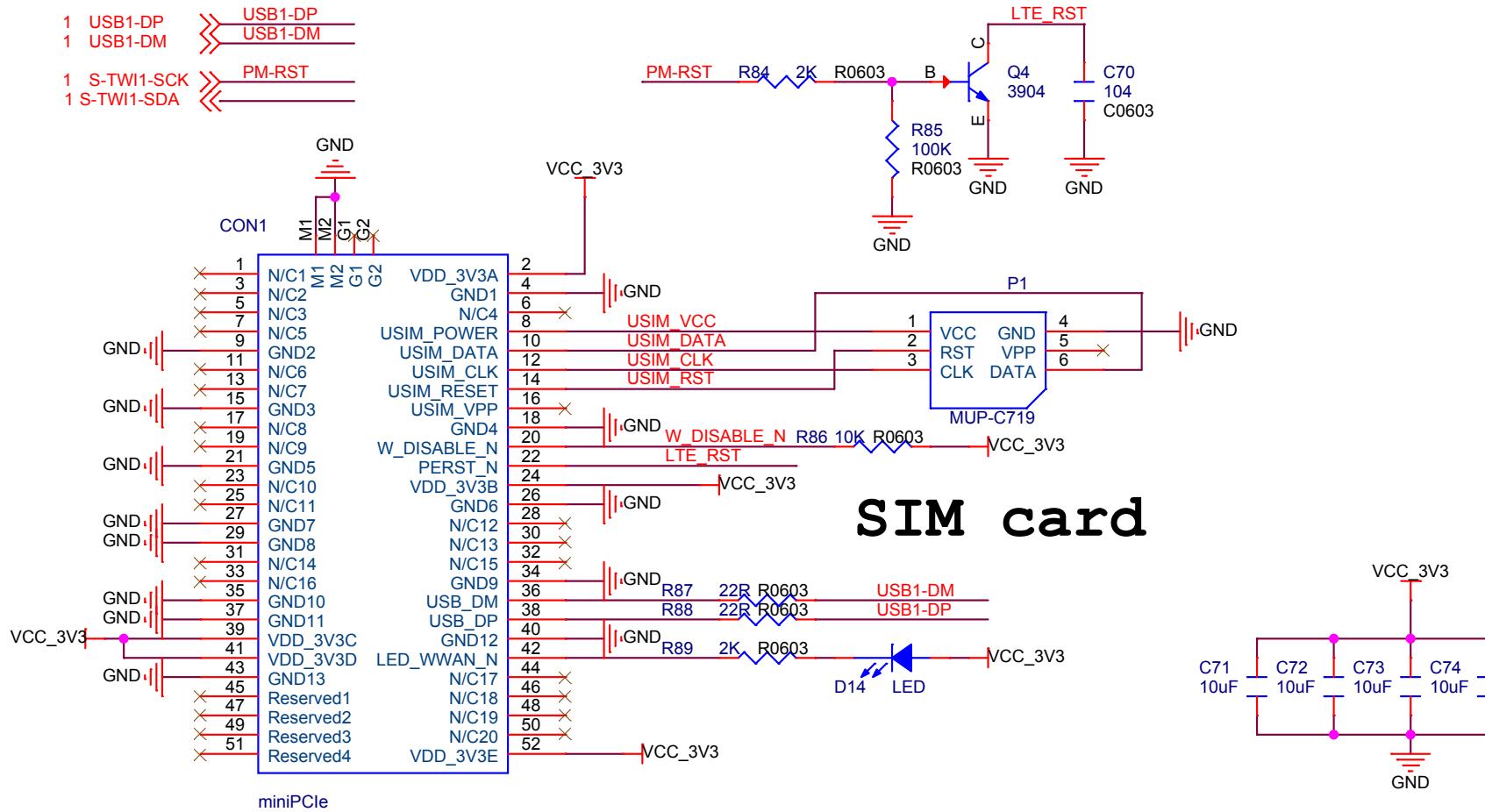
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MIPI CSI

1	MCSIA-D0P	MCSIA-D0P
1	MCSIA-D0N	MCSIA-D0N
1	MCSIA-D1P	MCSIA-D1P
1	MCSIA-D1N	MCSIA-D1N
1	MCSIA-CKP	MCSIA-CKP
1	MCSIA-CKN	MCSIA-CKN
1	MCSIA-CKN	MCSIA-CKN
1	MCSB-D0P	MCSB-D0P
1	MCSB-D0N	MCSB-D0N
1	MCSB-D1P	MCSB-D1P
1	MCSB-D1N	MCSB-D1N
1	MCSB-CKP	MCSB-CKP
1	MCSB-CKN	MCSB-CKN
1	MCSA-MCLK	MCSA-MCLK
1	MCSA-SDA	MCSA-SDA
1	MCSA-SCK	MCSA-SCK
1	MCSIC-D0P	MCSIC-D0P
1	MCSIC-D0N	MCSIC-D0N
1	MCSIC-D1P	MCSIC-D1P
1	MCSIC-D1N	MCSIC-D1N
1	MCSIC-CKP	MCSIC-CKP
1	MCSIC-CKN	MCSIC-CKN
1	MCSID-D0P	MCSID-D0P
1	MCSID-D0N	MCSID-D0N
1	MCSID-D1P	MCSID-D1P
1	MCSID-D1N	MCSID-D1N
1	MCSID-CKP	MCSID-CKP
1	MCSID-CKN	MCSID-CKN
1	MCSIC-MCLK	MCSIC-MCLK
1	MCSIC-SDA	MCSIC-SDA
1	MCSIC-SCK	MCSIC-SCK
1	MCSIA-PDN0	MCSIA-PDN0
1	MCSIA-RST0	MCSIA-RST0
1	MCSIA-PDN1	MCSIA-PDN1
1	MCSIC-PDN0	MCSIC-PDN0
1	MCSIC-PDN1	MCSIC-PDN1
1	MCSI-PWEN	MCSI-PWEN



MINI PCIE 4G

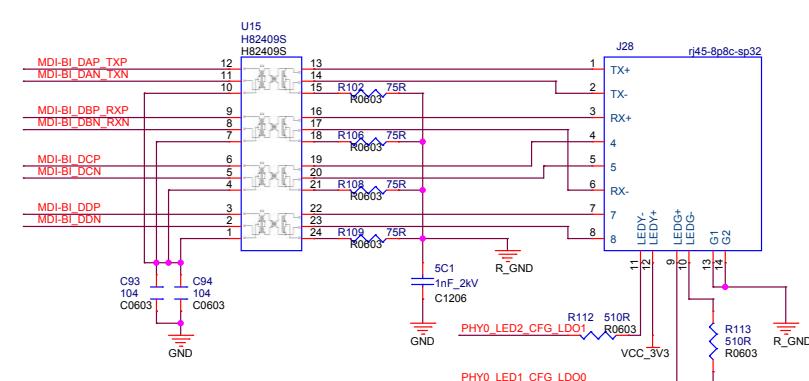
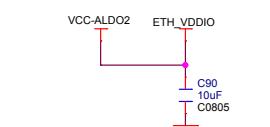
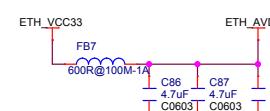
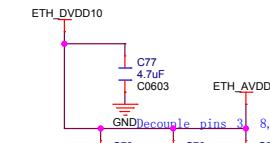
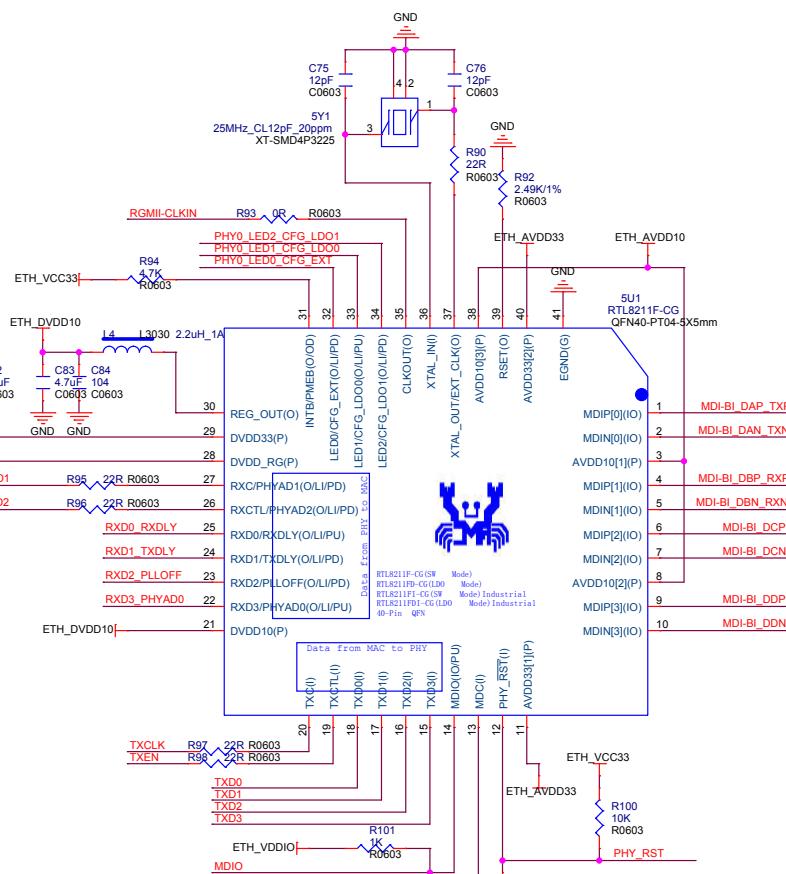


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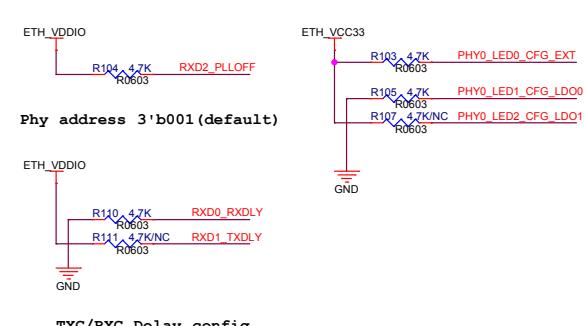
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ETHERNET0

1 RGMII0-RXCK RXCLK_PHYAD1
 1 RGMII0-RXCT RXCTL_PHYAD2
 1 RGMII0-RXD0 RXD0_RXDLY
 1 RGMII0-RXD1 RXD1_RXDLY
 1 RGMII0-RXD2 RXD2_Ploff
 1 RGMII0-RXD3 RXD3_PHYAD0
 1 RGMII0-TXCK TXCLK
 1 RGMII0-TXCT TXEN
 1 RGMII0-TXD0 TXD0
 1 RGMII0-TXD1 TXD1
 1 RGMII0-TXD2 TXD2
 1 RGMII0-TXD3 TXD3
 1 RGMII0-MDO MDIO
 1 RGMII0-MDC MDC
 1 PHY_RST PHY_RST
 1 RGMII-CLKIN RGMII-CLKIN
 1 EPHY-CLK-25M EPHY-CLK-25M

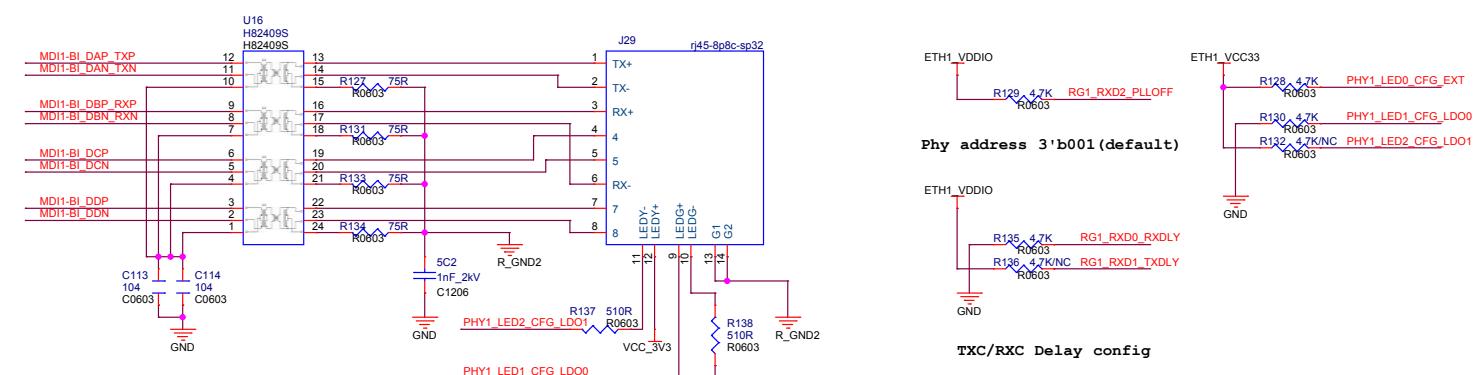
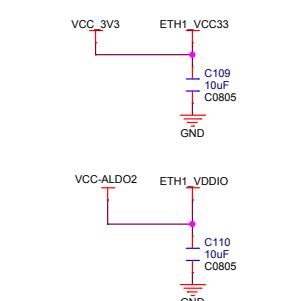
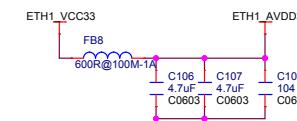
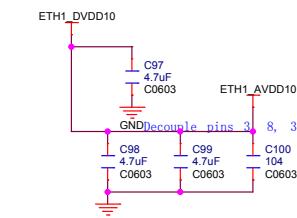
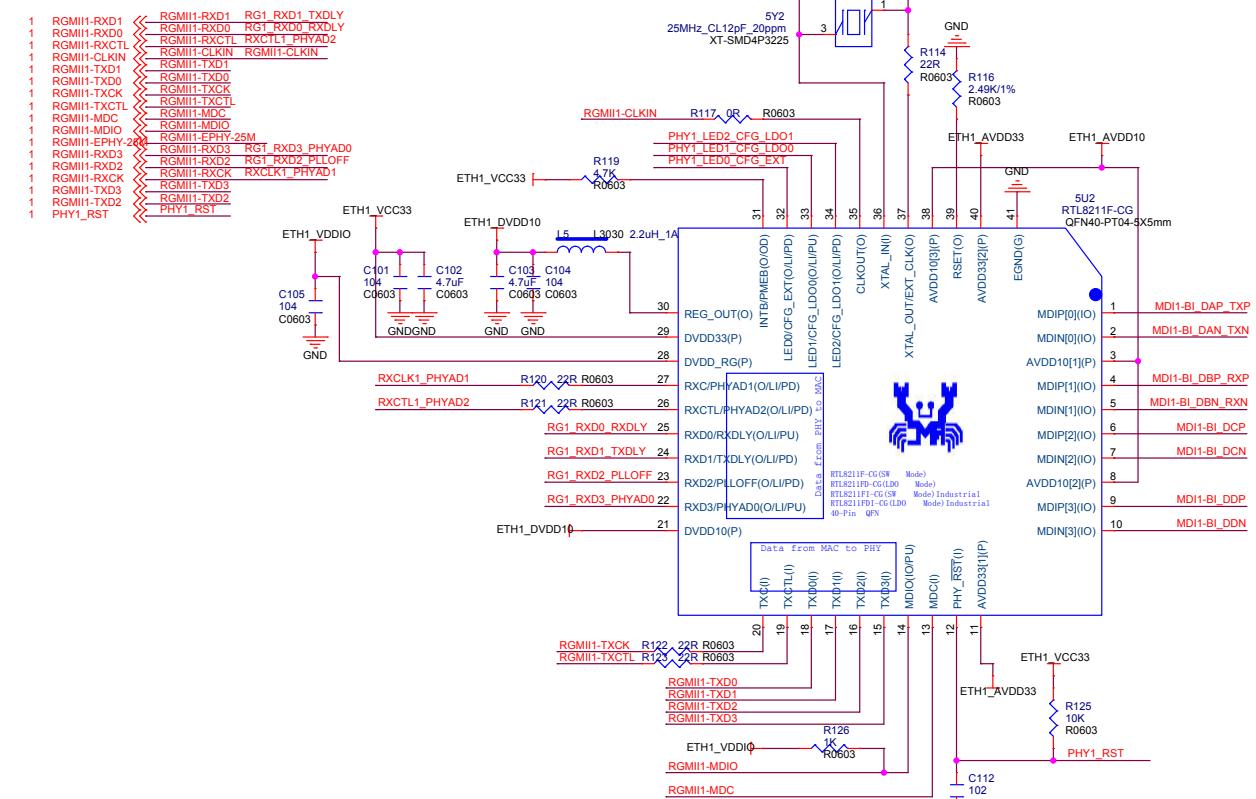


1000M RJ45



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ETHERNET1



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