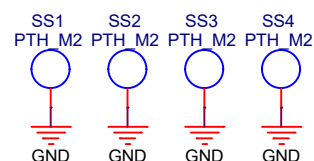


Pin connection diagram for J2:

Pin	Signal
1	VDD 5V
2	I2C_SDA
3	I2C_SCL
4	GND
5	MIPI_CSI_DP
6	MIPI_CSI_DN
7	GND
8	MIPI_CSI_CLKP
9	MIPI_CSI_CLKN
10	GND
11	MIPI_CSI_DP
12	MIPI_CSI_DN
13	X
14	GND
15	PWDN
16	X
17	X
18	X
19	X
20	X
21	X
22	RSTn
23	
24	
25	
26	

Additional components and labels:

- SH1, SH2
- FPC 0P5 24P D
- 定位孔 (Positioning Hole)
- SS1, SS2, SS3
- PTH M2



电平转换

The diagram illustrates the level conversion circuit for I2C signals. It shows two main sections: I2C_SDA and I2C_SCL. Each section has a 3V3 supply (VDD_3V3) and a 1V8 supply (DOVDD_1V8). The I2C_SDA section uses two 4.7K resistors (R9, R10) connected to 3V3 and two 4.7K resistors (R11, R12) connected to 1V8. The I2C_SCL section uses two 10K resistors (R13, R14) connected to 3V3 and two 10K resistors (R5, R6) connected to 1V8. The I2C_SDA section also includes two 2SK3018 MOSFETs (Q1, Q2) connected to the 1V8 supply and the I2C_SDA signal line. The I2C_SCL section includes two 2SK3018 MOSFETs (Q1, Q2) connected to the 1V8 supply and the I2C_SCL signal line. The I2C_SDA signal line is connected to the CAM_I2C_SDA signal line. The I2C_SCL signal line is connected to the CAM_I2C_SCL signal line. The RSTn signal line is connected to the CAM_RSTn signal line. The PWDN signal line is connected to the CAM_PWDN signal line.

AF_2V8 AVDD_2V8

C17 0.1uF C18 0.1uF

C0402 50V C0402 50V

GND GND

AVDD_2V8

CAM_I2C_SDA

CAM_I2C_SCL

CAM_RSTn

DVDD_1V2

DOVDD_1V8

DOVDD_1V8 DVDD_1V2

C20 0.1uF C15 0.1uF

C0402 50V C0402 50V

GND GND

AF_2V8

CAM_RSTn

C8 NC

C0402 NC

GND

J1

1	STROBE
2	AGND
3	SDA
4	AVDD
5	SCL
6	RESET
7	NC
8	PWDn
9	NC
10	DVDD1V2
11	DOVDD1V8
12	MDP1
13	MCLK
14	MDN1
15	DGND
16	MCP
17	NC
18	MCN
19	NC
20	MDP0
21	NC
22	MDN0
23	AFVDD
24	AFGND
25	
26	

SH1
SH2

FPC_0P5_24P_D

Pin connection diagram for the AXE630124D module. The diagram shows a 34-pin connector with pins numbered 1 to 34. Pins 1, 3, 5, 7, 9, 11, 13, 15, 17, 19, 21, 23, 25, 27, 29, 31, and 33 are connected to GND. Pins 2, 4, 6, 8, 10, 12, 14, 16, 18, 20, 22, 24, 26, 28, 30, 32, and 34 are connected to various signals: MIPI_CSI_DP0, MIPI_CSI_DN0, MIPI_CSI_DP1, MIPI_CSI_DN1, MIPI_CSI_CLKP, MIPI_CSI_CLKN, I2C_SCL, I2C_SDA, PWDN, RSTn, SH1, SH2, SH3, and SH4. Pins 21, 23, 25, 27, 29, 31, and 33 are marked with an 'X' and are not connected.

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