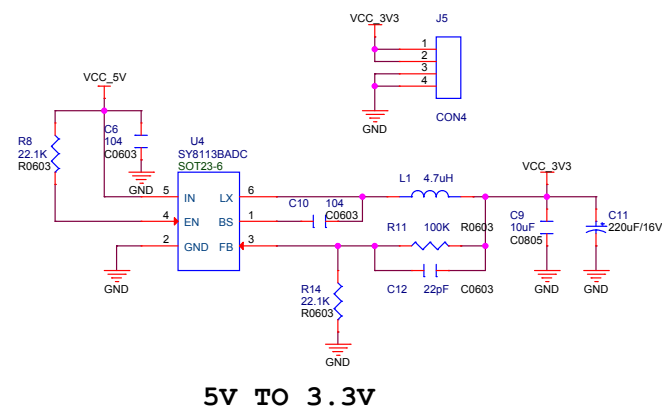
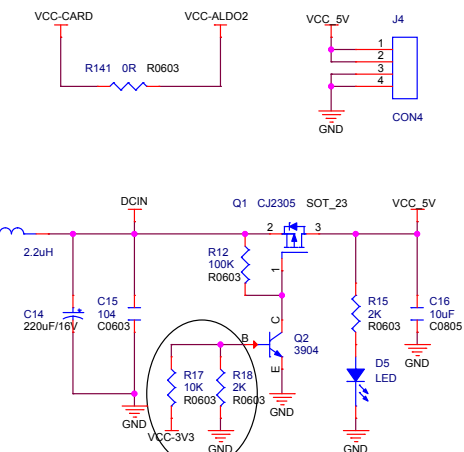
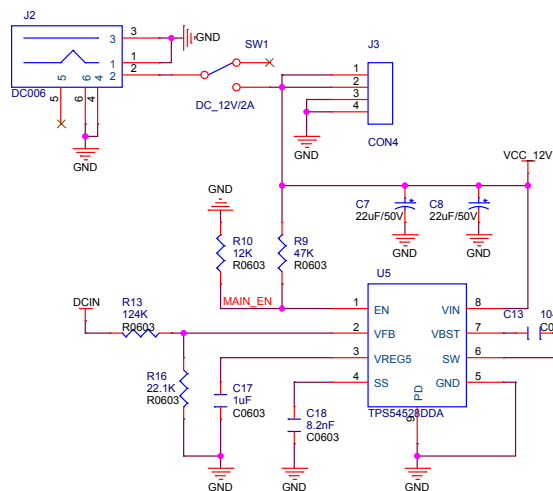
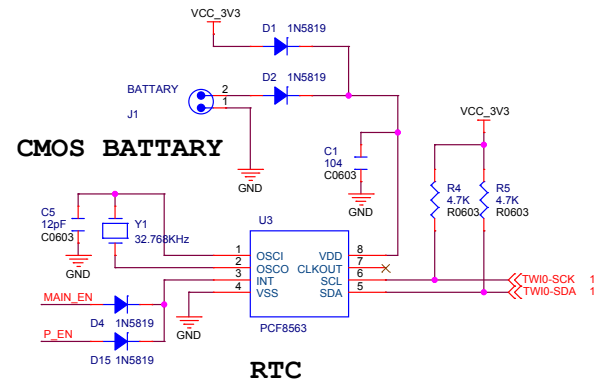
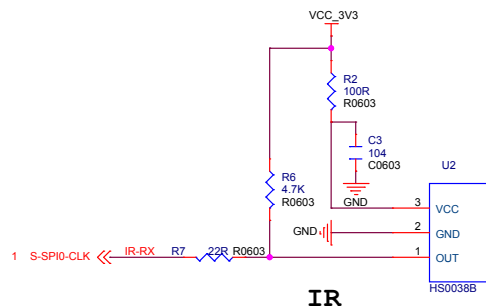
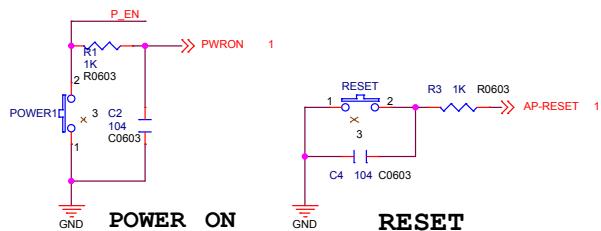


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POWER RTC IR



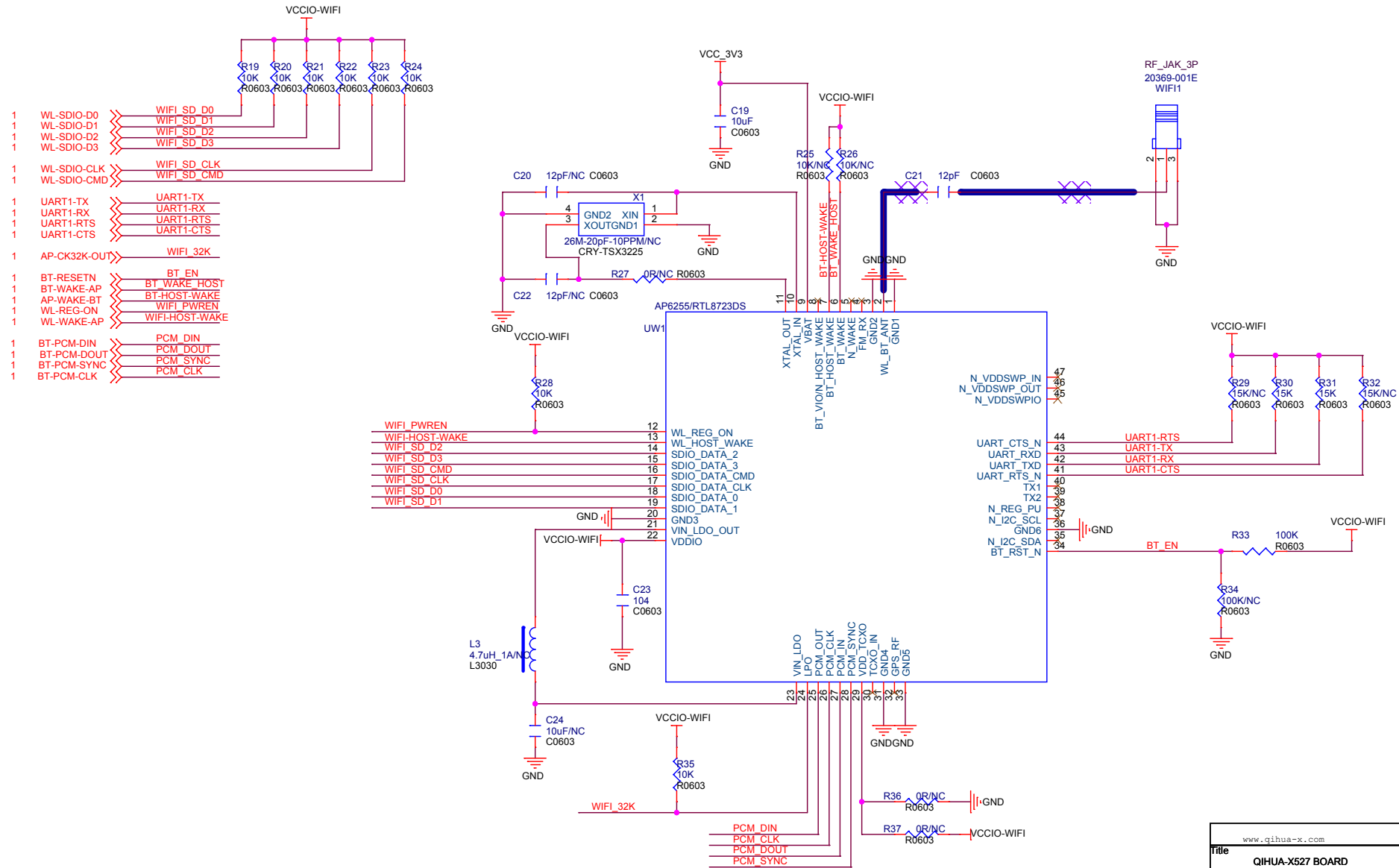
12V DC POWER INPUT

POWER OUTPUT 5V

5V TO 3.3V

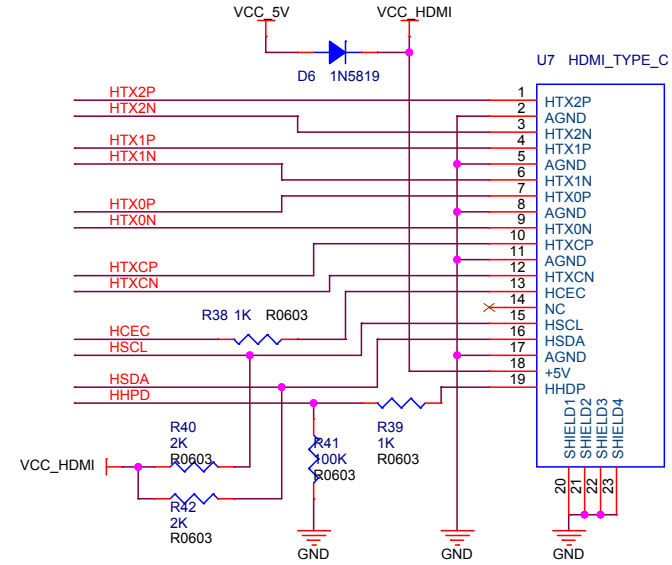
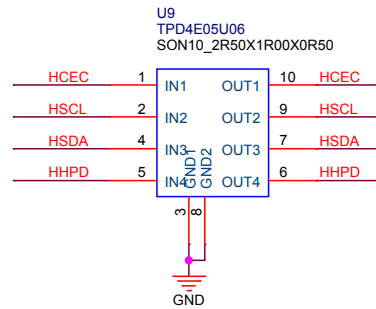
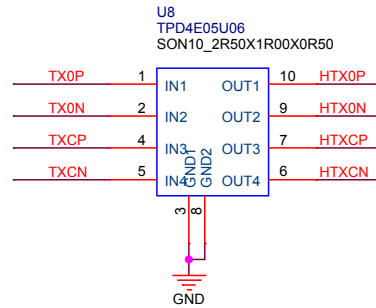
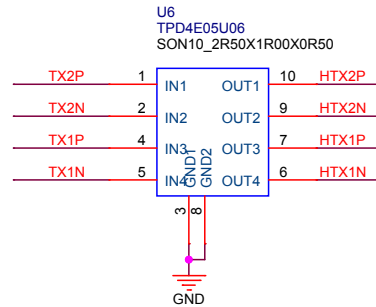
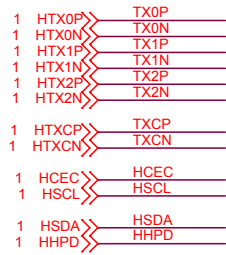
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WIFI + BT



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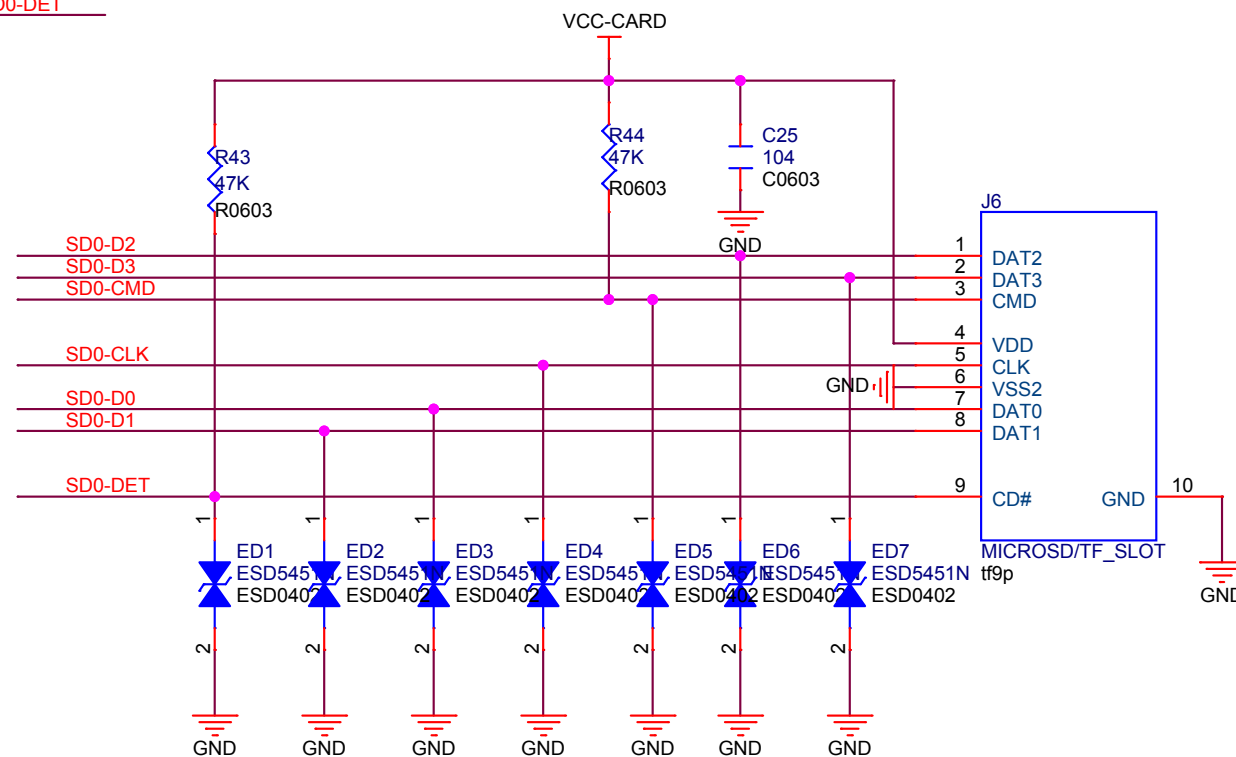
HDMI



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TF CARD

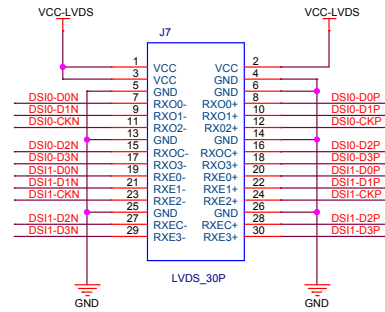
1	SDC0-D0	SD0-D0
1	SDC0-D1	SD0-D1
1	SDC0-D2	SD0-D2
1	SDC0-D3	SD0-D3
1	SDC0-CMD	SD0-CMD
1	SDC0-CLK	SD0-CLK
1	SDC0-DET	SD0-DET



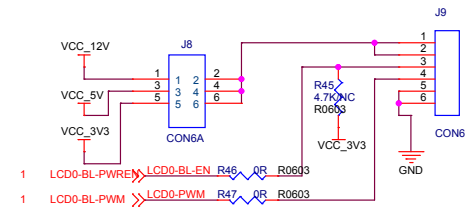
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LCD PHONE

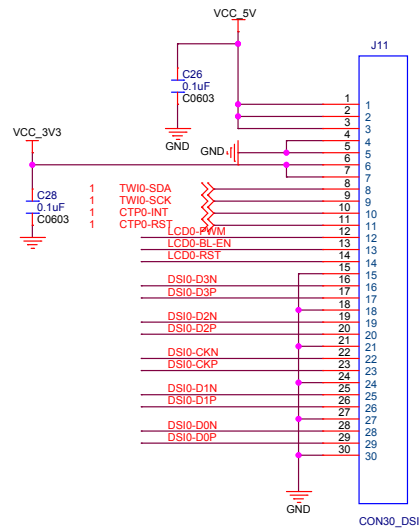
1	LVDS0-D0P	DSI0-D0P
1	LVDS0-D0N	DSI0-D0N
1	LVDS0-D1P	DSI0-D1P
1	LVDS0-D1N	DSI0-D1N
1	LVDS0-D2P	DSI0-D2P
1	LVDS0-D2N	DSI0-D2N
1	LVDS0-CKP	DSI0-CKP
1	LVDS0-CKN	DSI0-CKN
1	LVDS0-D3P	DSI0-D3P
1	LVDS0-D3N	DSI0-D3N
1	LVDS1-D0P	DSI1-D0P
1	LVDS1-D0N	DSI1-D0N
1	LVDS1-D1P	DSI1-D1P
1	LVDS1-D1N	DSI1-D1N
1	LVDS1-D2P	DSI1-D2P
1	LVDS1-D2N	DSI1-D2N
1	LVDS1-CKP	DSI1-CKP
1	LVDS1-CKN	DSI1-CKN
1	LVDS1-D3P	DSI1-D3P
1	LVDS1-D3N	DSI1-D3N
1	LCD0-RST	LCD0-RST
1	LCD1-RST	LCD1-RST
1	EDP-TX3P	EDP-TX3P
1	EDP-TX3N	EDP-TX3N
1	EDP-TX2P	EDP-TX2P
1	EDP-TX2N	EDP-TX2N
1	EDP-TX1P	EDP-TX1P
1	EDP-TX1N	EDP-TX1N
1	EDP-TX0P	EDP-TX0P
1	EDP-TX0N	EDP-TX0N
1	EDP-AUXP	EDP-AUXP
1	EDP-AUXN	EDP-AUXN
1	EDP-HPD	EDP-HPD



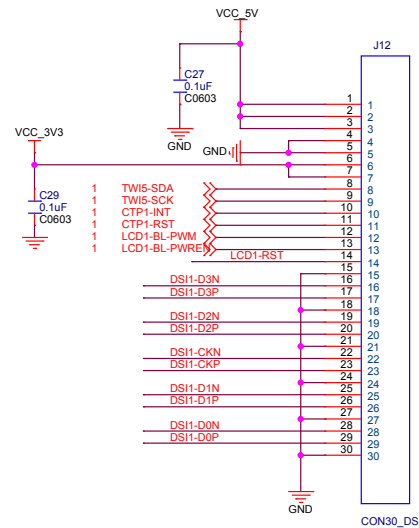
LVDS0



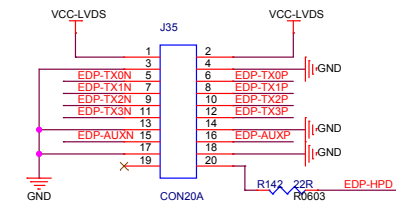
BACKLIGHT0



DSI0



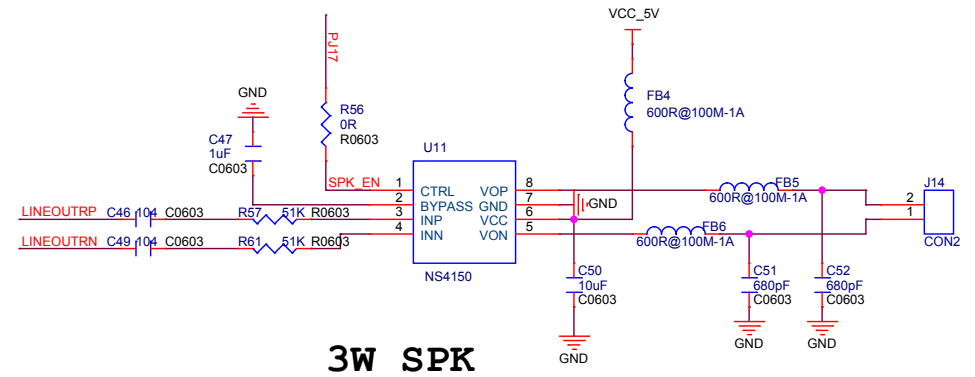
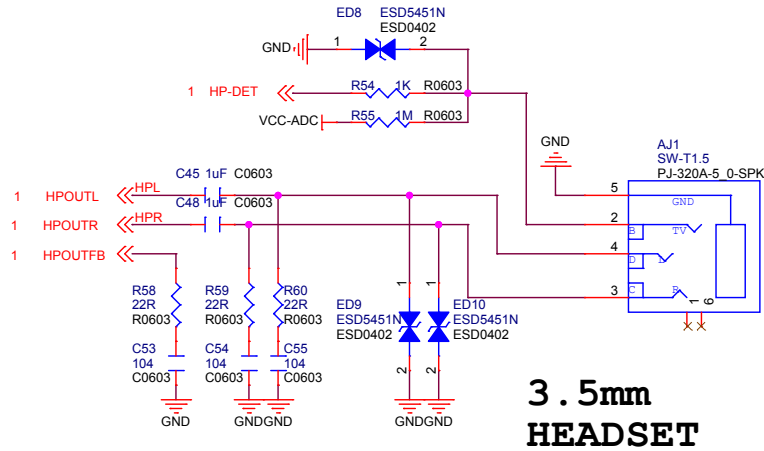
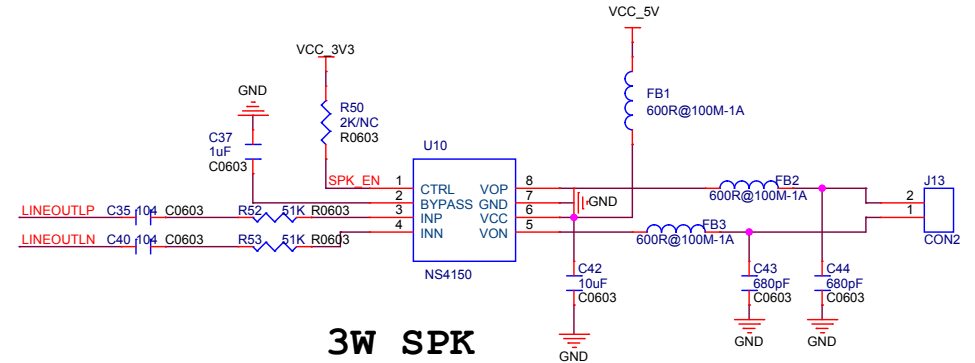
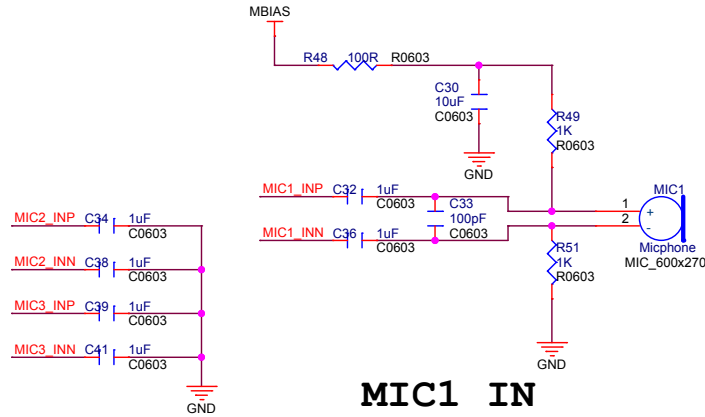
DSI1



EDP

AUDIO

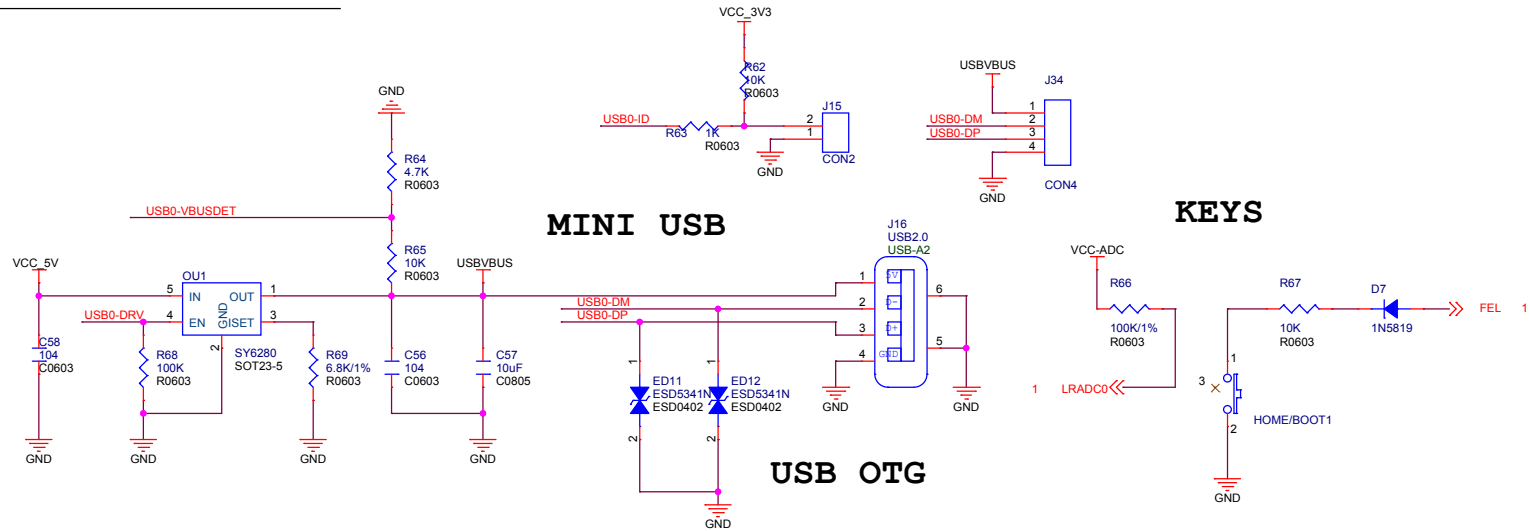
- 1 LINEOUTLP << LINEOUTLP
- 1 LINEOUTLN << LINEOUTLN
- 1 LINEOUTRP << LINEOUTRP
- 1 LINEOUTRN << LINEOUTRN
- 1 MBIAS << MIC1_INP
- 1 MICIN1P << MIC1_INN
- 1 MICIN1N << MIC2_INP
- 1 MICIN2P << MIC2_INN
- 1 MICIN2N << MIC3_INP
- 1 MICIN3P << MIC3_INN
- 1 MICIN3N << PJ17
- 1 PA-SHDN << PJ17



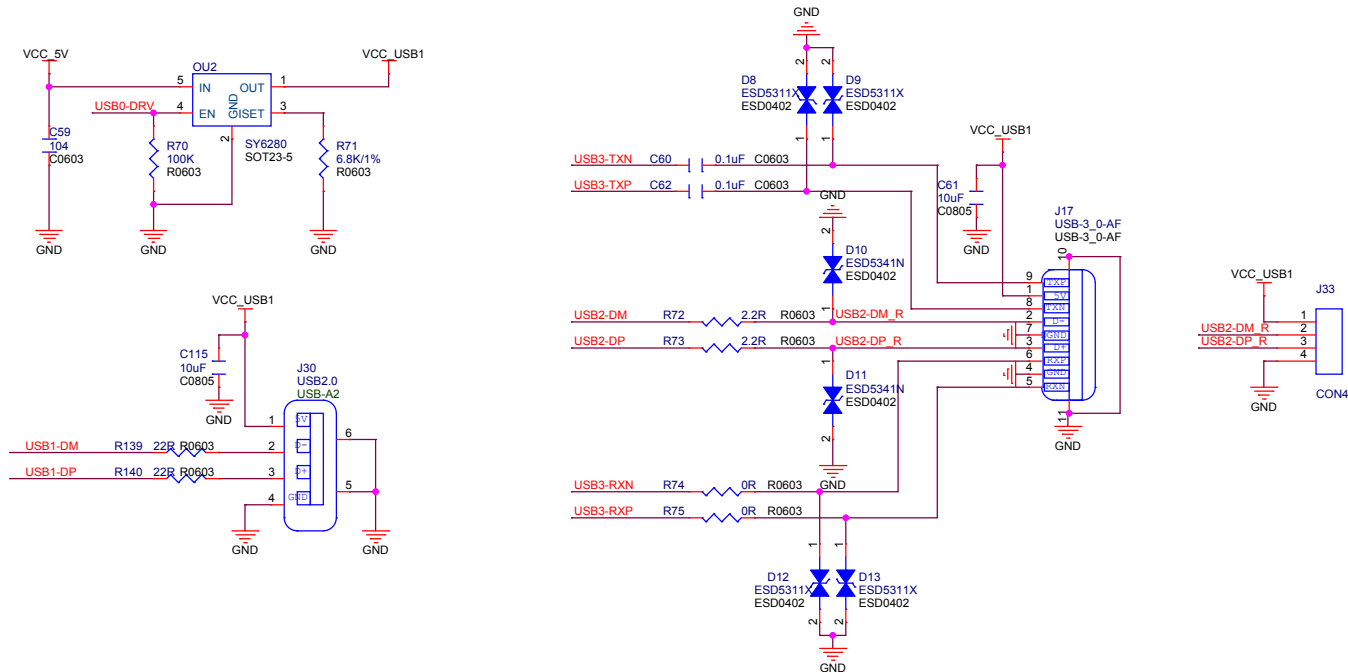
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OTG HUB KEY

- 1 USB0-DP >>> USB0-DP
- 1 USB0-DM >>> USB0-DM
- 1 USB0-ID >>> USB0-ID
- 1 USB0-VBUSDET >>> USB0-VBUSDET
- 1 USB0-DRV >>> USB0-DRV



- 1 USB1-DP >>> USB1-DP
- 1 USB1-DM >>> USB1-DM
- 1 USB2-DP >>> USB2-DP
- 1 USB2-DM >>> USB2-DM
- 1 PCIE21-TX0-D >>> USB3-TXP
- 1 PCIE21-TX0-D >>> USB3-TXN
- 1 PCIE21-RX0-D >>> USB3-RXP
- 1 PCIE21-RX0-D >>> USB3-RXN
- 1 PCIE21-REFCLKP >>> PCIE21-REFCLKP
- 1 PCIE21-REFCLKN >>> PCIE21-REFCLKN
- 1 PCIE21-PERSYS >>> PCIE21-PERSYS
- 1 PCIE21-CLKREQ >>> PCIE21-CLKREQ
- 1 PCIE21-WAKE >>> PCIE21-WAKE



UART RS485 I2C SPI ADC GPIO

1 S-UART0-TX <> S-UART0-TX
1 S-UART0-RX <> S-UART0-RX

1 UART0-TX <> UART0-TX
1 UART0-RX <> UART0-RX
1 UART2-TX <> UART2-TX
1 UART2-RX <> UART2-RX
1 UART2-RTS <> UART2-RTS
1 UART2-CTS <> UART2-CTS
1 UART3-TX <> UART3-TX
1 UART3-RX <> UART3-RX
1 UART4-TX <> UART4-TX
1 UART4-RX <> UART4-RX
1 UART6-TX <> UART6-TX
1 UART6-RX <> UART6-RX
1 UART7-TX <> UART7-TX
1 UART7-RX <> UART7-RX
1 UART7-RTS <> UART7-RTS
1 UART7-CTS <> UART7-CTS

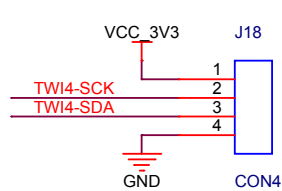
1 TWI4-SCK <> TWI4-SCK
1 TWI4-SDA <> TWI4-SDA

1 GPADC3 <> GPADC3
1 GPADC4 <> GPADC4
1 GPADC5 <> GPADC5
1 GPADC6 <> GPADC6

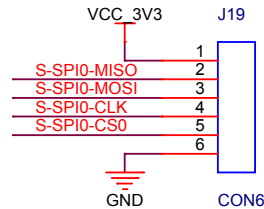
1 PGAOUT0 <> PGAOUT0
1 PGAOUT1 <> PGAOUT1

1 S-SPI0-CS0 <> S-SPI0-CS0
1 S-SPI0-MISO <> S-SPI0-MISO
1 S-SPI0-MOSI <> S-SPI0-MOSI
1 S-SPI0-CLK <> S-SPI0-CLK

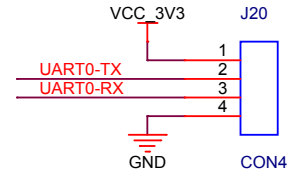
1 GPION_0 <> GPION_0
1 GPION_1 <> GPION_1
1 GPION_2 <> GPION_2
1 GPION_3 <> GPION_3
1 GPION_4 <> GPION_4



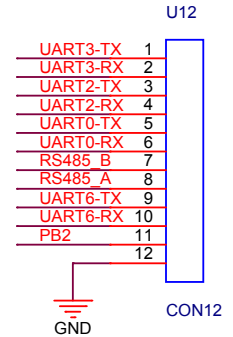
I2C



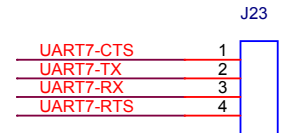
SPI



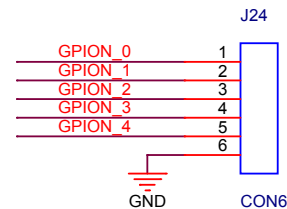
DEBUG



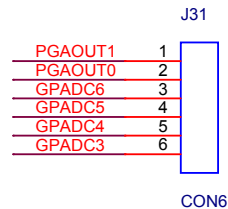
External Connector



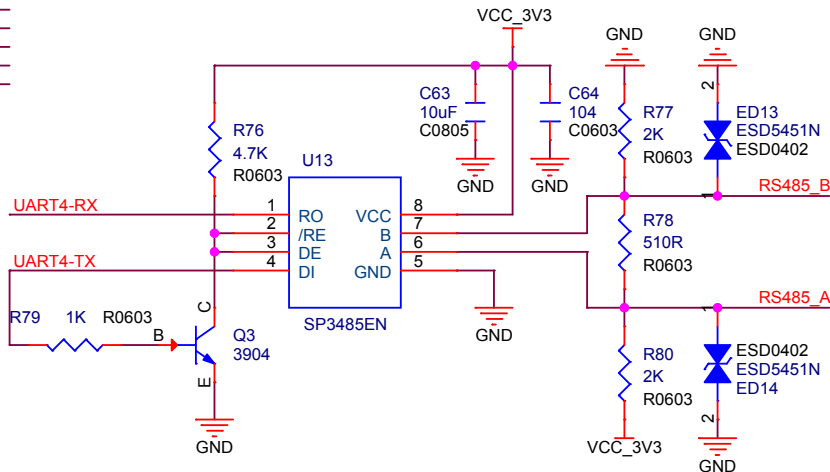
UART



GPIO



DAC/ADC

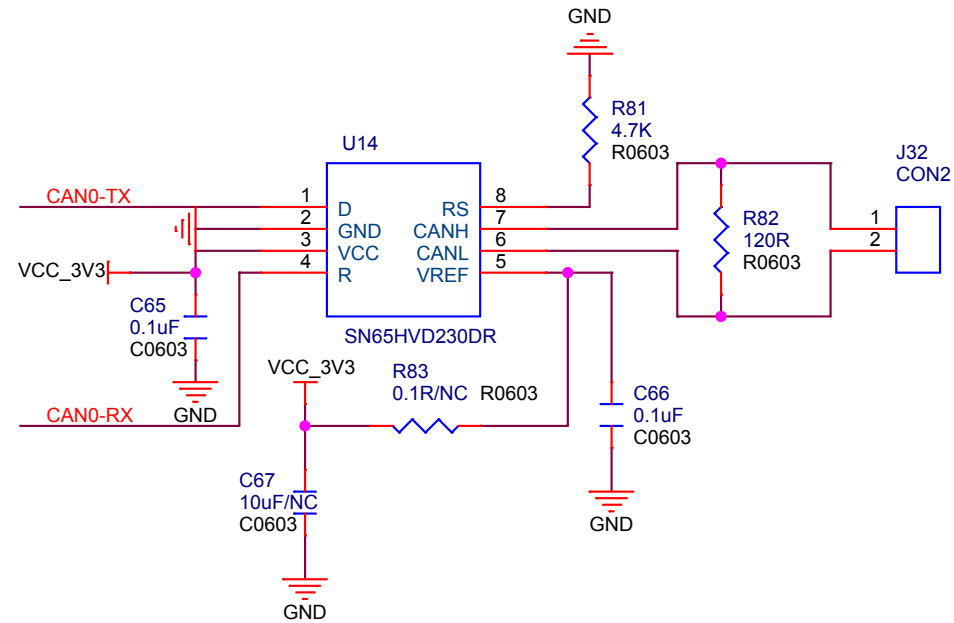


TTL TO RS485

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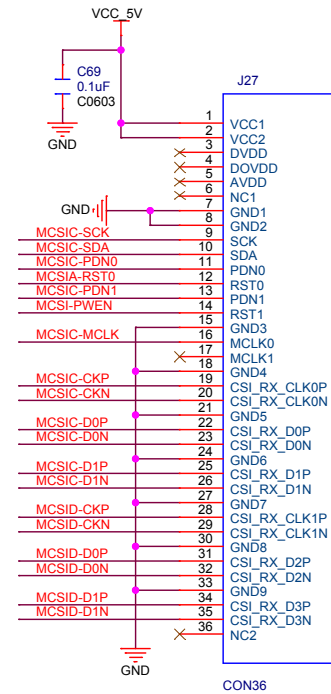
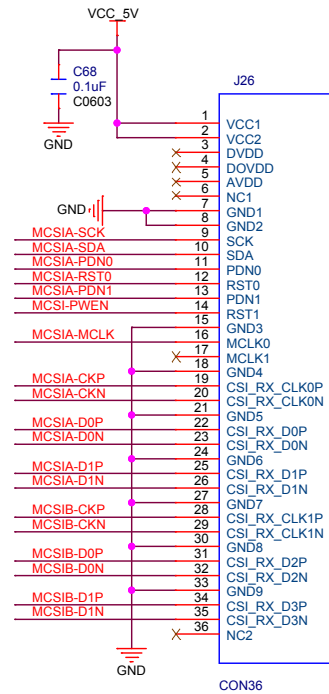
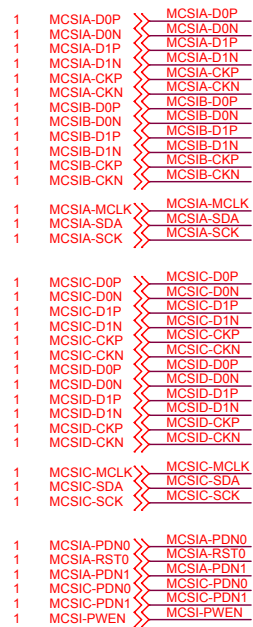
CAN

1 CAN0-TX
1 CAN0-RX
1 CAN0-CPUS-TX
1 CAN0-CPUS-RX

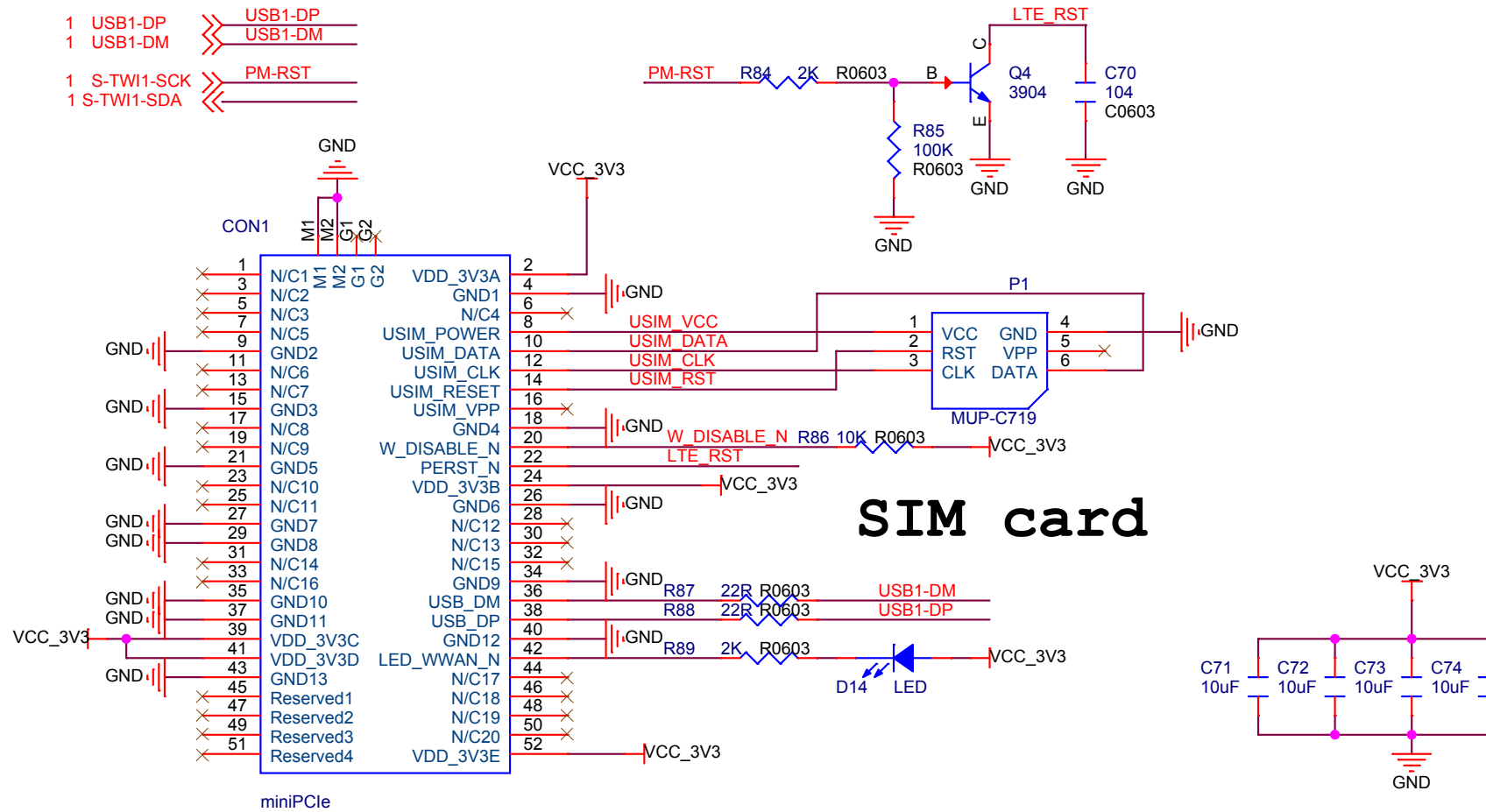


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MIPI CSI



MINI PCIE 4G

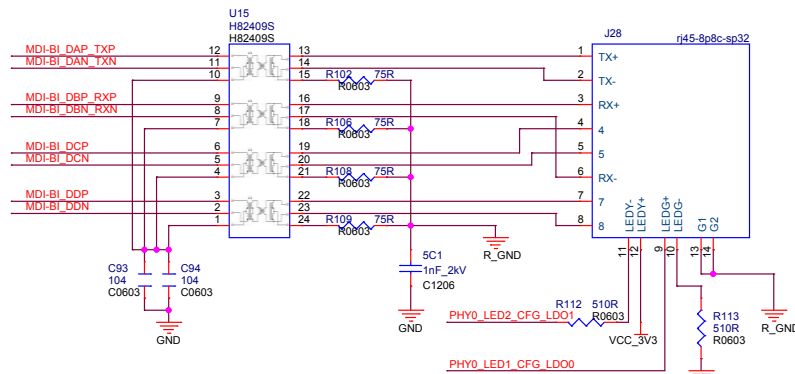
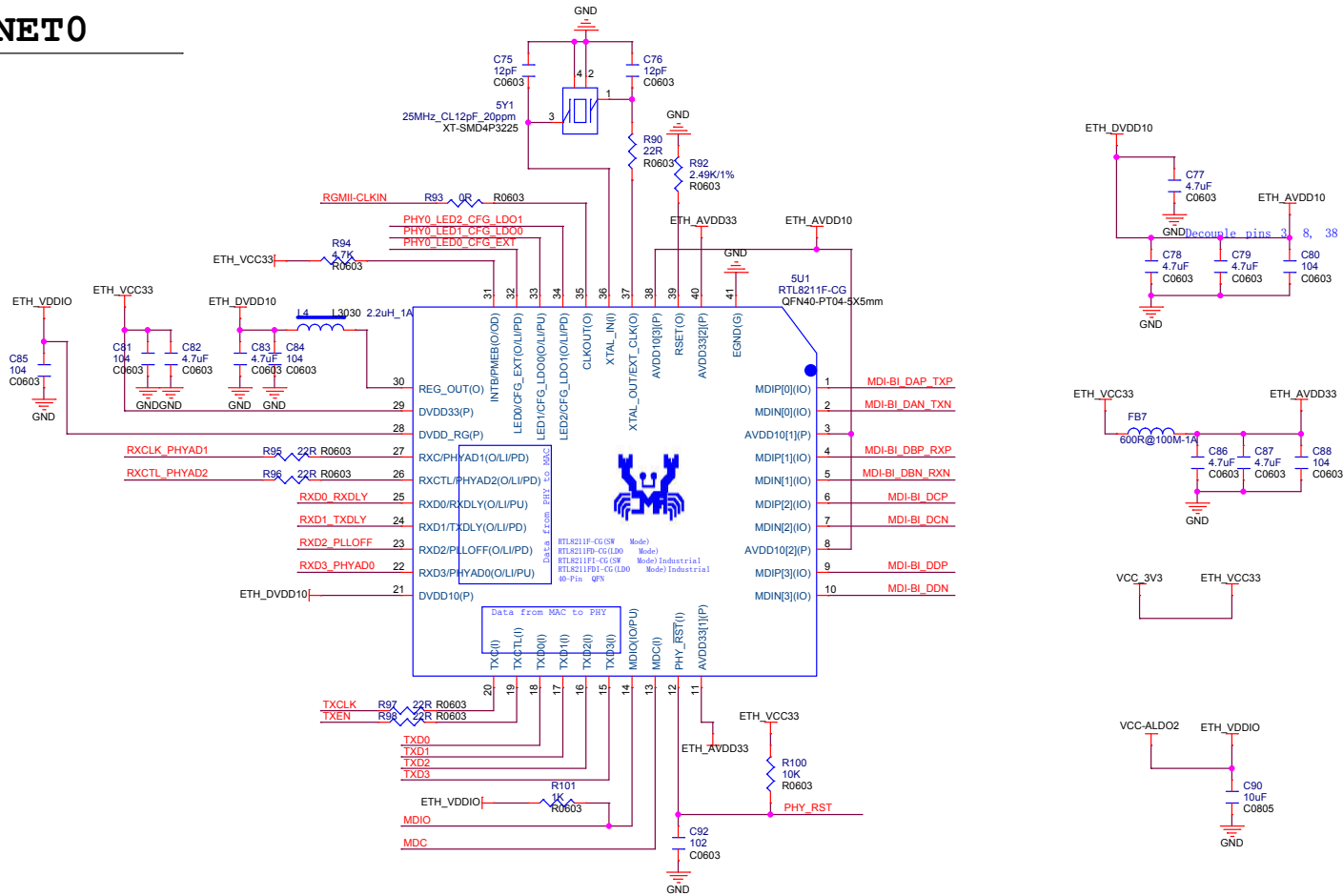


MINI PCIE

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ETHERNET0

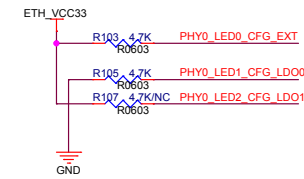
- 1 RGMII0-RXCK>> RXCLK_PHYAD1
- 1 RGMII0-RXCT>> RXCTL_PHYAD2
- 1 RGMII0-RXD0>> RXD0_RXDLY
- 1 RGMII0-RXD1>> RXD1_TXDLY
- 1 RGMII0-RXD2>> RXD2_PLLOFF
- 1 RGMII0-RXD3>> RXD3_PHYAD0
- 1 RGMII0-TXCK>> TXCLK
- 1 RGMII0-TXCT>> TXEN
- 1 RGMII0-TXD0>> TXD0
- 1 RGMII0-TXD1>> TXD1
- 1 RGMII0-TXD2>> TXD2
- 1 RGMII0-TXD3>> TXD3
- 1 RGMII0-MDIO>> MDIO
- 1 RGMII0-MDC>> MDC
- 1 PHY0_RST>> PHY_RST
- 1 RGMII0-CLKIN>> RGMII-CLKIN
- 1 EPHY-CLK-25M>>



1000M RJ45

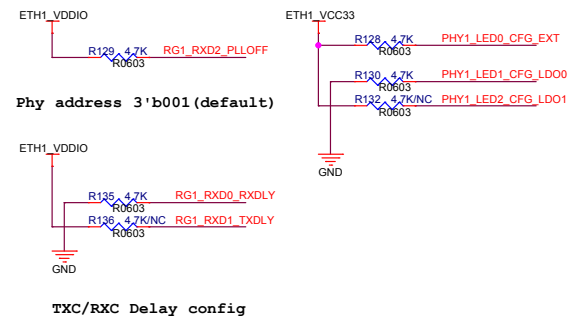
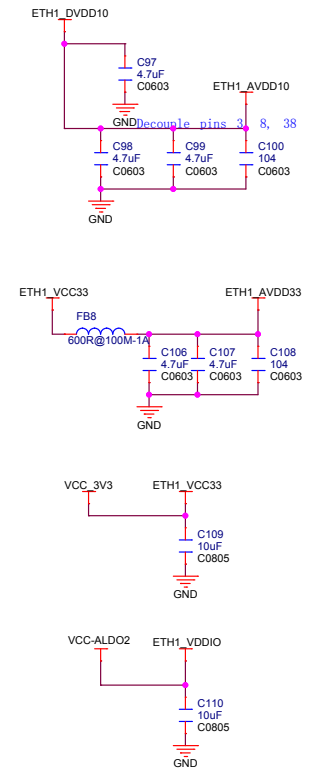
Phy address 3'b001 (default)

TXC/RXC Delay config



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1	RGMI1-RXD1	RG1	RXD1	TXD1
1	RGMI1-RXD0	RG1	RXD0	TXD0
1	RGMI1-RXCTL	RG1	TXCTL	PHY2D
1	RGMI1-CLKIN	RG1	CLKIN	CLKIN-CLKIN
1	RGMI1-TXD1	RG1	TXD1	
1	RGMI1-TXD0	RG1	TXD0	
1	RGMI1-TXCK	RG1	TXCK	
1	RGMI1-TXCTL	RG1	TXCTL	
1	RGMI1-MDC	RG1	MDC	
1	RGMI1-MDIO	RG1	MDIO	
1	RGMI1-EPHY20	RG1	EPHY25M	
1	RGMI1-RXD3	RG1	RXD3	PHY4D
1	RGMI1-RXD2	RG1	RXD2	PXIO P1T0
1	RGMI1-RXD1	RG1	RXD1	PHY1D
1	RGMI1-RXD0	RG1	RXD0	
1	RGMI1-TXD3	RG1	TXD3	
1	RGMI1-TXD2	RG1	TXD2	
1	PHY1_RST	PHY1	RST	



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