

MLCS 2020

# Separation Logic

Symposium - Mathematical Logic for Computer Science

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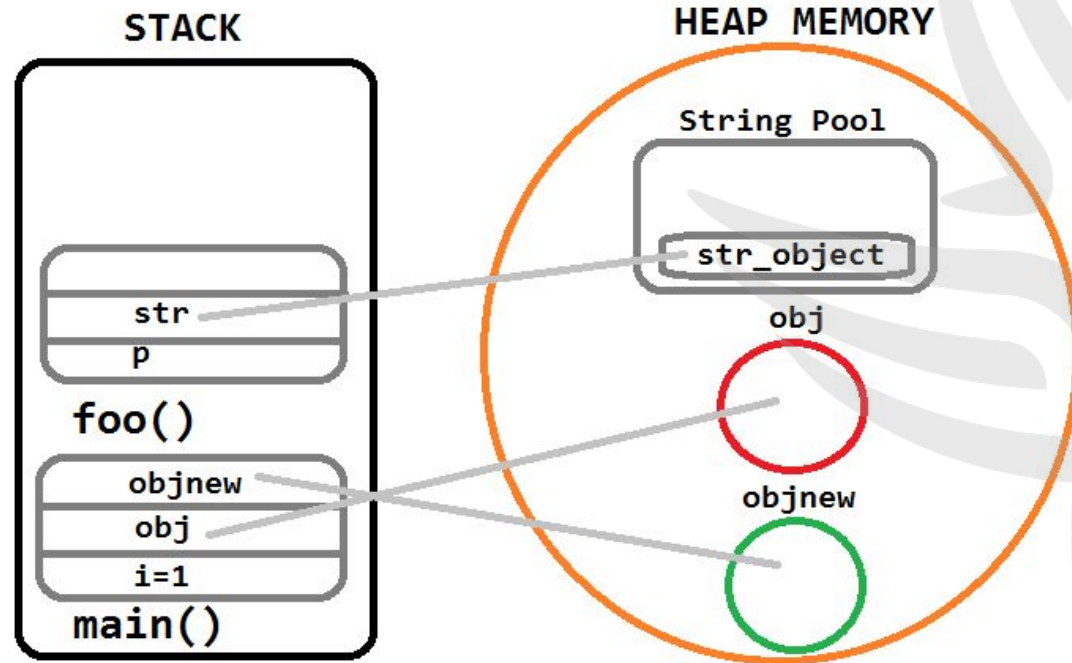
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# Recap - Stack & Heap



# Introduction

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Separation Logic (a.k.a. S.L.) is a logic framework that allow us to reason over an abstract machine which its “state” is made by store and heap.

In other words, S.L. handles the issue regarding the pointer in a language such as C, because the standard Hoare Logic (a.k.a. HL) was designed to deal just with a static memory, the dynamic memory (heap) wasn't considered.

# Hoare Logic

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To be more precise S.L. is an extension of H.L.

## What is Hoare Logic?

is a formal system with a set of logical rules for reasoning rigorously about the correctness of computer programs.



# IMP - A Small Programming Language

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IMP syntax, the grammar in BNF<sup>1</sup>

For **Aexp**:

Arithmetic **expression**

$a ::= n \mid X \mid a_0 + a_1 \mid a_0 - a_1 \mid a_0 \times a_1. \quad n \in \mathbb{Z}$

For **Bexp**:

Boolean **expression**

$b ::= \text{true} \mid \text{false} \mid a_0 = a_1 \mid a_0 \leq a_1 \mid \neg b \mid b_0 \wedge b_1 \mid b_0 \vee b_1$

For **Com**:

**c**

$c ::= \text{skip} \mid X := a \mid c_0; c_1 \mid \text{if } b \text{ then } c_0 \text{ else } c_1 \mid \text{while } b \text{ do } c$

<sup>1</sup> Backus-Naur Form



# IMP - Aexp Semantic

## IMP Semantic Aexp:

- Let define the set of states as  $\text{State} = \text{Var} \Rightarrow \text{Val}$   $\text{Val} \in \mathbb{Z}$
- Let define the evaluation function for the arithmetic expression as

$$\text{aval} : \text{Aexp} \times \text{State} \Rightarrow \text{Val}$$

$$\text{aval}(\underline{n}, s) = n$$

$$\text{aval}(X, s) = s(X)$$

$$\text{aval}(a_1 + a_2, s) = \text{aval}(a_1, s) + \text{aval}(a_2, s)$$

$$\text{aval}(a_1 - a_2, s) = \text{aval}(a_1, s) - \text{aval}(a_2, s)$$

$$\text{aval}(a_1 * a_2, s) = \text{aval}(a_1, s) \cdot \text{aval}(a_2, s)$$



# IMP - Bexp Semantic

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## IMP Semantic **Bexp**:

- Let define the evaluation function for the boolean expression as

$$bval : \mathbf{Bexp} \times \mathbf{State} \Rightarrow \mathbf{Bool}$$

$$bval(\mathbf{true}, s) = \mathit{true}$$

$$bval(\mathbf{false}, s) = \mathit{false}$$

$$bval(a_1 = a_2, s) = \mathit{aval}(a_1, s) = \mathit{aval}(a_2, s)$$

$$bval(a_1 < a_2, s) = \mathit{aval}(a_1, s) < \mathit{aval}(a_2, s)$$

$$bval(\neg b) = \neg bval(b, s)$$

$$bval(b_1 \wedge b_2, s) = bval(b_1, s) \wedge bval(b_2, s)$$

$$bval(b_1 \vee b_2, s) = bval(b_1, s) \vee bval(b_2, s)$$



# IMP - Structural Operational Semantic of Com

## IMP Semantic Com:

Let:

- $c \in \mathbf{Com}$

- $s, s' \in \mathbf{State}$

- $\langle c, s \rangle \Rightarrow s'$

$$\frac{}{\langle \mathbf{skip}, s \rangle \Rightarrow s} \text{skip}, \Rightarrow$$

$$\frac{}{\langle X := a, s \rangle \Rightarrow s[X := \text{aval}(a, s)]} \text{Loc}, \Rightarrow$$

$$\frac{\langle c_1, s \rangle \Rightarrow s'' \quad \langle c_2, s'' \rangle \Rightarrow s'}{\langle c_1; c_2, s \rangle \Rightarrow s'} \text{Comp}, \Rightarrow$$

$$\frac{bval(b, s) = \mathbf{true} \quad \langle c_1, s \rangle \Rightarrow s'}{\langle \mathbf{if } b \mathbf{ then } \{c_1\} \mathbf{ else } \{c_2\}, s \rangle \Rightarrow s'} \text{If-true}, \Rightarrow$$

$$\frac{bval(b, s) = \mathbf{false} \quad \langle c_2, s \rangle \Rightarrow s'}{\langle \mathbf{if } b \mathbf{ then } \{c_1\} \mathbf{ else } \{c_2\}, s \rangle \Rightarrow s'} \text{If-false}, \Rightarrow$$

$$\frac{bval(b, s) = \mathbf{false}}{\langle \mathbf{while } b \mathbf{ do } \{c\}, s \rangle \Rightarrow s} \text{While-false}, \Rightarrow$$

$$\frac{bval(b, s) = \mathbf{true} \quad \langle c, s \rangle \Rightarrow s'' \quad \langle \mathbf{while } b \mathbf{ do } \{c\}, s'' \rangle \Rightarrow s'}{\langle \mathbf{while } b \mathbf{ do } \{c\}, s \rangle \Rightarrow s'} \text{While-true}, \Rightarrow$$





# Hoare Logic - Assertion

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Just for convention we renamed the **State**  $\mathbf{s} = \sigma$  and the set of **States** as  $\Sigma$ .

In order to reason over the correctness of the program, we are going to use **FOL**

**Aexpv** :  $a ::= \underline{n} \mid x \mid X \mid a_1 + a_2 \mid a_1 - a_2 \mid a_1 \times a_2$

Ghost  
variable

**Assn** :  $A ::= \text{true} \mid \text{false} \mid a_1 = a_2 \mid a_1 \leq a_2 \mid \neg A \mid A_1 \wedge A_2 \mid A_1 \vee A_2 \mid A_1 \rightarrow A_2 \mid \forall x A \mid \exists x A$



# Hoare Logic - Semantic of FOL in Hoare Logic

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Let  $\mathbf{a} \in \mathbf{Aexp}$ ,  $\sigma \in \Sigma$  e  $/ : \text{Var} \rightarrow \mathbf{Z}$ .

$$\llbracket \underline{n} \rrbracket_{\sigma}^I = n$$

$$\llbracket x \rrbracket_{\sigma}^I = I(x)$$

$$\llbracket X \rrbracket_{\sigma}^I = \tilde{\sigma}(X)$$

$$\llbracket a_1 + a_2 \rrbracket_{\sigma}^I = \llbracket a_1 \rrbracket_{\sigma}^I + \llbracket a_2 \rrbracket_{\sigma}^I$$

$$\llbracket a_1 - a_2 \rrbracket_{\sigma}^I = \llbracket a_1 \rrbracket_{\sigma}^I - \llbracket a_2 \rrbracket_{\sigma}^I$$

$$\llbracket a_1 \times a_2 \rrbracket_{\sigma}^I = \llbracket a_1 \rrbracket_{\sigma}^I \times \llbracket a_2 \rrbracket_{\sigma}^I$$



# Hoare Logic - Semantic of FOL in Hoare Logic

Let  $\mathbf{A} \in \mathbf{Assn}$ ,  $\sigma \in \Sigma$  e  $/ : \text{Var} \rightarrow \mathbb{Z}$ .

We define the relation **true** in  $\sigma$  w.r.t.  $/$  i.i.f  $\sigma \models^I A$  by structural induction on  $\mathbf{A}$ :

$$\sigma \models^I \mathbf{true}$$

$$\sigma \models^I a_1 = a_2 \quad \Leftrightarrow \quad \llbracket a \rrbracket_\sigma^I = \llbracket a \rrbracket_\sigma^I$$

$$\sigma \models^I a_1 \leq a_2 \quad \Leftrightarrow \quad \llbracket a \rrbracket_\sigma^I \leq \llbracket a \rrbracket_\sigma^I$$

$$\sigma \models^I \neg A \quad \Leftrightarrow \quad \sigma \not\models^I A$$

$$\sigma \models^I A_1 \wedge A_2 \quad \Leftrightarrow \quad \sigma \models^I A_1 \text{ e } \sigma \models^I A_2$$

$$\sigma \models^I A_1 \vee A_2 \quad \Leftrightarrow \quad \sigma \models^I A_1 \text{ o } \sigma \models^I A_2$$

$$\sigma \models^I A_1 \rightarrow A_2 \quad \Leftrightarrow \quad \sigma \models^I A_1 \text{ implies } \sigma \models^I A_2$$

$$\sigma \models^I \forall x A \quad \Leftrightarrow \quad \sigma \models^{I[n/x]} A \text{ for every } n \in \mathbb{Z}$$

$$\sigma \models^I \exists x A \quad \Leftrightarrow \quad \sigma \models^{I[n/x]} A \text{ for some } n \in \mathbb{Z}$$



# Hoare Logic

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## Hoare triple

$$\{A\} c \{B\} \quad A, B \in \mathbf{Assn} , c \in \mathbf{Com}.$$

Just for convention we renamed the **State**  $s = \sigma$  and the set of **States** as  $\Sigma$

These are read, roughly speaking, as for any state  $\sigma$  satisfying  $A$ , if  $c$  transforms state  $\sigma$  to  $\sigma^1$ , then  $\sigma^1$  satisfies  $B$ .



# Hoare Logic - Inference Rules

## General rule HL:

The following inference rules are called general because they apply to any program C

**Note:**  $A \supset B \equiv A \rightarrow B$

$$\frac{}{\{P\} C \{true\}} \quad (true)$$

$$\frac{}{\{false\} C \{P\}} \quad (false)$$

$$\frac{P \supset Q \quad \{Q\} C \{R\}}{\{P\} C \{R\}} \quad (str)$$

$$\frac{\{P\} C \{Q\} \quad Q \supset R}{\{P\} C \{R\}} \quad (weak)$$

$$\frac{\{P\} C \{Q_0\} \dots \{P\} C \{Q_n\}}{\{P\} C \{Q_0 \wedge \dots \wedge Q_n\}} \quad (and)$$

$$\frac{\{P_0\} C \{Q\} \dots \{P_n\} C \{Q\}}{\{P_0 \vee \dots \vee P_n\} C \{Q\}} \quad (or)$$

# Hoare Logic - Inference Rules

## Special rules HL:

The following inference rules are called special because they are specific of each program construct.

$$\frac{}{\{P\} \text{ skip } \{P\}} \text{ (skip)}$$

$$\frac{}{\{[E/x]P\} x := E \{P\}} \text{ (assign)}$$

$$\frac{\{P \wedge Q\} C_1 \{R\} \quad \{P \wedge \neg Q\} C_2 \{R\}}{\{P\} \text{ if } Q \text{ then } C_1 \text{ else } C_2 \{R\}} \text{ (if)}$$

$$\frac{\{P \wedge Q\} C \{P\}}{\{P\} \text{ while } Q \text{ do } C \{P \wedge \neg Q\}} \text{ (while)}$$

$$\frac{\{P\} C_1 \{Q\} \quad \{Q\} C_2 \{R\}}{\{P\} C_1; C_2 \{R\}} \text{ (comp)}$$

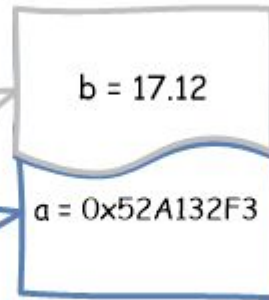


# Why we need to extend the HL?

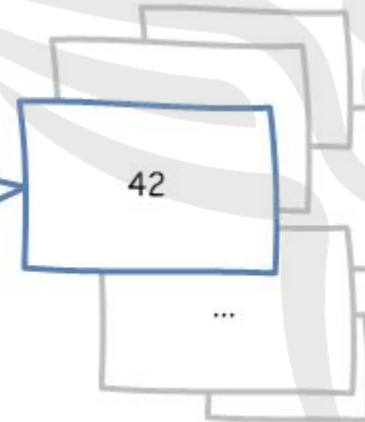
Programs are represented semantically as relations between initial and final states. To represent the pointer structures used to represent lists, trees etc. we need to add another component to states called the **heap**.

```
void heyDoSomething()  
{  
  int *a = 42;  
  double b = 17.12;  
}
```

*YOUR CODE*



*THE STACK*



*THE HEAP*

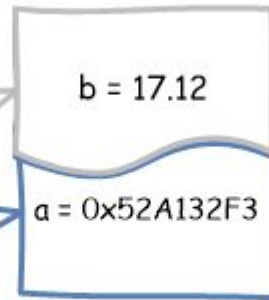
# Why we need to extend the HL?

To be more **precise**:

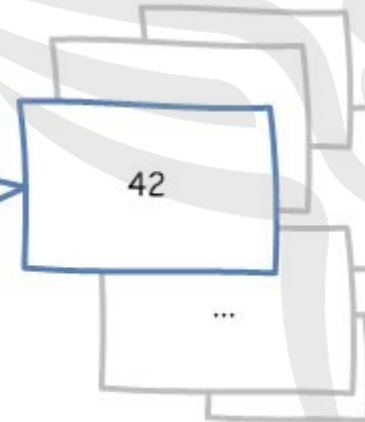
Inside the Heap(RAM) at the address 0x52A132F3 we have the value  
**42**

```
void heyDoSomething()  
{  
    int *a = 42;  
    double b = 17.12;  
}
```

*YOUR CODE*



*THE STACK*



*THE HEAP*

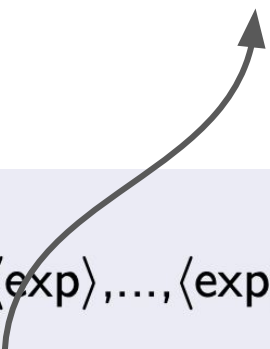


# Why we need to extend the HL?

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In the previous example, the lookup operator was  $*$ , here  
 $*a$  is represented by **[a]**

```
 $\langle comm \rangle ::= \dots$   
|  $\langle var \rangle := \mathbf{cons}(\langle exp \rangle, \dots, \langle exp \rangle)$   
|  $\langle var \rangle := [\langle exp \rangle]$   
|  $[\langle exp \rangle] := \langle exp \rangle$   
| dispose  $\langle exp \rangle$ 
```



allocation  
lookup  
mutation  
deallocation

# Why we need to extend the HL?

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With HL we meet problem when the heap is introduced - e.g.  
assignment rule:

$$\begin{array}{ccc} \{P[e/x]\} & x := e & \{P\} \\ \Downarrow & & \\ \begin{array}{ccc} \checkmark & \{1 = 1\} & \mathbf{x} := \mathbf{1} & \{x = 1\} \\ \times & \{?\} & \mathbf{[x]} := \mathbf{1} & \{[x] = 1\} \end{array} \end{array}$$

# Why we need to extend the HL?

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**Preconditions have to explicitly specify that a set of addresses do not overlap with the addresses affected by a command.**

$$\{x \mapsto - \wedge [y] = 2 \wedge x \neq y\} [x] := 1 \{[x] = 1 \wedge [y] = 2 \wedge x \neq y\}$$



x point **to some** value into  
the heap

# Separation Logic - Introduction

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Simplifies the problem of specifying preconditions by introducing two new logical connectives:

**\* : Separation conjunction**

**- \* : Separation implication**

$[y] = 2$

$\{x \mapsto - * y \mapsto 2\} [x] := 1 \{x \mapsto 1 * y \mapsto 2\}$  (\* has  $x \neq y$  built into it)

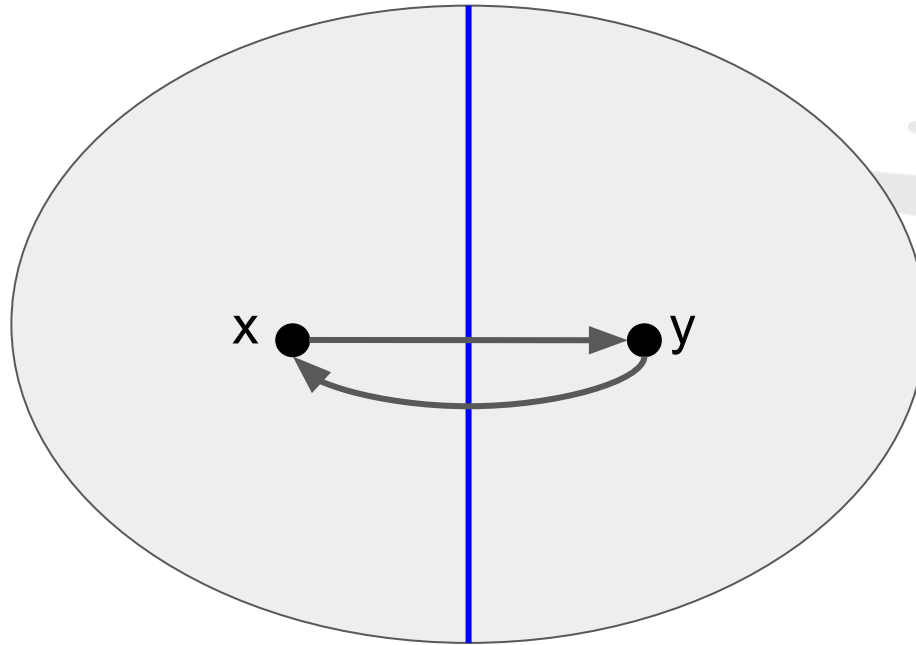
x points **to some** value  
into the heap.  $[x] = \_$

# Separation conjunction - Intuition

$$x \mapsto y * y \mapsto x$$

$x = 10$   
 $y = 42$

RAM(heap)	
Address	Value
10	42
42	10

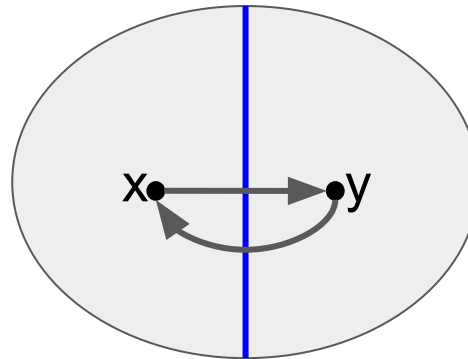


# Separation conjunction - Intuition

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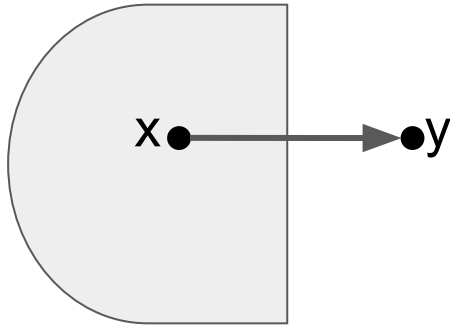
$$x \mapsto y * y \mapsto x$$

**Roughly speaking:  $x$  points to  $y$ , and separately  $y$  points to  $x$**



# Separation conjunction - Intuition

$$x \mapsto y$$

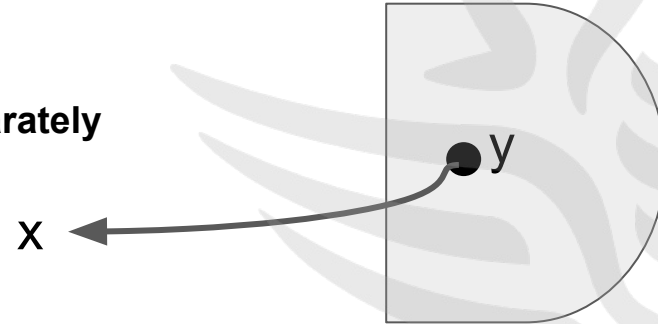


$x = 10$   
 $y = 42$

RAM splitted (heap)	
Address	Value
10	42

$$y \mapsto x$$

And Separately



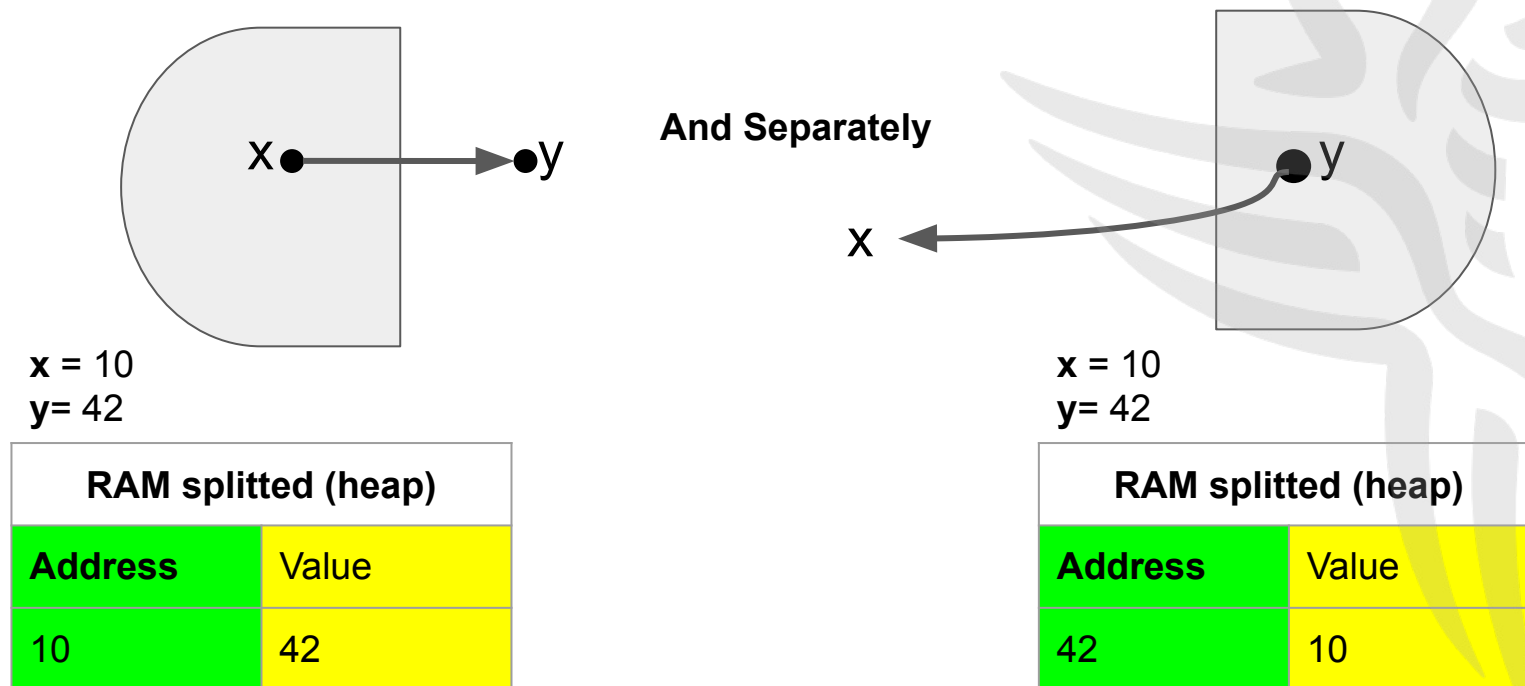
$x = 10$   
 $y = 42$

RAM splitted (heap)	
Address	Value
42	10



# Separation conjunction - Intuition

**Notice that:** the separating conjunction splits the heap/RAM, but it does not split the association of variables to values: heap cells, but not variable associations





# Separation Logic - Formal Definition

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The model has two components, the store and the heap:

$$State = (Store \times Heap)$$

- The store is a finite partial function mapping from variables to integers

$$Store = Var \Rightarrow Val$$

- The heap is a finite function from natural numbers to integers.

$$Heap = Address \Rightarrow Val$$



# Separation Logic - Formal Definition

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1.  $dom(h)$  denotes the domain of definition of a heap  $h \in \text{Heaps}$ , and  $dom(s)$  is the domain of  $s \in \text{Stores}$ ;
2.  $h \# h'$  says that  $dom(h) \cap dom(h') = \emptyset$ ;
3.  $h \bullet h'$  denotes the union of functions with disjoint domains, which is undefined if the domains overlap;
4.  $(f \mid i \mapsto j)$  is the partial function like  $f$  except that  $i$  goes to  $j$ .

2. In other way states that the address space of  $h$  and  $h'$  doesn't overlap



# Separation Logic - Semantic of Assn extended

$$s, h \models B \quad \text{iff } \llbracket B \rrbracket s = \text{true}$$

Notice that, it requires a **single** element inside the heap's domain

$$s, h \models E \mapsto F \quad \text{iff } \{\llbracket E \rrbracket s\} = \text{dom}(h) \text{ and } h(\llbracket E \rrbracket s) = \llbracket F \rrbracket s$$

$$s, h \models \text{false} \quad \text{never}$$

$$s, h \models P \Rightarrow Q \quad \text{iff if } s, h \models P \text{ then } s, h \models Q$$

$$s, h \models \forall x. P \quad \text{iff } \forall v \in \text{Ints}. [s \mid x \mapsto v], h \models P$$

$$s, h \models \text{emp} \quad \text{iff } h = [] \text{ is the empty heap}$$

$$s, h \models P * Q \quad \text{iff } \exists h_0, h_1. h_0 \# h_1, h_0 * h_1 = h, s, h_0 \models P \text{ and } s, h_1 \models Q$$

$$s, h \models P \multimap Q \quad \text{iff } \forall h'. \text{if } h' \# h \text{ and } s, h' \models P \text{ then } s, h \bullet h' \models Q$$



# Separation Logic - And Inference Rule

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Let  $Mod(C) = \{x | \exists a \in \mathbf{Aexp}. x := a \in C\}$

$$\frac{\{A\}C\{B\}}{\{A \wedge R\}C\{B \wedge R\}} \quad FV(R) \cap Mod(C) = \emptyset$$

**It works for the HL.  
BUT In SL might not be enough.**

**Example....**



# Separation Logic - And Inference Rule

x points into some points on the heap

$$\frac{\{x \mapsto -\}[x] := 4\{x \mapsto 4\}}{\{x \mapsto - \wedge y \mapsto 3\}[x] := 4\{x \mapsto 4 \wedge y \mapsto 3\}}$$

- $s, h \models x \mapsto - \wedge y \mapsto 3$  for some  $n \in \text{Addr } \text{dom}(h) = \{n\}$ . and  $s(x) = s(y) = n$ ,  $h(n) = 3$
- $([x] := 4, s, h) \downarrow (-, s, \{n \mapsto 4\})$   $h'$
- $s, h' \not\models x \mapsto 4 \wedge y \mapsto 3$


It requires that the heap is made up at by just one address



# Separation Logic - Separation Conjunction

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To overcome this issue, in SL we introduce another inference rule, which is called the **Frame-Rule**

$$\frac{\{A\}C\{B\}}{\{A * R\}C\{B * R\}} \quad \text{FV}(R) \cap \text{Mod}(C) = \emptyset$$


The separation conjunction allows to split the heap in 2 parts. It avoids the heap's domain made up by just one element

# Separation Logic - Tool Example

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[Infer Static Analyzer](#)

# References

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- Hoare Logic:  
<https://mitpress.mit.edu/books/formal-semantics-programming-language-s>
- Separation Logic:  
<http://www0.cs.ucl.ac.uk/staff/p.ohearn/papers/Marktoberdorf11LectureNotes.pdf>
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