### RAVI TEJA TAMVADA

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Targeting assignments as Software Engineer in the Automotive/Aerospace Industry

Location Preference: Bangalore, Hyderabad, Visakhapatnam

#### PROFILE SUMMARY

- Achievement-driven engineer with an experience of 7 years in software development
- Experience in aerospace industry and knowledge of D0-178B safety standard and Avionics architecture
- Experience in **automotive** industry and knowledge of **ASIL ISO26262** safety standard and **AUTOSAR** architecture
- Skilled in coding for **embedded platforms** with in-depth knowledge of hardware & software
- Proficient in C/C++, GCC compiler, OS based systems and testing tools like Vector CAST, Cantata and Gtest framework.
- Comprehensive knowledge of ECU/Processor based hardware and software integration process
- Appreciated by customers for the outstanding contributions made on the various projects undertaken
- Possess insightful knowledge of the academic concepts and their applicability in the real world
- An effective communicator with strong interpersonal skills, team management, relationship management, coordination and analytical skills

#### WORK EXPERIENCE

## Dec'17-Present with Wafer Space Pvt. Ltd., as Sr. Software Engineer I Key Result Areas:

- Presently deployed at Nvidia Graphics Ltd., as Software Engineer for functional safety tesing of automated driviing software. Responsibilities include developing Unit Tests on VectorCAST and SW-SW Integration Test using Gtest framework.
- Worked as Software Engineer at Continental TCI, Bengaluru for 2 years. Worked in BU ADAS.
- Have worked under different sub-groups like Radar Algo, Error Handling, Diagnostics and COM.
- Primary responsibilities include SW Test environment setup, develop test suites, run the tests and generate test summary reports using tools like Cantata.
- Develop and run tests on software against software requirements and design. Also, perform a structural coverage analysis and by running coverage-based tests.
- Acquired functional knowledge of AUTOSAR architecture, ASIL safety standards and MISRA-C coding guidelines.

# Apr'17-Sep'17 with Datamatics Global Services Pvt Ltd., as Associate Consultant (Contractual) Key Result Areas:

- Software Engineer at Robert Bosch, Bangalore.
- Perform unit-testing of software modules coded in C and MATLAB
- Setup the **Vector CAST** tool environment, implementation of test-cases, executing tests and generating reports
- Verifying the low-level requirements and requirements linking in DOORS
- Tracking and identifying bugs in the software along with capturing test summary and reporting it to the customers

## Apr'14-Jun'16 with Honeywell Technology Solutions Lab Pvt. Ltd., as Engineer Kev Result Areas:

- Worked in Displays & Graphics SBG developing software for Gulfstream G650 aircraft cockpit flight data displays
- Testing, debugging and validation of software systems. Also, perform Structural Coverage Analysis with VectorCAST
- Verifying complex algorithms on high density, frequency, perform timing analysis & simulation of design
- Tracking and identifying bugs in the software along with capturing test summary and reporting it to the customers
- Debugging and executing the software and preparing it for release to the end customers



- Co-ordinating with team members for designing high level technical design documents and configuring work products in CM
- Worked on DO-178B levels B, C avionics software. Hands-on experience in avionics hardware bench(MAU) setup, knowledge of ASCB-D bus and ARINC 429 protocol
- Automated several key processes in the project and improved the Cycle-Time using Six Sigma methods

### **ACADEMIC QUALIFICATIONS**

- M.Tech in RF & Microwave Engineering from GITAM University, Visakhapatnam in 2013
- **B.Tech.** in Electronics & Communication Engineering from St. Theressa Institute of Engineering & Technology, Garividi, affiliated to JNTU Kakinada in 2011
- 12th from Zinc Smelter Senior Secondary School, Visakhapatnam, CBSE Board in 2007
- 10th from M.V.P. Public School, Visakhapatnam, ICSE Board in 2005

#### **CERTIFICATIONS**

- **PG-Diploma** in Embedded Systems Design from **C-DAC**, Bangalore in 2014
- Green Belt in Design For Six Sigma from Honeywell International Inc. in 2015

#### **PUBLICATION**

 High Conversion Efficiency Rectenna for RF Energy Harvesting in International Journal of Systems, Algorithms and Applications(IJSAA), ISSN:2277-2677 in Special Issues, ICRASE-2013, May 2013

#### PAPER PRESENTATION

• International Conference on Recent Advances in Science & Engineering(ICRASE) at the conference, Association of Scholars and Professionals(ASP), Hyderabad in April 2013

#### WORKSHOP

- Design for Six Sigma at Honeywell Technology Solutions Lab., Bangalore in May 2014
- Recent Advances in RF & Microwave Techniques and Measurements at IIST, Thiruvananthapuram in collaboration with IIT, Bombay, sponsored by Agilent Inc. and Cadence Inc. in July 2012

#### IT SKILLS

**Languages**: C(expert-level), C++(intermediate-level), shell/batch scripting

Software: IBM Rational DOORS, PTC Integrity, Serena Dimensions, MS Visual Studio, Eclipse, Jira

Platforms: Linux & Device Drivers, RTOS

Tools: VectorCAST, Cantata, Gtest framework, GCC Toolchain, Doxygen, PlantUML

Embedded Boards: Beaglebone Black (ARM Cortex-A8), Raspberry Pi, STM32 Discovery/Nucleo(ARM Cortex-M4)

boards.

Others: Comprehensive knowledge of computer hardware, networking and electronics

#### PERSONAL DETAILS

**Date of Birth:** 16th March 1989

**Languages Known:** English, Hindi and Telugu

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