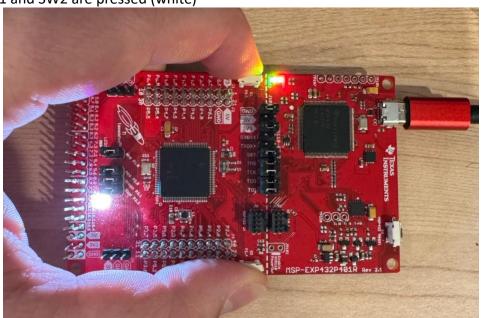
Rick Brophy ECE1188 Cyberphysical

Dr. Dickerson Due: 1/23/24

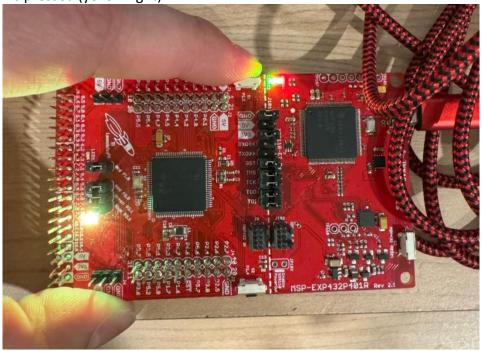
ECE1188 HW2 RTB30

InputOutput_mod

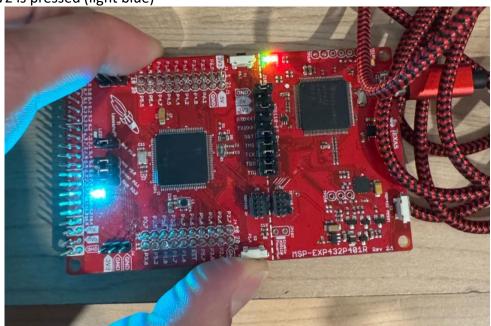
1. SW1 and SW2 are pressed (white)



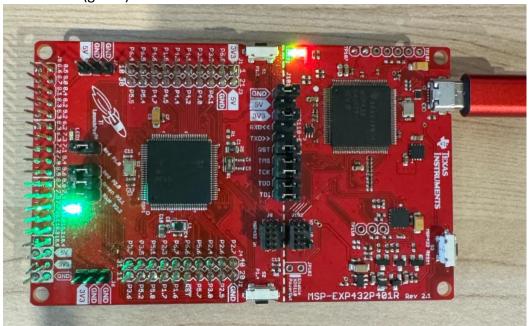
2. SW1 is pressed (yellow light)



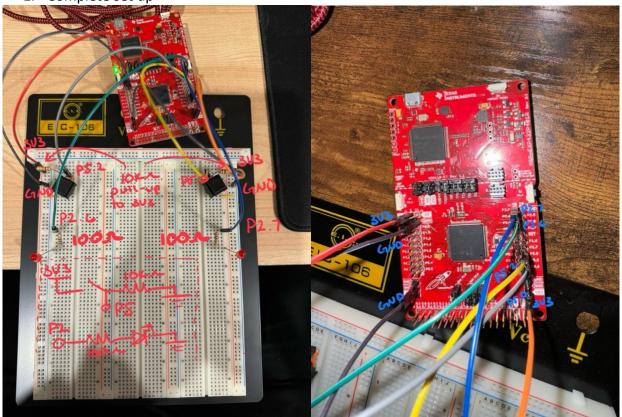
3. SW2 is pressed (light blue)



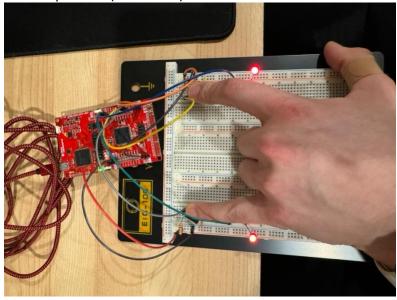
4. No switches (green)



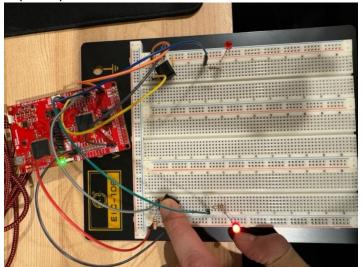
InputOutput_extern 1. Complete set up



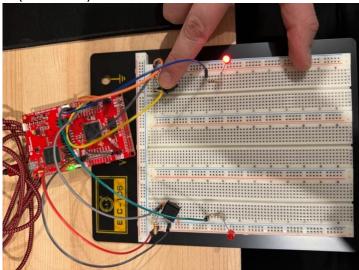
2. SW1 and SW2 are pressed (both LEDs)



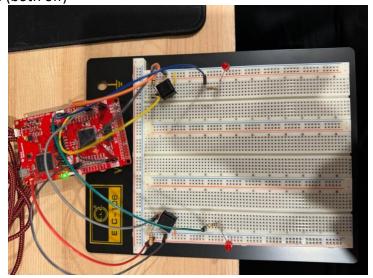
3. SW1 is pressed (1 LED)



4. SW2 is pressed (other LED)



5. No switches (both off)



6. Memory Map

Port 2 Registers	Memory Address	Initialized Value	Comments
P2SEL0	0x4000_4C0B	P2SEL0 &= ~0xC0 = xxxx xxxx & 0011 1111 = 00xx xxxx	Only care ab P2SEL0.6 & P2SEL0.7
P2SEL1	0x4000_4C0D	P2SEL1 &= ~0xC0 = xxxx xxxx & 0011 1111 = 00xx xxxx	Only care ab P2SEL1.6 & P2SEL1.7
P2DIR	0x4000_4C05	P2DIR = 0xC0 = xxxx xxxx & 1100 0000 = 11xx xxxx	Only care ab P2DIR.6 & P2DIR.7
P2DS	0x4000_4C09	P2DS = 0xC0 = xxxx xxxx & 1100 0000 = 11xx xxxx	Only care ab P2DS.6 & P2DS.7
P2OUT	0x4000_4C03	P2OUT &= ~0xC0 = xxxx xxxx & 0011 1111 = 00xx xxxx	Only care ab P2OUT.6 & P2OUT.7
Port 5 Registers	Memory Address	Initialized Value	Comments
P5SEL0	0x4000_4C4A	P5SEL0 &= ~0x05 = xxxx xxxx & 1111 1010 = xxxx x0x0	Only care ab P5SEL0.0 & P5SEL0.2
P5SEL1	0x4000_4C4C	P5SEL1 &= ~0x05 = xxxx xxxx & 1111 1010 = xxxx x0x0	Only care ab P5SEL1.0 & P5SEL1.2
P5DIR	0x4000_4C44	P5DIR &= ~0x05 = xxxx xxxx & 1111 1010 = xxxx x0x0	Only care ab P5DIR.0 & P5DIR.2
P5REN	0x4000_4C46	P5REN &= ~0x05 = xxxx xxxx & 1111 1010 = xxxx x0x0	Only care ab P5REN.0 & P5REN.2