StickOS Freescale MCU Crystal and Pin Pre-Assignment Details

	heartbeat LED pin ¹	safemode* (autorun disable) switch pin ¹	UART Transport data pins ²	I2C data pins	QSPI data pins	QSPI cs* pin ¹	ZigFlea rst* pin ^{1,4}	ZigFlea attn* pin ^{1,3,4}	ZigFlea rxtxen pin ^{1,4}	ZigFlea irq* pin
mcu direction	out	in	in/out	out/inout	in/out/out	out	out	out	out	in
<i>MCF5225x-80</i> w/48MHz crystal	dtin3	irq5*	urxd0 utxd0	sc10 sda0	qspi_din qspi_dout qspi_clk	qspi_cs0	an2	an3	an5	irq1*
<i>MCF5223x-60</i> w/25MHz crystal	dtin3	irq1*	urxd0 utxd0	scl sda	qspi_din qspi_dout qspi_clk	qspi_cs0	gpt0	gpt1	an5	irq4*
MCF5222x-66 w/48MHz crystal	dtin3	irq1*	urxd0 utxd0	scl sda	qspi_din qspi_dout qspi_clk	qspi_cs0	an2	an3	an5	irq4*
<i>MCF521x-66</i> w/ internal oscillator	dtin3	irq4*	urxd0 utxd0	scl sda	qspi_din qspi_dout qspi_clk	qspi_cs0	gpt0	gpt1	an5	irq1*
MCF51JM128-50 w/12MHz crystal	ptf0	ptg0	pte1 (u1) pte0 (u1)	scl1 (ptc0) sda1 (ptc1)	miso1 (pte4) mosi1 (pte5) spsck1 (pte6)	ss1 (pte7)	pte2	pte3	ptb5	irq*
MCF51CN128-50 w/25MHz crystal	pte3	ptg6	ptd3 (u2) ptd2 (u2)	scl1 (ptg2) sda1 (ptg3)	miso1 (ptc6) mosi1 (ptc5) spsck1 (ptc7)	ptf0	ptf1	ptf2	ptf3	irq* (ptc4)
MCF51QE128-50 w/32.768kHz oscillator	ptc2	pta2	ptb0 (u1) ptb1 (u1)	scl1 (pta3) sda1 (pta2)	miso1 (ptb4) mosi1 (ptb3) spsck1 (ptb2)	ss1 (ptb5)	ptc0	ptc1	ptf1	irq*
MC9S12DP512-50 w/16MHz crystal	pb7	pp0	ps0 (u0) ps1(u0)	scl (pj7) sda (pj6)	miso0 (pm2) mosi0 (pm4) sck0 (pm5)	ss0 (pm3)	pt0	pt1	pb6	irq* (pe1)
MC9S12DT256-50 w/4MHz crystal	pb7	pp0	ps0 (u0) ps1(u0)	scl (pj7) sda (pj6)	miso0 (pm2) mosi0 (pm4) sck0 (pm5)	ss0 (pm3)	pt0	pt1	pb6 (jumper)	irq* (pe1)
MC9S08QE128-50 w/32.768kHz oscillator	ptc2	pta2	ptb0 (u1) ptb1 (u1)	scl1 (pta3) sda1 (pta2)	miso1 (ptb4) mosi1 (ptb3) spsck1 (ptb2)	ss1 (ptb5)	ptc0	ptc1	ptf1	irq*

¹ may be reassigned persistently with the "pins" command

²UART Transport: 9600 baud, 8 data bits, no parity, 2 stop bits, xon/xoff; baud rate may be persistently changed with "baud" command

³ not needed by StickOS

⁴ not assigned for Firebird32