Denotational semantics

malloc takes a state and number of bytes n, finds a contiguous region of free memory starting at address p, and returns p and an updated state where addresses [p, p + n) are marked as used.

Big-step operational semantics

 $\langle e, \alpha, \sigma \rangle \Downarrow \langle v, \sigma' \rangle$ means: with argument α and state σ , e evaluates to v and the resulting state is σ' .

A state is a pair (M, L), containing memory and a print log.

$$\frac{\llbracket e_1 \rrbracket (\alpha, \sigma) = (v_1, \sigma') \qquad \llbracket e_2 \rrbracket (\alpha, \sigma') = (v_2, \sigma'')}{\llbracket \text{Add } e_1 \ e_2 \rrbracket (\alpha, \sigma) \triangleq (v_1 + v_2, \sigma'')}$$
(E-ADD)

$$\frac{\llbracket e_c \rrbracket(\alpha, \sigma) = (\top, \sigma') \qquad \llbracket e_t \rrbracket(\alpha, \sigma') = (v_t, \sigma'')}{\llbracket \text{If } e_c \ e_t \ e_e \rrbracket(\alpha, \sigma) \triangleq (v_t, \sigma'')}$$
(E-IFTRUE)

$$\frac{\llbracket e_c \rrbracket(\alpha, \sigma) = (\bot, \sigma') \qquad \llbracket e_e \rrbracket(\alpha, \sigma') = (v_e, \sigma'')}{\llbracket \text{If } e_c \ e_t \ e_e \rrbracket(\alpha, \sigma) \triangleq (v_e, \sigma'')}$$
(E-Iffalse)

$$\frac{\llbracket e_{pred} \rrbracket(\alpha, \sigma) = (i, \sigma') \qquad \llbracket e_i \rrbracket(\alpha, \sigma') = (v, \sigma'')}{\llbracket \text{Switch } e_{pred} \ (e_1, \dots, e_n) \rrbracket(\alpha, \sigma) \triangleq (v, \sigma'')}$$
(E-SWITCH)

$$\underbrace{\llbracket e_{in} \rrbracket (\alpha, \sigma) = (v_{in}, \sigma')}_{\llbracket \text{Let } e_{in} e_{out} \rrbracket (\alpha, \sigma) \triangleq (v_{out}, \sigma'')} \underbrace{\llbracket e_{out} \rrbracket (\alpha, \sigma) \triangleq (v_{out}, \sigma'')}_{(\text{E-Let})}$$

$$\frac{\llbracket e \rrbracket(\alpha,\sigma) = (v,(M,L))}{\llbracket \text{Print } e \rrbracket(\alpha,\sigma) \triangleq (\llbracket ,(M,L++v)) \end{bmatrix}} \tag{E-Print}$$

$$\frac{\llbracket e \rrbracket(\alpha,\sigma) = (v,(M,L))}{\llbracket \text{Read } e \rrbracket(\alpha,\sigma) \triangleq (M[v],(M,L))}$$
 (E-READ)

$$\frac{\llbracket e \rrbracket(\alpha,\sigma) = (n,(M,L)) \qquad (M',p) = \operatorname{malloc}(M,n * \operatorname{sizeof}(\tau))}{\llbracket \operatorname{Alloc}\ e\ \tau \rrbracket(\alpha,\sigma) \triangleq (p,(M',L))} \tag{E-Alloc}$$

$$\frac{\llbracket e_p \rrbracket(\alpha,\sigma) = (v_p,\sigma') \qquad \llbracket e_d \rrbracket(\alpha,\sigma') = (v_d,(M,L))}{\llbracket \text{Write } e_p \ e_d \rrbracket(\alpha,\sigma) \triangleq ([],(M[v_p \mapsto v_d],L))} \tag{E-Write}$$

$$\frac{\llbracket e \rrbracket(\alpha,\sigma) = (v,\sigma')}{\llbracket \text{Single } e \rrbracket(\alpha,\sigma) \triangleq ([v],\sigma')}$$
 (E-SINGLE)

$$\frac{\llbracket e_1 \rrbracket (\alpha, \sigma) = (v_1, \sigma') \qquad \llbracket e_2 \rrbracket (\alpha, \sigma') = (v_2, \sigma'')}{\llbracket \text{Concat Sequential } e_1 \ e_2 \rrbracket (\alpha, \sigma) \triangleq (v_1 + v_2, \sigma'')} \tag{E-CONCATSEQ}$$

$$\frac{\llbracket e_{in} \rrbracket(\alpha,\sigma) = (\alpha',\sigma') \qquad \llbracket e_{po} \rrbracket(\alpha',\sigma') = ([\bot,v],\sigma'')}{\llbracket \text{DoWhile } e_{in} \ e_{out} \rrbracket(\alpha,\sigma) \triangleq (v,\sigma'')}$$
 (E-DoWhileFalse)

$$\underbrace{\llbracket e_{in} \rrbracket(\alpha, \sigma) = (\alpha', \sigma') \qquad \llbracket e_{po} \rrbracket(\alpha', \sigma') = ([\top, \alpha''], \sigma'') \qquad \llbracket \text{DoWhile } e_{in} \ e_{po} \rrbracket(\alpha'', \sigma'') = (v, \sigma''')}_{\llbracket \text{DoWhile } e_{in} \ e_{po} \rrbracket(\alpha, \sigma) \triangleq (v, \sigma''')}$$
(E-DoWhileTrue)

$$\frac{\llbracket e \rrbracket(\alpha,\sigma) = (v,\sigma') \qquad \exists \ \alpha_2,\sigma_2,\sigma_3. \ \llbracket e_{in} \rrbracket(\alpha_2,\sigma_2) = (\alpha,\sigma_3)}{\llbracket \text{Assume (InLet } e_{in}) \ e \rrbracket(\alpha,\sigma) \triangleq (v,\sigma')} \tag{E-AssumeInLet)}$$

$$\frac{\llbracket e \rrbracket(\alpha,\sigma) = (v,\sigma') \qquad \exists \ \alpha_2,v_2,\sigma_2. \ \llbracket \text{Write} \ e_p \ e_d \rrbracket(\alpha_2,\sigma_2) = (v_2,\sigma_3)}{\llbracket \text{Assume (AfterWrite} \ e_p \ e_d) \ e \rrbracket(\alpha,\sigma) \triangleq (v,\sigma')} \text{ (E-AssumeAfterWrite)}$$

$$\frac{\llbracket e \rrbracket(\alpha,\sigma) = (v,\sigma') \qquad \exists \ \alpha_2,\sigma_2,\sigma_3. \ \llbracket e_{in} \rrbracket(\alpha_2,\sigma_2) = (\alpha,\sigma_3)}{\llbracket \text{Assume (InLoop } e_{in} \ e_{po}) \ e \rrbracket(\alpha,\sigma) \triangleq (v,\sigma')} \qquad \text{(E-AssumeInLoop1)}$$

$$\frac{\exists \ \alpha_2, \sigma_2, \sigma_3. \ [\![\text{Assume (InLoop} \ e_{in} \ e_{po}) \ e]\!] \ (\alpha_2, \sigma_2) = (\alpha, \sigma_3)}{[\![\text{Assume (InLoop} \ e_{in} \ e_{po}) \ e]\!] \ (\alpha, \sigma) \triangleq (v, \sigma')} \quad \text{(E-AssumeInLoop2)}$$

Abstract grammar

```
\langle basetype \rangle ::= IntT
                                                                                                                 \langle expr \rangle ::= Arg \langle type \rangle
   BoolT
                                                                                                                           Int \langle \mathbb{N} \rangle
                                                                                                                           Bool \langle bool \rangle
\langle type \rangle ::= \langle basetype \rangle
                                                                                                                           Empty
          PointerT \langle type \rangle
                                                                                                                           Add \langle expr \rangle \langle expr \rangle
         TupleT ⟨basetype⟩*
                                                                                                                           Sub \langle expr \rangle \langle expr \rangle
                                                                                                                           Mul \langle expr \rangle \langle expr \rangle
\langle bool \rangle ::= \top
                                                                                                                           LessThan \langle expr \rangle \langle expr \rangle
   | _____
                                                                                                                           And \langle expr \rangle \langle expr \rangle
\langle order \rangle ::= Parallel
                                                                                                                           Or \langle expr \rangle \langle expr \rangle
   Sequential
                                                                                                                           Write \langle expr \rangle \langle expr \rangle
                                                                                                                           Not \langle expr \rangle
\langle function \rangle ::= Function \langle type \rangle \langle expr \rangle
                                                                                                                           Print \langle expr \rangle
\langle assumption \rangle ::= InLet \langle expr \rangle
                                                                                                                           Read \langle expr \rangle
          InLoop \langle expr \rangle \langle expr \rangle
                                                                                                                           Get \langle expr \rangle \langle \mathbb{N} \rangle
          AfterWrite \langle expr \rangle \langle expr \rangle
                                                                                                                           Alloc \langle expr \rangle \langle type \rangle
                                                                                                                           Call \langle function \rangle \langle expr \rangle
                                                                                                                           Single \langle expr \rangle
                                                                                                                           Concat \langle order \rangle \langle expr \rangle \langle expr \rangle
                                                                                                                           Switch \langle expr \rangle \langle expr \rangle^*
                                                                                                                           If \langle expr \rangle \langle expr \rangle \langle expr \rangle
                                                                                                                           Let \langle expr \rangle \langle expr \rangle
```

DoWhile $\langle expr \rangle \langle expr \rangle$ Assume $\langle assumption \rangle \langle expr \rangle$