MyCPU SID Address Range

	Address (HEX)		Name	Databits								Read/Write
	SID 1	SID 2		D7 (MSB)	D6	D5	D4	D3	D2	D1	D0 (LSB)	
Voice 1	2A00h	2B00h	**Freq Low**	F7	F6	F5	F4	F3	F2	F1	F0	Write only
	2A01h	2B01h	**Freq High**	F15	F14	F13	F12	F11	F10	F9	F8	Write only
	2A02h	2B02h	**PW Low**	PW7	PW6	PW5	PW4	PW3	PW2	PW1	PW0	Write only
	2A03h	2B03h	**PW High**	--	--	--	--	PW11	PW10	PW9	PW8	Write only
	2A04h	2B04h	**Control**	Noise	Rect.	Saw.	Tri.	Test	Ring Mod	Sync	Gate	Write only
	2A05h	2B05h	**A/D**	ATK3	ATK2	ATK1	ATK0	DCY3	DCY2	DCY1	DCY0	Write only
	2A06h	2B06h	**S/R**	STN3	STN2	STN1	STN0	RIS3	RIS2	RIS1	RIS0	Write only
Voice 2	2A07h	2B07h	**Freq Low**	F7	F6	F5	F4	F3	F2	F1	F0	Write only
	2A08h	2B08h	**Freq High**	F15	F14	F13	F12	F11	F10	F9	F8	Write only
	2A09h	2B09h	**PW Low**	PW7	PW6	PW5	PW4	PW3	PW2	PW1	PW0	Write only
	2A0Ah	2B0Ah	**PW High**	--	--	--	--	PW11	PW10	PW9	PW8	Write only
	2A0Bh	2B0Bh	**Control**	Noise	Rect.	Saw.	Tri.	Test	Ring Mod	Sync	Gate	Write only
	2A0Ch	2B0Ch	**A/D**	ATK3	ATK2	ATK1	ATK0	DCY3	DCY2	DCY1	DCY0	Write only
	2A0Dh	2B0Dh	**S/R**	STN3	STN2	STN1	STN0	RIS3	RIS2	RIS1	RIS0	Write only
Voice 3	2A0Eh	2B0Eh	**Freq Low**	F7	F6	F5	F4	F3	F2	F1	F0	Write only
	2A0Fh	2B0Fh	**Freq High**	F15	F14	F13	F12	F11	F10	F9	F8	Write only
	2A10h	2B10h	**PW Low**	PW7	PW6	PW5	PW4	PW3	PW2	PW1	PW0	Write only
	2A11h	2B11h	**PW High**	--	--	--	--	PW11	PW10	PW9	PW8	Write only
	2A12h	2B12h	**Control**	Noise	Rect.	Saw.	Tri.	Test	Ring Mod	Sync	Gate	Write only
	2A13h	2B13h	**A/D**	ATK3	ATK2	ATK1	ATK0	DCY3	DCY2	DCY1	DCY0	Write only
	2A14h	2B14h	**S/R**	STN3	STN2	STN1	STN0	RIS3	RIS2	RIS1	RIS0	Write only
Filter	2A15h	2B15h	**FC Low**	--	--	--	--	--	FC2	FC1	FC0	Write only
	2A16h	2B16h	**FC High**	FC10	FC9	FC8	FC7	FC6	FC5	FC4	FC3	Write only
	2A17h	2B17h	**Res/Filt**	RES3	RES2	RES1	RES0	FILEX	FIL3	FIL2	FIL1	Write only
	2A18h	2B18h	**Mode/Vol**	3 OFF	HP	BP	LP	VOL3	VOL2	VOL1	VOL0	Write only
Misc	2A19h	2B19h	**PotX**	PX7	PX6	PX5	PX4	PX3	PX2	PX1	PX0	Read only
	2A1Ah	2B1Ah	**PotY**	PY7	PY6	PY5	PY4	PY3	PY2	PY1	PY0	Read only
	2A1Bh	2B1Bh	**OSC3(Rand)**	RAN7	RAN6	RAN5	RAN4	RAN3	RAN2	RAN1	RAN0	Read only
	2A1Ch	2B1Ch	**ENV3**	E7	E6	E5	E4	E3	E2	E1	E0	Read only