Parallel Algorithms Patterns: Map

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SLIDES ADAPTED FROM

"STRUCTURED PARALLEL PROGRAMMING - CIS 410/510, UNIVERSITY OF OREGON"
REZAUL A. CHOWDHURY - CSE 613: PARALLEL PROGRAMMING

Bibliography

Sections 3.5 to 3.7 and Chapter 4 of Structured Parallel Programming, Michael McCool, Arch D. Robison and James Reinders. Morgan Kaufmann (2012)

Parallel Control Patterns

Serial control patterns

Loop, recursion, and more

Parallel control patterns extend serial control patterns

Each parallel control pattern is related to at least one serial control pattern, but relaxes assumptions of serial control patterns

Parallel control patterns: fork-join, map, stencil, reduction, scan, recurrence

Parallel Control Patterns: Fork-Join

Fork-join: allows control flow to fork into multiple parallel flows, then rejoin later

CUDA implements this when executing a kernel

A "join" is different than a "barrier

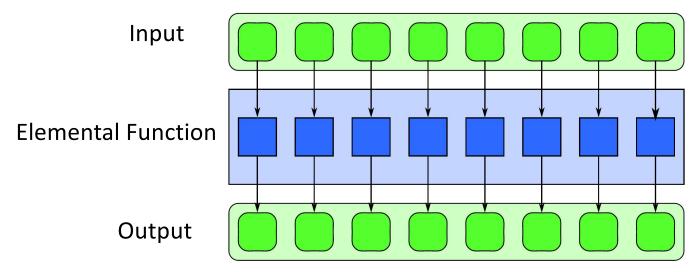
- After join only one thread continues
- Barrier all threads continue

Parallel Control Patterns: Map

Map: performs a function over every element of a collection

Map replicates a serial iteration pattern where each iteration is independent of the others, the number of iterations is known in advance, and computation only depends on the iteration count and data from the input collection

The replicated function is referred to as an "elemental function"



Mapping

"Do the same thing many times"

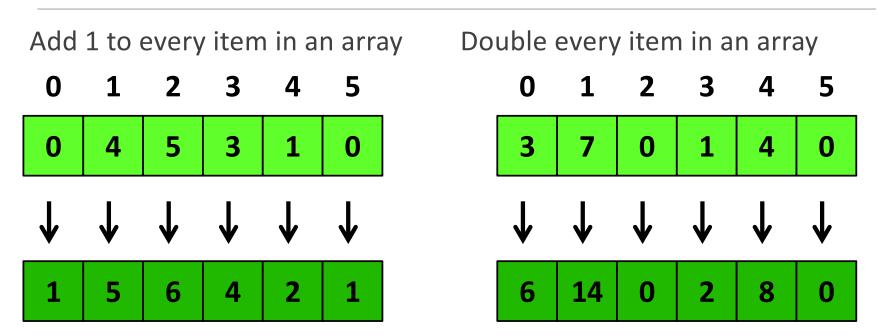
foreach i in foo:
 do something

Well-known higher order function in languages like ML, Haskell, Scala

map:
$$\forall ab.(a \rightarrow b)List\langle a \rangle \rightarrow List\langle b \rangle$$

applies a function each element in a list and returns a list of results

Example Maps



Key Point: An operation is a map if it can be applied to each element without knowledge of neighbors.

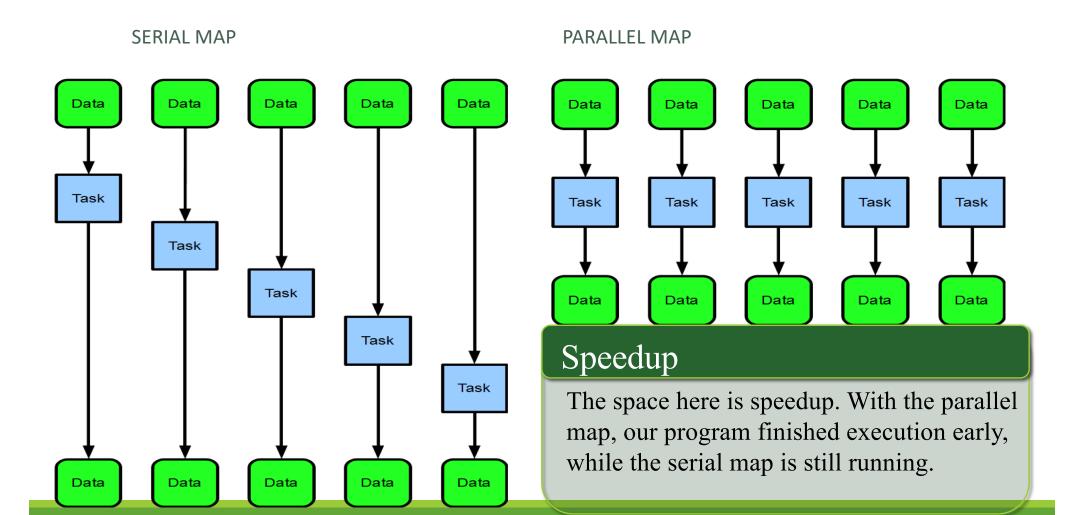
Key Idea

Map is a "foreach loop" where each iteration is **independent**

Embarrassingly Parallel

Independence is a big win. We can run map completely in parallel. Significant speedups! More precisely: $T(\infty)$ is O(1) plus implementation overhead

Comparing Maps



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Independence

The key to (embarrasing) parallelism is independence

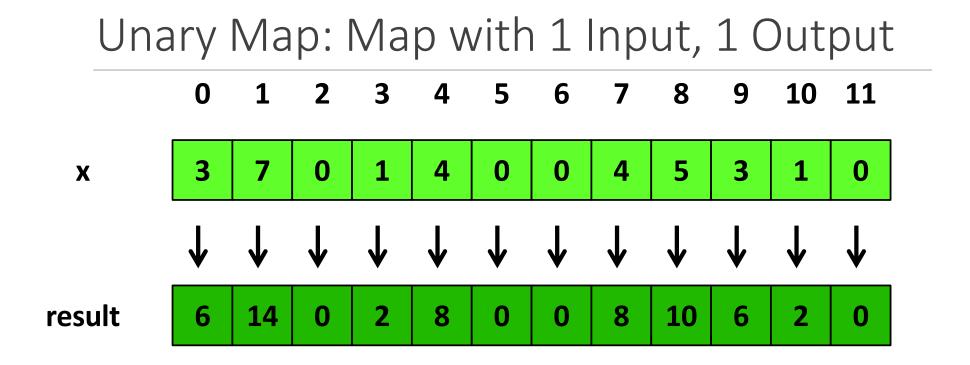
Warning: No shared state!

Map function should be "pure" (or "pure-ish") and should not modify shared states

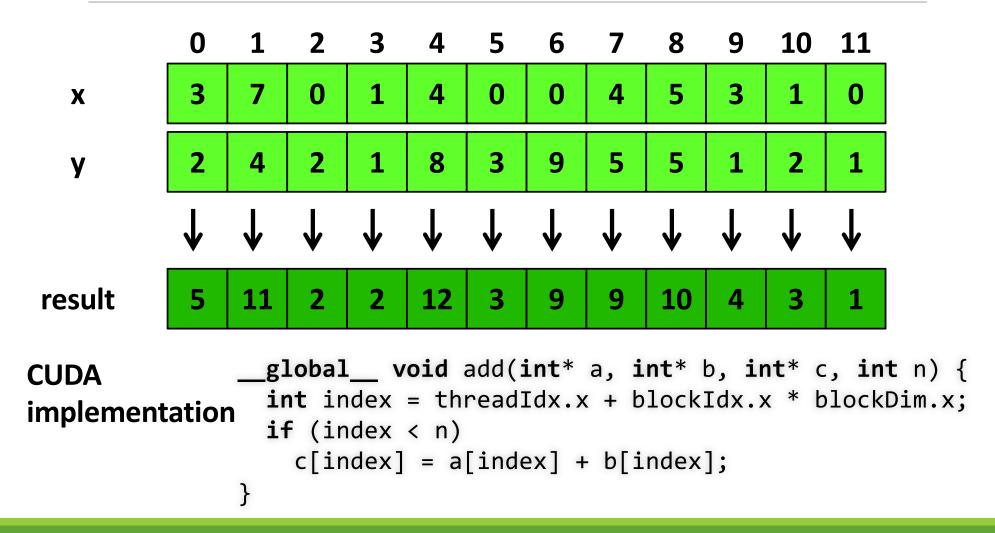
Modifying shared state breaks perfect independence

Results of accidentally violating independence:

- non-determinism
- data-races
- undefined behavior
- segfaults



N-Array Map: Map with N Inputs, 1 Output

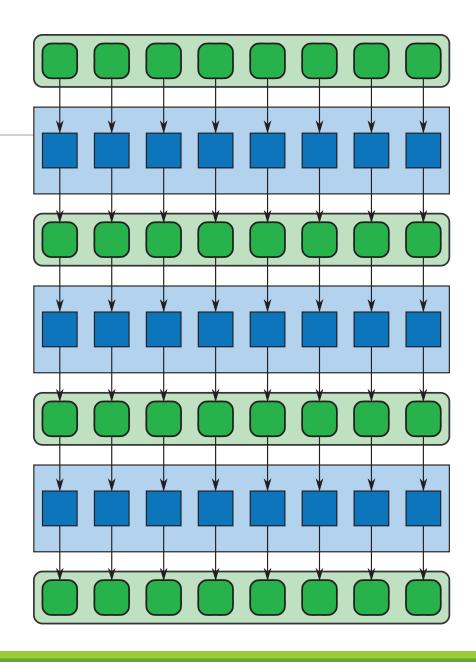


Optimization Sequences of Maps

Often several map operations occur in sequence

 Vector math consists of many small operations such as additions and multiplications applied as maps

A naïve implementation may write each intermediate result to memory, wasting memory BW and likely overwhelming the cache

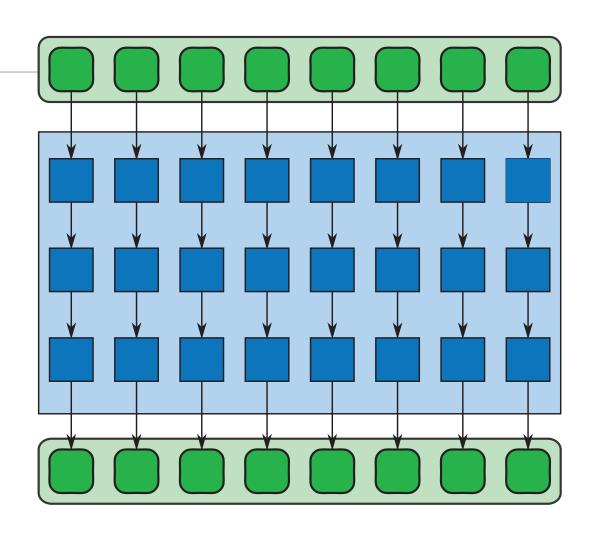


Optimization Code Fusion

Can sometimes "fuse" together the operations to perform them at once

Adds arithmetic intensity, reduces memory/cache usage

Ideally, operations can be performed using registers alone



Optimization Code Fusion

Optimization In GPU computing is not always possible

In the general case, it is not possible to know if c[index-1] has already been computed

Related Patterns

Three patterns related to map are discussed here:

- Stencil
- Workpile
- Divide-and-Conquer

More detail presented in a later lecture

CAD 2018/2019

SAXPY: $y \leftarrow ax + y$ a X y

y

CAD 2018/2019

SAXPY: $y \leftarrow ax + y$

0	1	2	3	4	5	6	7	8	9	10	11
4	4	4	4	4	4	4	4	4	4	4	4
2	4	2	1	8	3	9	5	5	1	2	1
3	7	0	1	4	0	0	4	5	3	1	0
↓	↓	↓	 	↓	 						
11	23	8	5	36	12	36	49	50	7	9	4

CAD 2018/2019

SAXPY: $y \leftarrow ax + y$

0	1	2	3	4	5	6	7	8	9	10	11
4	4	4	4	4	4	4	4	4	4	4	4
2	4	2	1	8	3	9	5	5	1	2	1
3	7	0	1	4	0	0	4	5	3	1	0
\	\	\	\	\	\	↓	\	↓	↓	↓	\
11	23	8	5	36	12	36	49	50	7	9	4

CAD 2018/2019