

BananaPro/Pi:Pin definition

From BananaPro/Pi

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Banana Pro pin definition

This page covers the PIN definitions of the Banana Pro, including **CON2**, **CON5**, **CON6**. All pins can be configured to the GPIO pins (General Purpose Input/Output), and some of them have one or two alternatives.

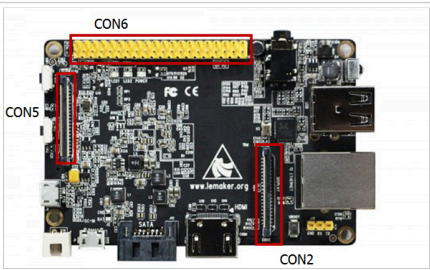
CON6 contains the CAN bus, SPI bus, I2C bus, PWM, serial port, etc. It can be configured to be used for many kinds of peripherals.

CON5 is a CSI camera connector. CON5 of Pro's pin denifition is same as CON1 of Pi

CON2 is an LVDS/RGB/CPU display connector.CON2 of Pro's pin denifition is same as CON2 of Pi

Banana Pro CON6									
Pin#	NAME					NAME	Pin#		
1	VCC-3.3V					VCC-5V	2		
3	TWI2-SDA					VCC-5V	4		
5	TWI2-SCK					GND	6		
7	IO-1					UART4_TX	8		
9	GND					UART4_RX	10		
11	IO-0 (UART2_RX)					PWM1	12		
13	IO-2 (UART2_TX)					GND	14		
15	IO-3 (UART2_CTS)					IO-4(CAN_TX)	16		
17	VCC-3.3V					IO-5(CAN_RX)	18		
19	SPI0_MOSI					GND	20		
21	SPI0_MISO					IO-6(UART2_RTS)	22		
23	SPI0_CLK					SPI0_CS0	24		
25	GND					SPI0_CS1	26		
27	TWI3-SDA					TWI3-SCK	28		
29	IO-7(IIR0_TX/SPDIF_MCLK)					GND	30		
31	UART7_RX					UART7_TX	32		
33	IO-8(SPDIF_DO)					GND	34		
35	I2S0_LRCK					I2S0_BCLK	36		
37	I2S0_MCLK					I2S0_DI	38		
39	GND					I2S0_DO0	40		

The Banana Pro's CON6 connectors



Location of the Banana Pro's connectors

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PIN define

GPIO

CON5-P01	LINEINL	
CON5-P02	LINEINR	
CON5-P37	HPL	
CON5-P36	HPR	
CON5-P07	FMINL	
CON5-P09	FMINR	
CON5-P04	ADC_X1	
CON5-P06	ADC_X2	
CON5-P08	ADC_Y1	
CON5-P10	ADC_Y2	
CON5-P13	LRADC0	
CON5-P15	LRADC1	
CON5-P33	RESET#	
CON5-P17	CSI-D0	PE4
CON5-P19	CSI-D1	PE5
CON5-P21	CSI-D2	PE6
CON5-P23	CSI-D3	PE7
CON5-P25	CSI-D4	PE8
CON5-P27	CSI-D5	PE9
CON5-P29	CSI-D6	PE10
CON5-P31	CSI-D7	PE11
CON5-P20	CSI-PCLK	PE0
CON5-P24	CSI-MCLK	PE1
CON5-P28	CSI-VSYNC	PE3
CON5-P30	CSI-HSYNC	PE2
CON5-P18	CSI0-STBY-EN	PH19
CON5-P26	CSI0-RESET#	PH14
CON5-P32	CSI1-STBY-EN	PH18
CON5-P34	CSI1-RESET#	PH13
CON5-P14	TWI1-SDA	PB19
CON5-P16	TWI1-SCK	PB18
CON5-P12	CSI-FLASH	PH17
CON5-P22	CSI0-PWR-EN	PH16
CON5-P35	CSI-IO0	PH11
CON5-P38	IPSOUT	
CON5-P40	IPSOUT	
CON5-P05	GND	
CON5-P11	GND	
CON5-P39	GND	
CON5-P03	VCC-CSI	
CON2-P09	LCD0-D00	PD0

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CON2-P13

LCD0-D02

PD2

CON2-P15	LCD0-D03	PD3
CON2-P17	LCD0-D04	PD4
CON2-P19	LCD0-D05	PD5
CON2-P21	LCD0-D06	PD6
CON2-P23	LCD0-D07	PD7
CON2-P25	LCD0-D08	PD8
CON2-P27	LCD0-D09	PD9
CON2-P29	LCD0-D10	PD10
CON2-P31	LCD0-D11	PD11
CON2-P33	LCD0-D12	PD12
CON2-P35	LCD0-D13	PD13
CON2-P37	LCD0-D14	PD14
CON2-P39	LCD0-D15	PD15
CON2-P40	LCD0-D16	PD16
CON2-P38	LCD0-D17	PD17
CON2-P36	LCD0-D18	PD18
CON2-P34	LCD0-D19	PD19
CON2-P32	LCD0-D20	PD20
CON2-P30	LCD0-D21	PD21
CON2-P28	LCD0-D22	PD22
CON2-P26	LCD0-D23	PD23
CON2-P22	LCD0-CLK	PD24
CON2-P20	LCD0-CS	PH6
CON2-P18	LCD0-HSYNC	PD26
CON2-P16	LCD0-VSYNC	PD27
CON2-P14	LCD0-DE	PD25
CON2-P12	LCD0-IO2	PH9
CON2-P10	PWM0	PB2
CON2-P08	LCD0-IO1	PH8
CON2-P06	LCD0-IO0	PH7
CON2-P04	TWI3-SCK	PI0
CON2-P02	TWI3-SDA	PI1
CON2-P07	LCDIO-03	PH12
CON2-P01	IPSOUT	
CON2-P03	IPSOUT	
CON2-P05	GND	
CON2-P24	GND	
CON6-P18	IO-5(CAN_RX)	PH21
CON6-P16	IO-4(CAN_TX)	PH20
CON6-P23	SPIO_CLK	PI11
CON6-P21	SPIO_MISO	PI13

CON6-P04	GPIO_C00	PH10
CON6-P26	SPI0_CS1	PH14
CON6-P05	TWI2-SCK	PB20
CON6-P03	TWI2-SDA	PB21
CON6-P28	TWI3-SCK	PI0
CON6-P27	TWI3-SDA	PI1
CON6-P15	IO-3(UART2_CTS)	PI17
CON6-P22	IO-6(UART2_RTS)	PI16
CON6-P11	IO-0(UART2_RX)	PI19
CON6-P13	IO-2(UART2_TX)	PI18
CON6-P10	UART4_RX	PH5
CON6-P08	UART4_TX	PH4
CON6-P31	UART7_RX	PI21
CON6-P32	UART7_TX	PI20
CON6-P07	IO-1	PH2
CON6-P29	IO-7(IRO_TX/SPDIF_MCLK)	PB3
CON6-P33	IO-8(SPDIIF_DO)	PB13
CON6-P12	PWM1	PI3
CON6-P35	I2S0_LRCK	PB07
CON6-P36	I2S0_BCLK	PB06
CON6-P37	I2S0_MCK	PB05
CON6-P38	I2S0_DI	PB12
CON6-P40	I2S0_DO0	PB08
CON6-P01	VCC-3V3	
CON6-P17	VCC-3V3	
CON6-P02	VCC-5V	
CON6-P04	VCC-5V	
CON6-P09	GND	
CON6-P25	GND	
CON6-P06	GND	
CON6-P14	GND	
CON6-P20	GND	
CON6-P30	GND	
CON6-P34	GND	
CON6-P39	GND	
LED1	GREEN LED	PH24
LED2	BLUE LED	PG2

Banana Pi pin definition

This page also covers the PIN definitions of the Banana Pi, including **CON1**, **CON2**, **CON3**, **J11** and **J12**. All pins can be configured to the GPIO pins (General Purpose Input/Output), and some of them have one or two alternatives.

Pin on Board	Pin Definition	IO on A20
CON1-P01	LINEINL	
CON1-P02	LINEINR	
CON1-P03	VCC-CSI	
CON1-P04	ADC_X1	
CON1-P05	GND	
CON1-P06	ADC_X2	
CON1-P07	FMINL	
CON1-P08	ADC_Y1	
CON1-P09	FMINR	
CON1-P10	ADC_Y2	
CON1-P11	GND	
CON1-P12	CSI-FLASH	PH17
CON1-P13	LRADC0	
CON1-P14	TWI1-SDA	PB19
CON1-P15	LRADC1	
CON1-P16	TWI1-SCK	PB18
CON1-P17	CSI-D0	PE4
CON1-P18	CSI0-STBY-EN	PH19
CON1-P19	CSI0-D1	PE5
CON1-P20	CSI-PCLK	PE0
CON1-P21	CSI-D2	PE6
CON1-P22	CSI0-PWR-EN	PH16
CON1-P23	CSI-D3	PE7
CON1-P24	CSI0-MCLK	PE1
CON1-P25	CSI-D4	PE8
CON1-P26	CSI0-RESET	PH14
CON1-P27	CSI-D5	PE9
CON1-P28	CSI-VSYNC	PE3
CON1-P29	CSI-D6	PE10
CON1-P30	CSI-HSYNC	PE2
CON1-P31	CSI-D7	PE11
CON1-P32	CSI1-STBY-EN	PH18
CON1-P33	RESET	
CON1-P34	CSI1-RESET	PH13
CON1-P35	CSI-IO0	PH11
CON1-P36	HPR	
CON1-P37	HPL	
CON1-P38	IPSOUT	

Pin on Board	Pin Definition	IO on A20
CON2-P01	IPSOUT	
CON2-P02	TWI3-SDA	PI1
CON2-P03	IPSOUT	
CON2-P04	TWI3-SCK	PI0
CON2-P05	GND	
CON2-P06	LCD0-IO0	PH7
CON2-P07	LCDIO-03	PH12
CON2-P08	LCD0-IO1	PH8
CON2-P09	LCD0-D0	PD0
CON2-P10	PWM0	PB2
CON2-P11	LCD0-D1	PD1
CON2-P12	LCD0-IO2	PH9
CON2-P13	LCD0-D2	PD2
CON2-P14	LCD0-DE	PD25
CON2-P15	LCD0-D3	PD3
CON2-P16	LCD0-VSYNC	PD27
CON2-P17	LCD0-D4	PD4
CON2-P18	LCD0-HSYNC	PD26
CON2-P19	LCD0-D5	PD5
CON2-P20	LCD0-CS	PH6
CON2-P21	LCD0-D6	PD6
CON2-P22	LCD0-CLK	PD24
CON2-P23	LCD0-D7	PD7
CON2-P24	GND	
CON2-P25	LCD0-D8	PD8
CON2-P26	LCD0-D23	PD23
CON2-P27	LCD0-D9	PD9
CON2-P28	LCD0-D22	PD22
CON2-P29	LCD0-D10	PD10
CON2-P30	LCD0-D21	PD21
CON2-P31	LCD0-D11	PD11
CON2-P32	LCD0-D20	PD20
CON2-P33	LCD0-D12	PD12
CON2-P34	LCD0-D19	PD19
CON2-P35	LCD0-D13	PD13
CON2-P36	LCD0-D18	PD18
CON2-P37	LCD0-D14	PD14
CON2-P38	LCD0-D17	PD17

CON2-P40	LCD0-D16	PD16
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Banana Pi CON3 definition

Pin on Board	Pin Definition	IO on A20
CON3-P01	VCC-3.3V	
CON3-P02	VCC-5V	
CON3-P03	TWI2-SDA	PB21
CON3-P04	VCC-5V	
CON3-P05	TWI2-SCK	PB20
CON3-P06	GND	
CON3-P07	GPCLK	PI3
CON3-P08	UART3-TX	PH0
CON3-P09	GND	
CON3-P10	UART3-RX	PH1
CON3-P11	IO-0(UART2-RX)	PI19
CON3-P12	IO-1	PH2
CON3-P13	IO-2(UART2-TX)	PI18
CON3-P14	GND	
CON3-P15	IO-3(UART2-CTS)	PI17
CON3-P16	IO-4(CAN_TX)	PH20
CON3-P17	VCC-3.3V	
CON3-P18	IO-5(CAN_RX)	PH21
CON3-P19	SPI0_MOSI	PI12
CON3-P20	GND	
CON3-P21	SPI0-MISO	PI13
CON3-P22	IO-6(UART2_RTS)	PI16
CON3-P23	SPI0_CLK	PI11
CON3-P24	SPI0_CS0	PI10
CON3-P25	GND	
CON3-P26	SPI0_CS1	PI14

Banana Pi J11 definition

Pin on Board	Pin Definition	IO on A20
J11-P01	UART0-TX	PB22
J11-P02	UART0-RX	PB23

Pin on Board	Pin Definition	IO on A20
J12-P01	VCC-5V	
J12-P02	VCC-3.3V	
J12-P03	IO-7	PH5
J12-P04	UART7_RX	PI21
J12-P05	IO-8	PH3
J12-P06	UART7_TX	PI20
J12-P07	GND	
J12-P08	GND	

References

- <http://linux-sunxi.org/A20/PIO>
- http://linux-sunxi.org/Fex_Guide

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