

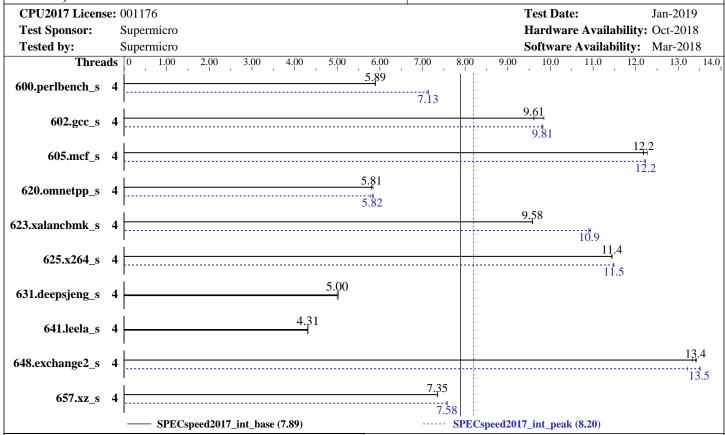
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Supermicro

SPECspeed2017_int_base = 7.89

SuperWorkstation 5039C-T (X11SCA, Intel Core i3-8300)

SPECspeed2017_int_peak = 8.20



Hardy	ware
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CPU Name: Intel Core i3-8300

Max MHz.: 3700 Nominal: 3700

Enabled: 4 cores, 1 chip

Orderable: 1 chip

Cache L1: 32 KB I + 32 KB D on chip per core L2: 256 KB I+D on chip per core L3: 8 MB I+D on chip per chip

Other: None

Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2666V-E,

running at 2400)

Storage: 1 x 200 GB SATA III SSD

Other: None

Software

OS: SUSE Linux Enterprise Server 12 SP3 (x86_64)

Kernel 4.4.114-94.11-default

Compiler: C/C++: Version 18.0.2.199 of Intel C/C++

Compiler for Linux;

Fortran: Version 18.0.2.199 of Intel Fortran

Compiler for Linux

Parallel: Yes

Firmware: Version 1.0a released Sep-2018

File System: xfs

System State: Run level 3 (multi-user)

Base Pointers: 64-bit Peak Pointers: 32/64-bit

Other: jemalloc memory allocator library V5.0.1



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CPU2017 License: 001176Test Date:Jan-2019Test Sponsor:SupermicroHardware Availability:Oct-2018Tested by:SupermicroSoftware Availability:Mar-2018

Results Table

	Base							Peak						
Benchmark	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	4	301	5.90	302	5.89	<u>301</u>	<u>5.89</u>	4	<u>249</u>	<u>7.13</u>	249	7.14	250	7.10
602.gcc_s	4	404	9.85	414	9.61	<u>414</u>	<u>9.61</u>	4	405	9.83	406	9.80	<u>406</u>	<u>9.81</u>
605.mcf_s	4	385	12.3	388	12.2	<u>387</u>	<u>12.2</u>	4	387	12.2	386	12.2	<u>386</u>	<u>12.2</u>
620.omnetpp_s	4	281	5.80	279	5.84	<u>281</u>	<u>5.81</u>	4	282	5.78	<u>280</u>	<u>5.82</u>	279	5.85
623.xalancbmk_s	4	148	9.59	148	9.57	<u>148</u>	<u>9.58</u>	4	129	10.9	130	10.9	<u>130</u>	<u>10.9</u>
625.x264_s	4	<u>154</u>	<u>11.4</u>	154	11.4	154	11.4	4	153	11.5	154	11.5	<u>154</u>	<u>11.5</u>
631.deepsjeng_s	4	285	5.03	<u>287</u>	<u>5.00</u>	287	5.00	4	285	5.03	<u>287</u>	<u>5.00</u>	287	5.00
641.leela_s	4	<u>396</u>	<u>4.31</u>	396	4.30	395	4.31	4	<u>396</u>	4.31	396	4.30	395	4.31
648.exchange2_s	4	221	13.3	219	13.4	<u>219</u>	<u>13.4</u>	4	222	13.2	218	13.5	<u>218</u>	<u>13.5</u>
657.xz_s	4	841	7.35	<u>841</u>	<u>7.35</u>	841	7.35	4	816	7.58	<u>816</u>	<u>7.58</u>	817	7.57

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM

memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases



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Platform Notes

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-65nv Sun Jan 20 04:11:41 2019
SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
  https://www.spec.org/cpu2017/Docs/config.html#sysinfo
From /proc/cpuinfo
   model name : Intel(R) Core(TM) i3-8300 CPU @ 3.70GHz
      1 "physical id"s (chips)
      4 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
      cpu cores : 4
      siblings : 4
      physical 0: cores 0 1 2 3
From lscpu:
                            x86_{64}
     Architecture:
     CPU op-mode(s):
                            32-bit, 64-bit
     Byte Order:
                            Little Endian
     CPU(s):
     On-line CPU(s) list:
                            0 - 3
     Thread(s) per core:
                            1
     Core(s) per socket:
     Socket(s):
                             1
     NUMA node(s):
                             1
     Vendor ID:
                            GenuineIntel
     CPU family:
     Model:
                            158
     Model name:
                            Intel(R) Core(TM) i3-8300 CPU @ 3.70GHz
     Stepping:
                            11
     CPU MHz:
                            3700.610
     CPU max MHz:
                            3700.0000
     CPU min MHz:
                            800.0000
     BogoMIPS:
                            7391.95
     Virtualization:
                            VT-x
     Lld cache:
                             32K
     Lli cache:
                             32K
     L2 cache:
                             256K
                             8192K
     L3 cache:
     NUMA node0 CPU(s):
                            0 - 3
                           fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
     Flags:
     pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
     lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
     aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3 sdbg fma
```



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Platform Notes (Continued)

cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch arat epb invpcid_single pln pts dtherm hwp hwp_notify hwp_act_window hwp_epp intel_pt rsb_ctxsw spec_ctrl retpoline kaiser tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmil avx2 smep bmi2 erms invpcid mpx rdseed adx smap clflushopt xsaveopt xsavec xgetbvl

```
/proc/cpuinfo cache data
   cache size : 8192 KB
From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
  available: 1 nodes (0)
 node 0 cpus: 0 1 2 3
 node 0 size: 64283 MB
 node 0 free: 51746 MB
 node distances:
 node
       Ω
    0: 10
From /proc/meminfo
                 65826480 kB
   MemTotal:
   HugePages_Total:
                         Ω
                       2048 kB
   Hugepagesize:
From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 3
      # This file is deprecated and will be removed in a future service pack or release.
      # Please check /etc/os-release for details about this release.
   os-release:
     NAME="SLES"
      VERSION="12-SP3"
      VERSION_ID="12.3"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp3"
uname -a:
   Linux linux-65nv 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
   x86_64 x86_64 x86_64 GNU/Linux
Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown):
                                   Mitigation: PTI
```



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Test Date: CPU2017 License: 001176 Jan-2019 **Test Sponsor:** Supermicro Hardware Availability: Oct-2018 **Tested by:** Supermicro **Software Availability:** Mar-2018

Platform Notes (Continued)

CVE-2017-5753 (Spectre variant 1): Mitigation: Barriers CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

run-level 3 Jan 19 15:17

SPEC is set to: /home/cpu2017

Filesystem Type Size Used Avail Use% Mounted on /dev/sda3 xfs 145G 31G 115G 21% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.0a 09/27/2018

4x Micron 18ADF2G72AZ-2G6H1R 16 GB 2 rank 2667, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

CC 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base, peak) 657.xz_s(base)
icc (ICC) 18.0.2 20180210 Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
======================================
icc (ICC) 18.0.2 20180210 Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base) 641.leela_s(base)
icpc (ICC) 18.0.2 20180210 Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
CXXC 620.omnetpp_s(peak) 623.xalancbmk_s(peak) 631.deepsjeng_s(peak)



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Test Date: CPU2017 License: 001176 Jan-2019 **Test Sponsor:** Supermicro Hardware Availability: Oct-2018 Software Availability: Mar-2018 Tested by: Supermicro

Compiler Version Notes (Continued)

641.leela_s(peak) icpc (ICC) 18.0.2 20180210 Copyright (C) 1985-2018 Intel Corporation. All rights reserved. ______ FC 648.exchange2_s(base, peak) ifort (IFORT) 18.0.2 20180210 Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icc -m64 -std=c11

C++ benchmarks: icpc -m64

Fortran benchmarks:

ifort -m64

Base Portability Flags

600.perlbench_s: -DSPEC LP64 -DSPEC LINUX X64

602.gcc_s: -DSPEC LP64 605.mcf_s: -DSPEC_LP64 $620.omnetpp_s: -DSPEC_LP64$

623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX

625.x264_s: -DSPEC_LP64 631.deepsjeng_s: -DSPEC_LP64 641.leela_s: -DSPEC LP64 648.exchange2_s: -DSPEC_LP64 657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div

(Continued on next page)



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CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Jan-2019
Hardware Availability: Oct-2018
Software Availability: Mar-2018

Base Optimization Flags (Continued)

C benchmarks (continued):

- -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
- -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

- -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:

- -Wl,-z,muldefs -xCORE-AVX2 -ipo -03 -no-prec-div
- -qopt-mem-layout-trans=3 -nostandard-realloc-lhs
- -L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:

icc -m64 -std=c11

C++ benchmarks (except as noted below):

icpc -m64

623.xalancbmk_s:icpc -m32 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin

Fortran benchmarks:

ifort -m64

Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64



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CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Jan-2019
Hardware Availability: Oct-2018
Software Availability: Mar-2018

Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -W1,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-prefetch -ipo -03
-qopt-mem-layout-trans=3 -no-prec-div
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc
602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-prefetch -ipo -03
-qopt-mem-layout-trans=3 -no-prec-div
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc
605.mcf_s: -Wl, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -03 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc
625.x264_s: -W1,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc
657.xz_s: Same as 602.qcc s
C++ benchmarks:
620.omnetpp_s: -W1, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -03 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -DSPEC SUPPRESS OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc
623.xalancbmk_s: -W1, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -03 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC OPENMP -L/usr/local/je5.0.1-32/lib -ljemalloc
631.deepsjeng_s: basepeak = yes
641.leela_s: basepeak = yes
Fortran benchmarks:
-W1,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-gopt-mem-layout-trans=3 -nostandard-realloc-lhs
-L/usr/local/je5.0.1-64/lib -ljemalloc
```



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The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.html http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SKL-revD.html

You can also download the XML flags sources by saving the following links:

 $\label{limit} $$ $$ $ \begin{array}{l} \text{http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SKL-revD.xml http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SKL-r$

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