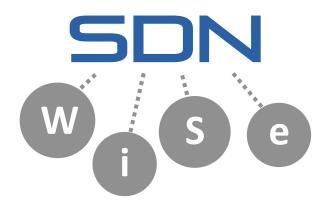
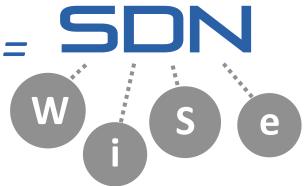
L. Galluccio, S. Milardo, G. Morabito, and S. Palazzo



A "wise" choice for Wireless Sensor Networks Management, Experimentation, and Application Development

Presented by

Sebastiano Milardo R&D Engineer @ CNIT-Catania SDN + WIreless SEnsors =



Requirements

- Support nodes with scarce resources
- Reduce energy consumption
- Increase network flexibility

Proposed solutions

- Logically centralized approach
- Flow rules, virtual sensor networks
- Duty cycles, data aggregation, stateful SDN in WSN

SDN-WISE Statefulness

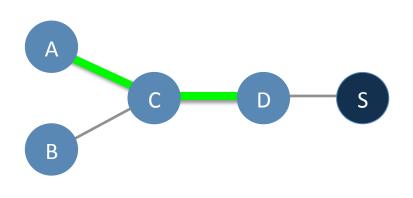


Fig.1: Data_B \geq X_{thr}

- Extremely hard to implement with traditional SDN solutions:
 - No packets handling depending on the content of another packet

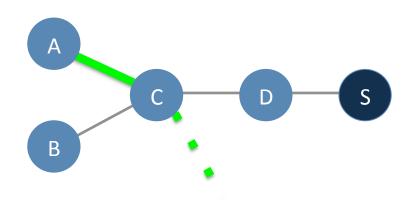


Fig.2: Data_B < X_{thr}

G. Bianchi, M. Bonola, A. Capone, and C. Cascone.

OpenState: Programming Platformindependent Stateful OpenFlow Applications Inside the Switch. ACM Computer Communication Review. Vol. 44, No. 2, pp.: 45-51. April 2014.

SDN-WISE Simplicity

- Managing a WSN with SDN-WISE is simple
 - Firmware overhead ≈ 1000 lines of code ≈ 10kB
 Flash Memory
 - "C" + "Any other language that can handle UDP sockets or Web Services (SOAP, REST)"
 - The behaviour of each node is encoded in just three data structures:
 - WISE Flow Table
 - WISE State Array
 - Accepted IDs Array

Proposed Architecture: Control Plane

CONTROLLER

It controls network topology representation, routing and other parameters management

WISE-VISOR

It abstracts the network resources so different logical networks can be created

ADAPTATION

It formats packets received from the WSN (Real/Simulated) in such a way they can be handled by the upper layers and *vice versa*.

Proposed Architecture: Data Plane

TOPOLOGY DISCOVERY (TD)

It manages Parameters
Configuration, Beaconing,
Neighbours reporting

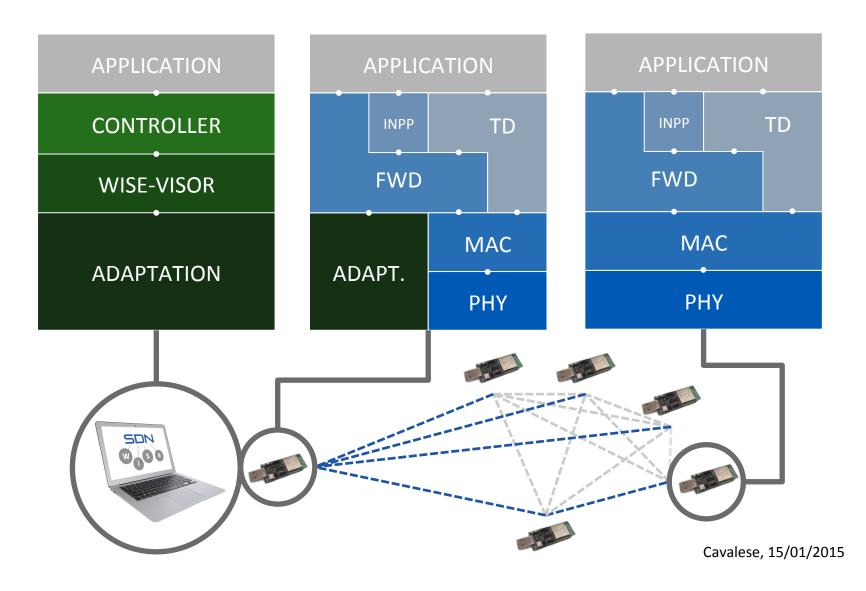
IN-NETWORK PACKET PROCESSING (INPP)

It is responsible for Data aggregation, *Network Coding, Compressive Sensing*

FORWARDING (FWD)

It handles incoming packets as specified in the WISE Flow Table

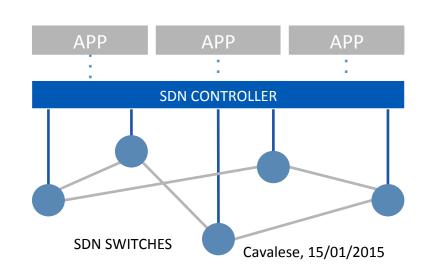
Example of SDN-WISE Network



Setting up the network

Targets

- Nodes have to learn the best path to reach the Control Plane
- The Control Plane has to create a representation of the network
- The protocol is based on
 - Beaconing
 - Periodic reporting of packets containing
 - Topological information
 - Battery level



WISE Flow Table

| | | Matching Rule |
|----------|--------------------|---------------|
| Operator | ==,!=, >=, <=,>,<; | |

Size 0 - 2

Location Packet, State Array

Offset 0 - 255;

Value 0x00 - 0xFF

Action Type Forward Unicast/Multicast, Drop, Modify, Send to INPP, Send to APP, Turn Off

Multimatch: True/False

Location Packet, State Array

Offset 0 - 255;

Value 0x00 - 0xFF

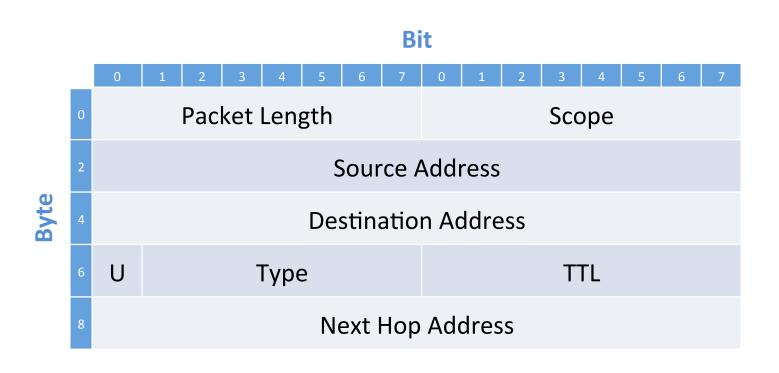
| | Statistics |
|--------------|------------|
| Time To Live | 0 - 255 |
| Counter | 0 - 255 |

WISE Flow Table

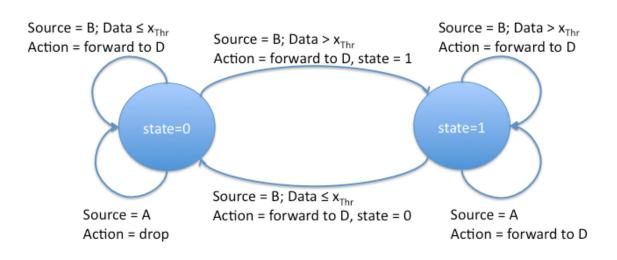
- If no rule matches a packet, the node asks the Control Plane
 - Rule Request
 - Rule Response
- The response is up to the Control Plane
 - Dijkstra's Algorithm
 - Static Routing

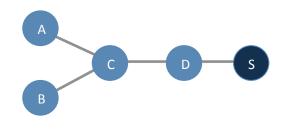
— ...

SDN-WISE: Packet Header



| | M | /latching | Rule | | | M | latching Rul | le | | | M | latching Rul | lle | | | A | ction | | | Stat | tistics |
|----------|------|-----------|----------|-------|----------|------|--------------|--------|-------|----------|------|--------------|--------|-------|---------|------------|-----------|--------|-------|------|---------|
| Operator | Size | Location | Offset | Value | Operator | Size | Location | Offset | Value | Operator | Size | e Location | Offset | Value | Туре | Multimatch | Location | Offset | Value | ΠL | Count |
| = | 2 | PACKET | SRC_ADDR | R В | >= | 2 | PACKET | DATA | Xthr | = | 1 | STATE_ARR | 0 | 0 | MODIFY | TRUE | STATE_ARR | 0 | 1 | 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | R В | < | 2 | PACKET | DATA | Xthr | = | 1 | STATE_ARR | R 0 | 1 | MODIFY | TRUE | STATE_ARR | . 0 | 0 | 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | R В | - | - | - | - | - | - | - | - | - | - | FORWARD | FALSE | PACKET | 0 | D | 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | R A | = | 1 | STATE_ARR | . 0 | 0 | - | 1 | - | - | - | DROP | FALSE | PACKET | 0 | 100% | 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | Α | = | 1 | STATE_ARR | 0 | 1 | - | - | - | - | - | FORWARD | FALSE | PACKET | 0 | D | 255 | 0 |





Cavalese, 15/01/2015

| | N | latching | Rule | | | M | atching Rul | le | | | М | atching Ru | le | | | Ad | ction | | | Stat | istics |
|----------|------|----------|----------|-------|----------|------|-------------|--------|-------|----------|------|------------|--------|-------|---------|------------|-----------|--------|-------|------|--------|
| Operator | Size | Location | Offset | Value | Operator | Size | Location | Offset | Value | Operator | Size | Location | Offset | Value | i yoe | Multimatch | Location | Offset | Value | TTL | Count |
| = | 2 | PACKET | SRC_ADDR | В | >= | 2 | PACKET | DATA | Xthr | = | 1 | STATE_ARR | 0 | 0 | MODIFY | TRUE | STATE_ARR | 0 | 1 | 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | В | < | 2 | PACKET | DATA | Xthr | = | 1 | STATE_ARR | 0 | 1 | MODIFY | TRUE | STATE_ARR | 0 | 0 | 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | В | - | - | - | - | - | - | - | - | - | - | FORWARD | FALSE | PACKET | 0 | D | 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | Α | = | 1 | STATE_ARR | 0 | 0 | - | - | - | - | - | DROP | FALSE | PACKET | 2 | 100% | 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | Α | = | 1 | STATE_ARR | 0 | 1 | - | - | - | - | - | FORWARD | FALSE | PACKET | 0 | D | 255 | 0 |
| | | | | | | | | | | | | | | | | | | | | | |

| Operator | Size | Location | Offset | Value | Operator | Size | Location | Offset | Value | Operator | Size | Location | Offset | Value |
|----------|------|----------|----------|-------|----------|------|----------|--------|-------|----------|------|-----------|--------|-------|
| = | 2 | PACKET | SRC_ADDR | В | >= | 2 | PACKET | DATA | Xthr | = | 1 | STATE_ARR | 0 | 0 |
| = | 2 | PACKET | SRC_ADDR | В | < | 2 | PACKET | DATA | Xthr | = | 1 | STATE_ARR | 0 | 1 |

| | M | /latching I | Rule | | | M | atching Rul | le | | | M | latching Rul | le | | | A | ction | | | Staf | tistics |
|----------|------|-------------|----------|-------|----------|------|-------------|--------|-------|----------|------|--------------|--------|---------|---------|------------|-----------|--------|-------|-------|---------|
| Operator | Size | Location | Offset | Value | Operator | Size | Location | Offset | Value | Operator | Size | Location | Offset | t Value | Туре | Multimatch | Location | Offset | Value | TTL | Count |
| = | 2 | PACKET | SRC_ADDR | R В | >= | 2 | PACKET | DATA | Xthr | = | 1 | STATE_ARK | 0 | 0 | MODIFY | TRUE | STATE_ARR | 0 | 1 | 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | R В | < | 2 | PACKET | DATA | Xthr | = | 1 | STATE_ARR | R 0 | 1 | MODIFY | TRUE | STATE_ARR | S | 0 | 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | R B | - | - | - | - | - | - | - | - | - | /- | FORWARD | FALSE | PACKET | 0 | D | 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | R A | = | 1 | STATE_ARR | 0 | 0 | - | - | - | - | - | DROP | FALSE | PACILET | 0 | 1.00% | 6 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | R A | = | 1 | STATE ARR | 0 | 1 | - | - | | - | - | FORWARD | FALSE | ACKET | 0 | D | 255 | 0 |

| Туре | Multimatch | Location | Offset | Value |
|--------|------------|-----------|--------|-------|
| MODIFY | TRUE | STATE_ARR | 0 | 1 |
| MODIFY | TRUE | STATE_ARR | 0 | 0 |

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| | N | /latchin _{ | g Rule | | | M | atching Ru | le | | | М | atching Ru | le | | | A | ction | | | Stat | istics |
|---------|--------|-----------------------|---------|-------|----------|------|------------|--------|-------|----------|------|------------|--------|-------|---------|------------|----------|--------|-------|------|--------|
| Operato | r Size | Location | Offset | Value | Operator | Size | Location | Offset | Value | Operator | Size | Location | Offset | Value | Туре | Multimatch | Location | Offset | Value | TTL | Coun |
| = | 2 | PACKET | SRC_ADD | R B | >= | 2 | PACKET | DATA | Xthr | = | 1 | STATE_ARR | 0 | 0 | MODIFY | TRUE | STATE_AF | RR 0 | 1 | 255 | 0 |
| = | 2 | PACKET | SRC_ADD | R B | < | 2 | PACKET | DATA | Xthr | = | 1 | STATE_ARR | 0 | 1 | MODIFY | TRUE | STATE_AF | RR O | 0 | 255 | 0 |
| = | 2 | PACKET | SRC_ADD | R B | | - | - | - | - | - | - | - | - | - | FORWARD | FALSE | PACKET | 0 | D | 255 | 0 |
| = | 2 | PACKET | SRC_ADD | R A | = | 1 | STATE_ARR | 0 | 0 | - | - | - | - | / | DROP | FALSE | PACKET | 0 | 100% | 255 | 0 |
| = | 2 | PACKET | SRC_ADD | R A | = | 1 | STATE_ANR | 0 | 1 | - | - | /- | -/ | - | FORWARD | FALSE | PACKET | 0 | D | 255 | 0 |
| | | | | | | | | | | | | | | | | | | | | | |
| Ol | oera | ator | Size | Locat | ion | C | Offset | Va | alue | | Ту | /pe | Mu | ltim | atch | Locati | on | Offse | t V | alu | e |
| | = | | 2 | PACI | KET S | SRC | _ADDF | ₹ | В | FC |)R\ | WARD | F | ALS | SE . | PACK | ET | 0 | | D | |

| | M | /latching | Rule | | | M | atching Rul | le | | | M | latching Ru | ile | | | A | ction | | | Stat | tistics |
|----------|------|-----------|----------|-------|----------|------|-------------|--------|-------|----------|------|-------------|--------|-------|---------|------------|-----------|--------|-------|------|---------|
| Operator | Size | Location | Offset | Value | Operator | Size | Location | Offset | Value | Operator | Size | Location | Offset | Value | Туре | Multimatch | Location | Offset | Value | TTL | Count |
| = | 2 | PACKET | SRC_ADDR | В | >= | 2 | PACKET | DATA | Xthr | = | 1 | STATE_ARR | . 0 | 0 | MODIFY | TRUE | STATE_ARR | 0 | 1 | 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | В | < | 2 | PACKET | DATA | Xthr | = | 1 | STATE_ARR | ₹ 0 | 1 | MODIFY | TRUE | STATE_ARR | R 0 | 0 | 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | В | - | - | - | - | - | - | - | - | - | - | FORWARD | FALSE | PACKET | 0 | D | 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | R A | = | 1 | STATE_ARR | 0 | 0 | | - | | - | - | DROP | FALSE | PACKET | 0 | 100% | 255 | 0 |
| - | 2 | PACKET | SRC_ADDR | A | = | 1 | STATE_ARR | . 0 | 1 | - | - | - | | _ | FORWARD | FALSE | PACKET | 0 | D | 255 | 0 |

| Operato | Size | Location | Offset | Value | Operator | Size | Location | Offset | Value |
|---------|------|----------|----------|-------|----------|------|-----------|--------|-------|
| = | 2 | PACKET | SRC_ADDR | Α | = | 1 | STATE_ARR | 0 | 0 |
| = | 2 | PACKET | SRC_ADDR | Α | = | 1 | STATE_ARR | 0 | 1 |

| | M | latching | Rule | | | Ma | atching Rul | e | | | M | atching Ru | le | | | Ac | ction | | | Stat | tistics |
|----------|------|----------|----------|-------|----------|------|-------------|--------|-------|----------|------|------------|--------|-------|---------|------------|-----------|--------|-------|------|---------|
| Operator | Size | Location | Offset | Value | Operator | Size | Location | Offset | Value | Operator | Size | Location | Offset | Value | Туре | Multimatch | Location | Offset | Value | TTL | Count |
| = | 2 | PACKET | SRC_ADDR | В | >= | 2 | PACKET | DATA | Xthr | = | 1 | STATE_ARR | 0 | 0 | MODIFY | TRUE | STATE_ARR | 0 | 1 | 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | В | < | 2 | PACKET | DATA | Xthr | = | 1 | STATE_ARR | 0 | 1 | MODIFY | TRUE | STATE_ARR | 0 | 0 | 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | В | - | - | - | - | - | - | - | - | - | - | FORWARD | FALSE | PACKET | 0 | D | 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | Α | = | 1 | STATE_ARR | 0 | 0 | - | - | ·- | | - | DROP | FALSE | PACKET | 0 | 100% | 255 | 0 |
| = | 2 | PACKET | SRC_ADDR | Α | = | 1 | STATE_ARR | 0 | 1 | | | | - | - | FORWARD | FALSE | PACKET | 0 | D | 255 | 0 |
| | | | | | | | | | | | | | | | | | | | | | |

| Туре | Multimatch | Location | Offset | Value |
|---------|------------|----------|--------|-------|
| DROP | FALSE | PACKET | 0 | 100% |
| FORWARD | FALSE | PACKET | 0 | D |

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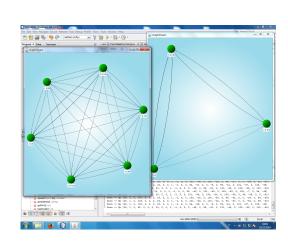
SDN-WISE: Prototype and Testbeds

- Simulated Testbed
 - OMNeT++

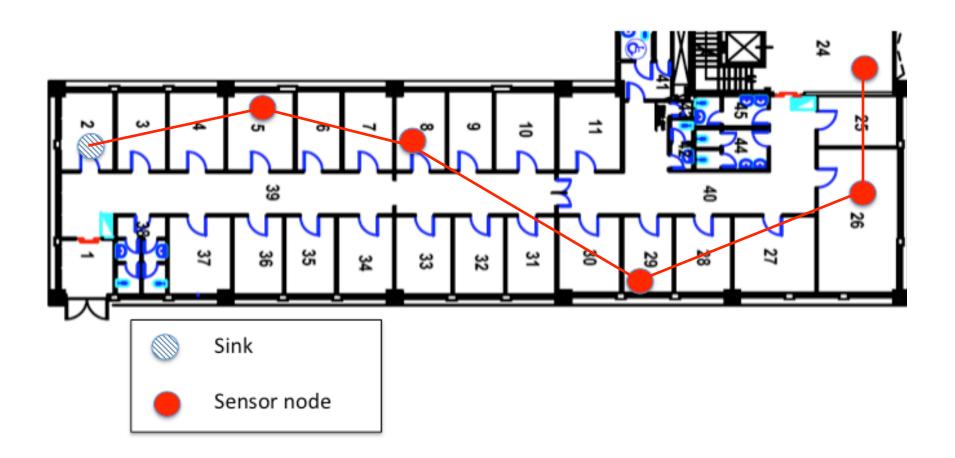


- 6 Embit EMB-Z2530PA
- IEEE 802.15.4
- TI CC2530 single chip device 8051 8-bit controller
- 8kB of RAM and 256kB of Flash memory
- Controller Plane + Application
 - Intel Core 2 CPU @ 2.40 GHz
 - 4GB of RAM
 - Windows 7 32 bit + Java 7

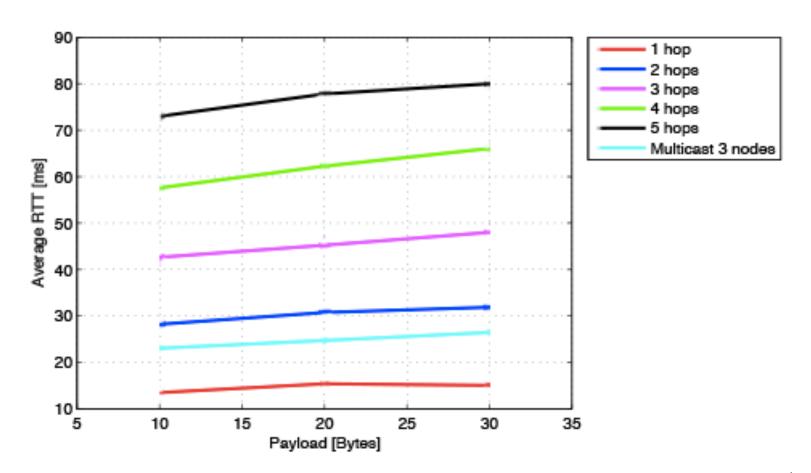




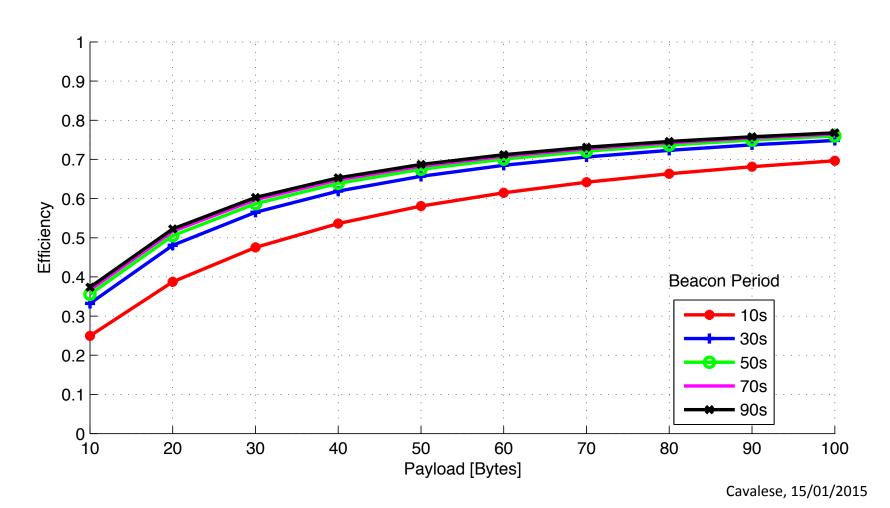
Physical Testbed



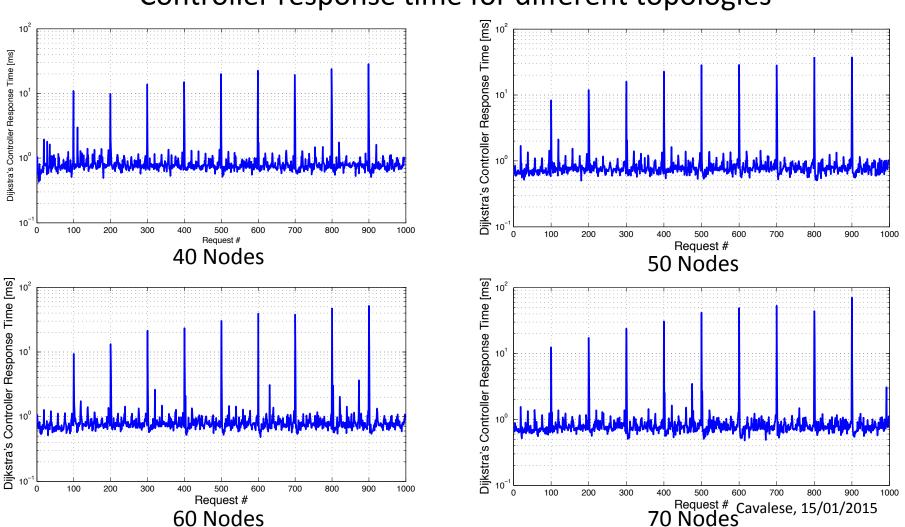
Average RTT vs. payload size for different values of number of hops



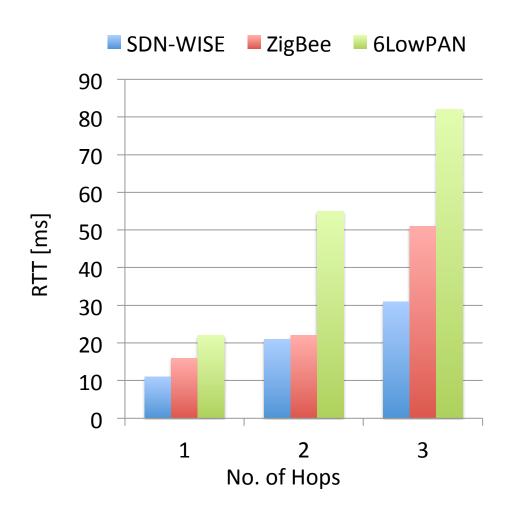
Efficiency for different values of beacon sending period



Controller response time for different topologies



Unicast traffic: RTT as a function of the number of hops when transmitting 20 bytes of payload SDN-WISE vs ZigBee vs 6LowPan



C. Buratti, G. Gardasevic, S. Milardo, M. D. Abrignani, S. Mijovic, A. Stajkic, G. Morabito, and R. Verdone.

Testing Protocols for The Internet of Things on The EuWIn Platform

Submitted to IEEE Internet of Things

Journal

Conclusions and Future Work

- We presented a stateful SDN solution for WSN
- We highlighted how this approach can simplify management and increase flexibility
- Open Issues: Security, INPP, QoS
- http://www.diit.unict.it/users/gmorabi/

Further details can be found in our paper:

L. Galluccio, S. Milardo, G. Morabito, and S. Palazzo.

SDN-WISE: design, prototyping and experimentation of a stateful SDN solution for WIreless SEnsor networks

In The 34th Annual IEEE International Conference on Computer Communications (INFOCOM 2015),

Hong Kong, P.R. China, April 2015



Questions?