

Ruchi Saha



EDUCATION	Dual Degree (Btech + Mtech) Computer Engineering CGPA: 8.89/10	(2015 - 2020)
	Indian Institute of Information Technology, Design and Manufacturing, Kancheep Class XII Percentage: 91.8, Kendriya Vidyalaya AFS Uttarlai Class X CGPA: 10/10, Kendriya Vidyalaya AFS Uttarlai	(2014) (2012)
RESEARCH EXPERIENCE	Resource-Efficient and High-Throughput FPGA-based Multi-match Packet Classification Architecture for NIDS (Final Year Project) Work submitted to the journal of Computer Systems Science and Engineering (awaiting review) (A short summary of work here). Supervised by Dr Noor Mahammad SK, supported by Ministry of Electronics and Information Technology, Govt. of India	
	Network Processor (Research Intern) IIITDM Kancheepuram (July - Oct'19) Designed an assembly language, assembler, simulator plus emulator and a firmware for a customized Hardware (github link). Supervised by Dr Noor Mahammad SK	
	Privacy Preserving Public Auditing with Data Deduplication for Secure Data Storage in Fog to Cloud based IoT (Cryptography Research Intern) IDRBT, Hyderabad (May - Sep'19) Researched on Data Integrity and Deduplication in fog to cloud based IoTs (link), Supervised by Dr P Syam Kumar	
	Network System (Research Intern) Optimized hardware for IPV4 packet forwarding engine, Supervised by Dr Noor I	I Kancheepuram (May - July'17) Mahammad SK
TEACHING EXPERIENCE	Teaching Assistant Courses Assisted: Digital and Analog Design, Design and Analysis of Algorithms. Assisted students in labs. Assisted professors in creating and evaluating assignments, projects and tests.	
Industrial Experience	Engineer, Backend Server Samsung Research Institute, Delhi (Jan'21 - Present) Worked in the domain of Cloud and Database Management. Designed and deployed services in multiprocessing cloud environment.	
Network Analyst (Intern) G Worked in the field of automation and networking. Worked with Info customised open source network monitoring softwares.		ologies, Chennai (May - July'18) anagement Systems. Studied and
SELECTED PROJECTS	Network Management System Designed a Software to help monitor health of the network for college Network Ir	I Kancheepuram (Aug - Nov'19) afra Department.
	32-bit Pipelined Processor (Without control circuits) IIITDM Kancheepuram (Feb - July'17) Made a 32-bit pipelined processor in verilog HDL and analysed area power and delay of so (link).	
ACHIEVEMENTS	Ford CSAM IoT Security Paper Presentation (link), Won Best Paper Award GATE (Graduate Aptitude Test in Engineering), All India Rank 1855 International Maths Olympiad, School Rank 2nd International Maths Olympiad, School Rank 4th	(2019) (2019) (2010) (2009)
EXTRA- CURRICULAR	Technical Affairs Coordinator, IIITDM Kancheepuram Volunteer (organizing team)-World Space Week, ISRO-SHAR School Captain, Kendriya Vidyalaya AFS Uttarlai School Vice Captain, Kendriya Vidyalaya AFS Uttarlai	(2016 - 2017) (4 - 10 Oct 2016) (2013 - 2014) (2012 - 2013)
Competences	Skills and Tools C/C++, Scala, AWS, Python, Postgres, git, Verilog, Xilinx ISE, I	<i>E</i> T _E X

Hobbies Painting, Reading Comics

Languages Hindi (native), English

AND HOBBIES