module muc\_newtest;

reg [1:0]a;

reg [1:0]b;

reg clk;

reg asign;

reg bsign;

wire [3:0]p;

wire sign;

muc\_new FF(a,b,clk,asign,bsign,p,sign);

initial begin

a=2'b11;b=2'b01;asign=1'b0;bsign=1'b1;

#1000 a=2'b11;b=2'b11;asign=1'b1;bsign=1'b1;

end

initial

begin

clk=1'b1;

forever #100 clk=~clk;

end

endmodule