JHOU YAN-RU
+886-975-791-527 | joe04024@gapp.nthu.edu.tw

EDUCATION

National Tsing Hua University (NTHU), Hsinchu (Sep 2014 – Jun 2018)
Bachelor degree of Computer Science
Overall GPA: 4.09/4.30; Class rank: 3/41

WORK AND PROJECT EXPERIENCE

Chunghwa Telecom Laboratories, Taipei (Jul 2018 – Aug 2018)
Intern
- Developed HiCloud CaaS/CVPC Java/Python SDK for developers
- Improved front-end user portal to enhance user usability
- Tested on HiCloud Rest API to improve its security

"**US Tax Computing Program with Software Testing Technique (C, Python)**," NTHU (Apr 2018 – May 2018)
- Implemented a tax computing program in C that yields the tax value based on 1040EZ
- Tested black box from boundary value analysis, equivalence testing, and decision table with Python
- Tested white box with 100% coverage

PRIP Lab, NTHU (Feb 2017 – Jan 2018)
"Data Processing and Analysis by MATLAB: A Study on Taiwanese Population Data Between 1974 and 2016." Supervisor: Prof. Chen Chaur-Chin
- Built auto regression (AR) mathematical model with MATLAB and predicted Taiwan's population against time, including total population, regional population, aging rates, birthrates, and death rates

"**Student Grading System by Agile Development Method (Java)**," NTHU (Mar 2017 – Apr 2017)
- Implemented a student grade system using the Agile method
- Responsible for the architecture, design, and testing of the grading system

"**Simple File Transfer Protocol Program** (**C++ Socket Programming)**," NTHU (Mar 2017 – Apr 2017)
- Implemented a simple FTP concurrent server that allows multiple users to download and upload files simultaneously

"**Carry Select Adder Cell-Based IC Design (Virtuoso & HSpice)**," NTHU (Nov 2016 – Dec 2016)
- Designed IC layout; simulated circuit behavior, including timing and functional correctness with Spice; achieved top 10% Area & Delay rank in class

"**IIR (infinite impulse response) system**," NTHU (May 2016 – Jun 2016)
- Used Verilog to implement an IIR (infinite impulse response) system to filter the noise data in a sounds data, turning the annoying sounds into pleasant sounds

"**Single Cycle Processor Simulator with Memory Management**," NTHU (May 2016 – Jun 2016)
- Used C++ to implement a single-cycle CPU simulator based on MIPS R3000 ISA with memory hierarchy, Translation-Lookaside Buffer (TLB), and virtual page table mechanism

"**Sokoban Game on FPGA**," NTHU (Nov 2015 – Jan 2016)
- Used Verilog to design a Sokoban Game on FPGA board with audio and visual effects, which allowed players to play on a screen from VGA

EXTRACURRICULAR ACTIVITIES

Phi Tau Phi Scholastic Honor Society, Taiwan (Jun 2018 – present)
Honorary Member

Photography Club, NTHU (Sep 2015 – Jun 2018)
Member

Financial Club, NTHU (Sep 2014 – Jun 2015)
Member

SONG-SHAN Senior High School Alumni Association, NTHU (Sep 2014 –Jun 2018)
Member

Course Activity Leadership

Software Quality Assurance, Taiwan (Mar 2018 – Jun 2018)
Team Leader
- Investigated DevOps and CI/CD application in real-time software industry by interviewing with Rayark staff

Software Project Management, Taiwan (Sep 2017 – Jan 2018)
Team Leader
- Studied how a software company develops approaches and styles of management for software projects at Fugle, a fintech startup company

Culture Creativity and New Media, Taiwan (Feb 2016 – Jul 2016)
Team Leader
- Shot a short video to introduce college life at NTHU, and served as an actor in it

AWARDS

Presidential Award (3 times), Computer Science Dept., NTHU (Mar 2015 – Mar 2018)
- An award given to students with the top 5% GPA for that semester

EECS College Scholarship, College of Electronic Engineering and Computer Science, NTHU (Jun 2017)
- An award given to the top students in the College of EE & CS

SKILLS AND INTERESTS

Languages: Mandarin Chinese (native), Taiwanese (native), English (advanced)

Computers: C, C++, Python, Java, MATLAB, Verilog, Latex, Socket, OpenGL, Virtuoso & HSpice Digital IC Layout, JSON, HTML

Interests: Tennis, Digital photography, Classical music (Chopin)