

ABSTRACT

AWESOME C++ DISTRIBUTED LIBRARY FOR EFFECTIVELY UTILIZING PARALLEL MEMORY AND STUFF

by John David Rudie Jr.

This is a really great paper about some great stuff. Keywords: awesome, really cool, great

AWESOME C++ DISTRIBUTED LIBRARY FOR
EFFECTIVELY UTILIZING PARALLEL MEMORY AND STUFF

A Thesis (or Thesis Proposal)

Submitted to the
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by

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Acknowledgements

Add your acknowledgements here.

Chapter 1

Introduction

Use this chapter to introduce readers to your research.

Discuss motivations, contributions and other introductory material.

Note: In general, the suggestions provided in this template (for content, chapter breakdown, etc.) are just that - suggestions. Consult with your advisor to determine the appropriate thesis structure for your sub-discipline, and adjust accordingly.

1.1 Motivation

1.2 Contributions

This thesis will make the following contributions:

- an improved C++ memory allocator designed for effectively utilizing big memory
- modular distributed data structures that can easily be inserted into existing programs with little change in code complexity and a resultant increase in performance
 - vector
 - unordered multi-map
 - heap
- garbage collector designed for memory hungry programs running on distributed systems

Chapter 2

Background & Related Work

According to the International Data Corporation, the size of global data is expected to grow from 33 in 2018 to 175 zetabytes in 2025 as shown in Figure 2.1. [2] To put that into perspective, the variations in one human genome can be compressed in a lossless fashion to 4 megabytes of data [3], so the size of our data in 2025 will be equivalent to the digital representation of over 4 quadrillion humans.

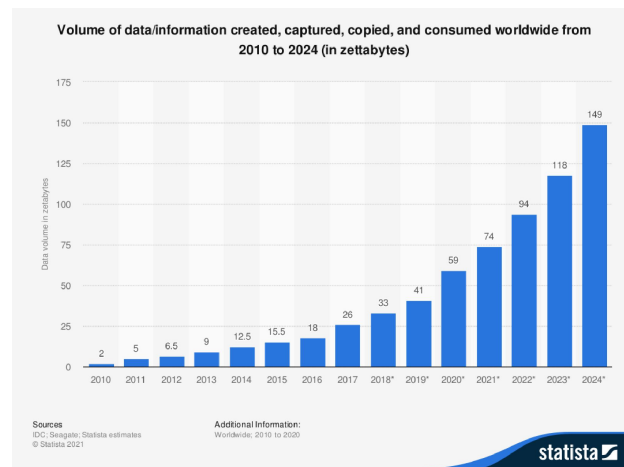


Figure 2.1: Global Data Growth through 2024. Source: International Data Corporation

Along with all of this growth in data comes a growth in the amount of memory with which applications must work. In high performance computing (HPC), we define a computing cluster as a co-located set of computers, each individually called a node, configured for accomplishing a shared task. Administrators have begun to include "big memory" nodes in these clusters which contain an above average amount of Random Access Memory (RAM) in order to address workloads that require excessive in-memory data. Prominent examples of these workloads include applications that utilize in-memory databases, graph analytics [4], and large simulations.

C++ is one of the most popular languages for distributed computing solutions, partly owing owing to the fact that it offers support for useful object-oriented abstractions in conjunction with highly optimized code. [5] While much prior work has been dedicated to creating distributed libraries in C++ in [6] [7] [8] [9] [10] [11] [12], other separate work has attempted to improve performance on big memory applications through various optimizations [4], and still other work has been dedicated to efficiently managing distributed memory

[13] [14] [15] [16] few, if any, attempts have been made to create a library of standard data structures that are designed with big memory capabilities and work loads in mind. Additionally, few general purpose distributed computing libraries are available to the public under open source distribution licenses, as many provide some sort of proprietary value add. This research will be an attempt to create such an open source distributed computing library optimized for big memory utilization, experimenting with novel implementation techniques for standard data structures and drawing from existing literature when necessary.

2.1 Parallel vs. Distributed Computing

Before detailing the most prominent continuing projects in this space, it is important to draw a distinction between parallel and distributed computing. In order to do that, clear definitions for parallel and distributed computing must be provided. While this thesis project will primarily be geared toward distributed applications running in high-performance computing (HPC) settings, it will also attempt to exploit parallelism, so understanding both is essential for a holistic view of the space in which the proposed project will exist. Definitions included below are a mixture of universal standards used in literature and what will be considered distributed/parallel computing for the purposes of this project.

Parallel Computing There are two main modes of parallelism in parallel computing: instruction-level parallelism and thread-level parallelism. Instruction-level parallelism refers to design techniques at the processor/compiler level that speed the execution of sequential programs by allowing individual machine instructions, e.g. additions, floating point multiplications, memory stores/loads, to execute in parallel [17]. This is typically done using different components of a processor to perform different parts of a given instruction at the same time. Modern processors typically have, at the very least, several floating point units and several integer units, which can handle different parts of the same instruction. The main distinction between instruction-level parallelism and thread-level parallelism is that instruction-level parallelism occurs at the processor level whereas thread-level parallelism exploits concurrency among multiple processors within the same computer [18]. The thread in thread-level parallelism refers to a thread of execution in which some part of a program runs its code. Threads are different from processes in that threads share the same memory space and are all added to the same existing process. These attributes give threads a distinct advantage over processes when one is working with multiple processors, as an arbitrary number of threads can work on the same shared data structure, either through synchronization or through splitting the structure up into discrete parts [18].

Distributed Computing The distinction between parallel computing and distributed computing is much like the distinction between instruction-level parallelism and thread-level parallelism. Whereas instruction-level parallelism occurs within one processor while thread-level parallelism occurs in a system with multiple processors, parallel computing occurs within one computer while distributed computing occurs across multiple computers.

Naturally, new and unique problems arise when a project migrates from a single system of computers to a cluster consisting of multiple computers that are just as challenging as the problems that arise when making sequential code parallel. Common problems include, but are not limited to heterogeneity in networks, hardware, programming languages, or implementation of standard constructs, openness of components within a distributed system, scalability, fault tolerance, concurrency of different resources across different machines, and unique quality of service (QoS) issues relating to availability, reliability, and performance that do not affect applications running on a single computer [19]. Distributed computing can refer to anything from blockchain networks, which distribute verification of information and transactions across many different systems, to web services architectures, in which a web application is broken into modular, discrete components which then run on different computers and communicate across the network, to cloud computing, in which innumerable virtual private servers are spun off and destroyed in tandem across many machines in a massive data center. This project will mainly focus on HPC settings in which memory-intensive scientific applications are run, but hopefully the deliverable will be able to be extended to cloud computing environments, and perhaps even to desktop computer environments if the memory management methods are applicable at this smaller scale. For the purposes of this project, computers with a discrete GPU or other processing component will not be considered distributed computing since many of the aforementioned challenges are less relevant when communicating between components within the same machine - offloading operations to a GPU is analogous to offloading to another CPU core or offloading cache contents to RAM.

2.2 First-Class Distributed Computing in C++

While the most powerful computers in research in industry continue to expand their processor and GPU counts, distributed computing is not yet included as a first class component of the C++ STL [5]. This forces many developers of high-performance scientific applications to reinvent the wheel for each individual project. Common parallel computing constructs, like threads, mutexes, and other locks, went through a similar process in C++. Exploring the history of other attempts to standardize distributed data structures, as well as analogous constructs, is essential to understand the full picture of distributed computing in C++.

Existing Constructs Despite the lack of distributed constructs and C++, there are many objects used in heterogeneous/parallel computing that serve as important references for any distributed data structures/algorithms. Most important among the aforementioned constructs are `std::async`, `std::future`, and `std::thread`. [20]

Recent attempts

Project	Paradigm	Architecture	Nested	Adaptive	Data Dist.	Scheduling	Continued development
PC2L	S/MPMD	Shared/Dist	Yes	Yes	Auto/User	?	Yes
STAPL	S/MPMD	Shared/Dist	Yes	Yes	Auto/User	Customizable	Yes
PSTL	SPMD	Shared/Dist	No	No	Auto	Tulip RTS	No
Charm++	MPMD	Shared/Dist	No	No	User	prioritized execution	Yes
CILK	S/MPMD	Shared/Dist	Yes	No	User	work stealing	No
NESL	S/MPMD	Shared/Dist	Yes	No	User	work and depth model	No
POOMA	SPMD	Shared/Dist	Yes	No	User	pthread scheduling	No
SPLIT-C	SPMD	Shared/Dist	Yes	No	User	user	No
X10	S/MPMD	Shared/Dist	No	No	Auto	-	Yes
Chapel	S/MPMD	Shared/Dist	Yes	No	Auto	-	Yes
Titanium	S/MPMD	Shared/Dist	No	No	Auto	-	No
Intel TBB	SPMD	Shared	Yes	Yes	Auto	work stealing	Yes

2.3 Memory in Distributed Applications

Since the beginning of the information era, there has been rapid development of better CPUs with increased cores and faster clock times in conjunction with an increase in demand of data accesses in many applications [21]. Since the affordability and speed of RAM has not followed that of CPUs, this means that the memory resources in a distributed system are now much more expensive relative to CPU cycles. As a result of this, any interventions that improve the efficiency of accessing and sharing memory, whether at an operating system level or user level, could greatly improve the profitability and efficiency of existing distributed applications. Literature presents several existing improvements to memory usage in distributed applications like [4] [21], this solution is unique in that it attempts to resolve many of the common pitfalls in distributed memory management at a language level.

2.4 Related Work

There exist a number of comparable libraries that have attempted to standardize commonly used distributed/parallel structures, or otherwise create a standard library for distributed computing in C++. Out of all of the libraries initially created to address distributed computing in C++, however, few are still in continued development in 2021. The table below compares PC2L, the new proposed solution, to various other attempts to solve the same problem. One additional significant difference not readily visible on the table is that none of these libraries attempt to solve any of the difficulties inherent in memory-bound distributed applications.

2.4.1 STAPL

Description The Standard Template Adaptive Parallel Library (STAPL) was developed by researchers at Texas A&M University in the late 1990s, far before the C++11 standard provided a set of standardized tools for concurrency. The library was originally created as a super-set of C++’s STL, with the intent to possibly replace the STL on small to medium multiprocessing systems [6]. STAPL is capable of running both single program multiple data

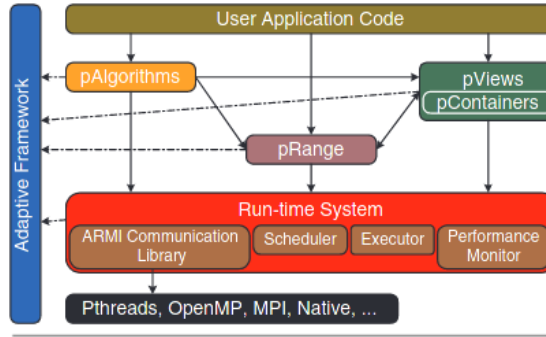


Figure 2.2: STAPL Architecture [1]

(SPMD) and multiple program multiple data applications, and its scheduling algorithm is customizable. STAPL, like the C++ STL, is a generic library, offering data structures and algorithms that can be exploited by a large number of heterogeneous applications. [6]. STAPL implements each distributed data structure as a thread-safe, concurrent object called a pContainer [1]. STAPL pContainers consist of a finite collection of typed elements, storage space, and an interface of methods/procedures that can be applied to the pContainer. [1] Each pContainer is globally addressable, meaning it provides shared memory address space, and can be composed with other containers to create new structures [1]. Distributed data structures implemented by STAPL include an array, a vector, a list, a matrix, a graph, a map, and a set. The library also includes common algorithms for these data structures like those one would find in the C++ STL. STAPL facilitates communication between data structures and algorithms by having algorithms communicate with pViews in the same way that algorithms in the C++ STL communicate with iterators [1]. This is done through a pRange object, which is a directed acyclic graph in which vertices represent tasks while edges represent dependencies between tasks. **TODO:** Write some stuff about the runtime system [22]

Contributions STAPL was designed with code translation in mind, so each parallel container within STAPL implements the same methods that would be implemented by its C++ STL counterpart [1]. For example, STAPL’s implementation of a vector, `stapl::pVector`, can be accessed, sorted, modified, and constructed using the exact same syntax as a `std::vector`. STAPL offers users the ability to modify the scheduling algorithm to better suit the needs of their own projects.

Optimizations for memory-bound programs

2.4.2 Charm++

Description Charm++ is a parallel programming framework designed to ease the development, execution, migration, and decomposition of parallel applications [10]. Charm++,

Figure 2.3: Charm++ Architecture

like STAPL, provides object-oriented abstractions to ease the development of distributed applications. Additionally, in contrast to STAPL, Charm++ provides its own interface for message passing that is inter-operable with MPI.

Contributions

2.4.3 X10

Figure 2.4: X10 Architecture

2.4.4 Chapel

Figure 2.5: Chapel Architecture

2.4.5 Intel Thread Building Blocks (TBB)

Figure 2.6: TBB Architecture

Chapter 3

Overview

This chapter will present a holistic view of your project. It should present the overall goals, and any architecture, and overarching elements.

Chapter 4

Content A

Use this chapter to discuss the main content of your thesis contributions.

You may have several of these chapters depending on your thesis - work with your advisor to determine chapter layouts. Remove or add as necessary.

4.1 Sub-Topic 1

4.1.1 Sub-Sub-Topic I

4.1.2 Sub-Sub-Topic II

4.2 Sub-Topic 2

4.3 Sub-Topic 3

Chapter 5

Content B

Use this chapter to discuss the main content of your thesis contributions.

You may have several of these chapters depending on your thesis - work with your advisor to determine chapter layouts. Remove or add as necessary.

5.1 Sub-Topic 1

5.1.1 Sub-Sub-Topic I

5.1.2 Sub-Sub-Topic II

5.2 Sub-Topic 2

5.3 Sub-Topic 3

Chapter 6

Content C

Use this chapter to discuss the main content of your thesis contributions.

You may have several of these chapters depending on your thesis - work with your advisor to determine chapter layouts. Remove or add as necessary.

6.1 Sub-Topic 1

6.1.1 Sub-Sub-Topic I

6.1.2 Sub-Sub-Topic II

6.2 Sub-Topic 2

6.3 Sub-Topic 3

Chapter 7

Validation

Use this chapter to describe the experiments you have run to validate your work. Describe the setup, show your data, and discuss the results.

7.1 Experimental Design

7.2 Results

Tables are great for showing results, as seen in Table 7.1.

Table 7.1: Sample Table with Data

Column Title	Column Title	Column Title
Row of Data	value	value
Row of Data	value	value
Row of Data	value	value
Row of Data	value	value

7.3 Discussion

Chapter 8

Conclusion

Present the following:

- summary of your work
- list of contributions
- limitations and drawbacks to your approach
- future work
- other important concluding details

Also, here's the Miami Logo to demonstrate including images in your thesis. The logo is seen in Figure 8.1.



Figure 8.1: The Miami University Logo

8.1 Future Work

Appendix A

Sample Appendix

Include data here and other details that fit appropriately in an Appendix.

You may have as many appendices as necessary, simply follow this one as a template for including more.

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