Project

Thursday, October 31, 2024   4:40 PM
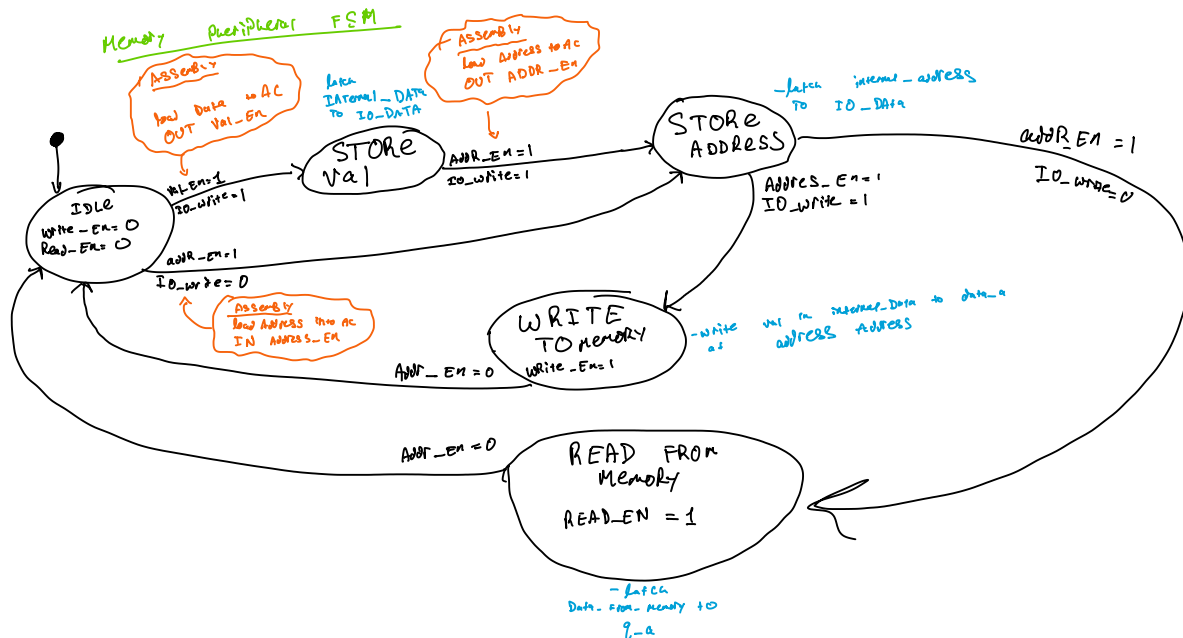

MEM_EN = 0x70
ADDR_EN = 0x71
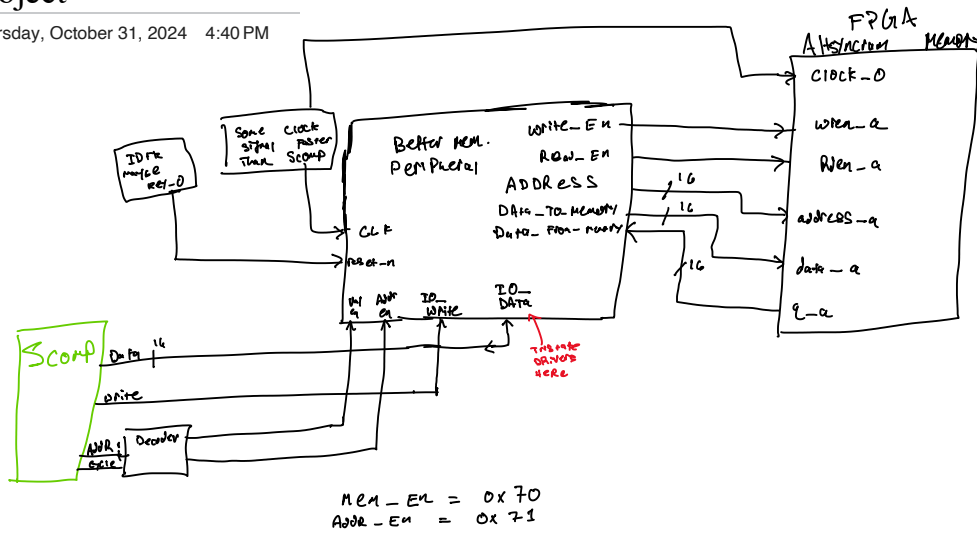
Steps to Store Data
1. load val into AC
2. OUT MEM_EN
3. load address into AC
4. OUT ADDR_EN

IO_write will be 1 and DATA_EN will be 1 ⇒ latch INTERNAL_DATA onto IO_DATA

IO_write will be 1 and ADDR_EN will be 1 ⇒ latch INTERNAL_ADDR onto IO_Data

Steps to Read Data
1. load address into AC
2. IN ADDR_Val

IO_write will be 0 and ADDR_EN will be 1 ⇒ latch IO_Data to q_a

⇒ write to memory

⇒ Read operation

Tristate Buffers in
Memory Peripheral

Internal Data

Internal Address

Data - FROM - Memory

(Io-Write=1
Val_En=1)

Adr-En=1
+ Rew_En= 0

Read-En = 1

I O_ Data

SCOMP   IO-Port   16