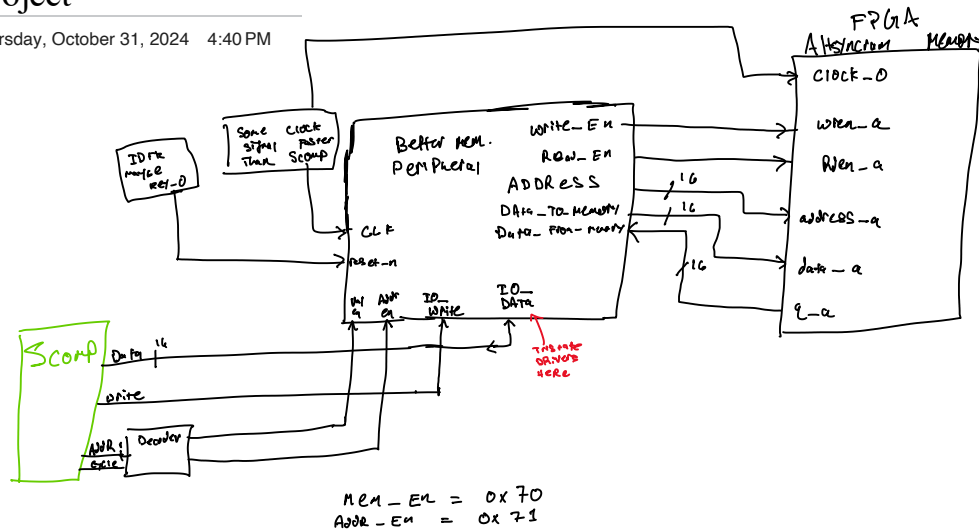


Project

Thursday, October 31, 2024 4:40 PM

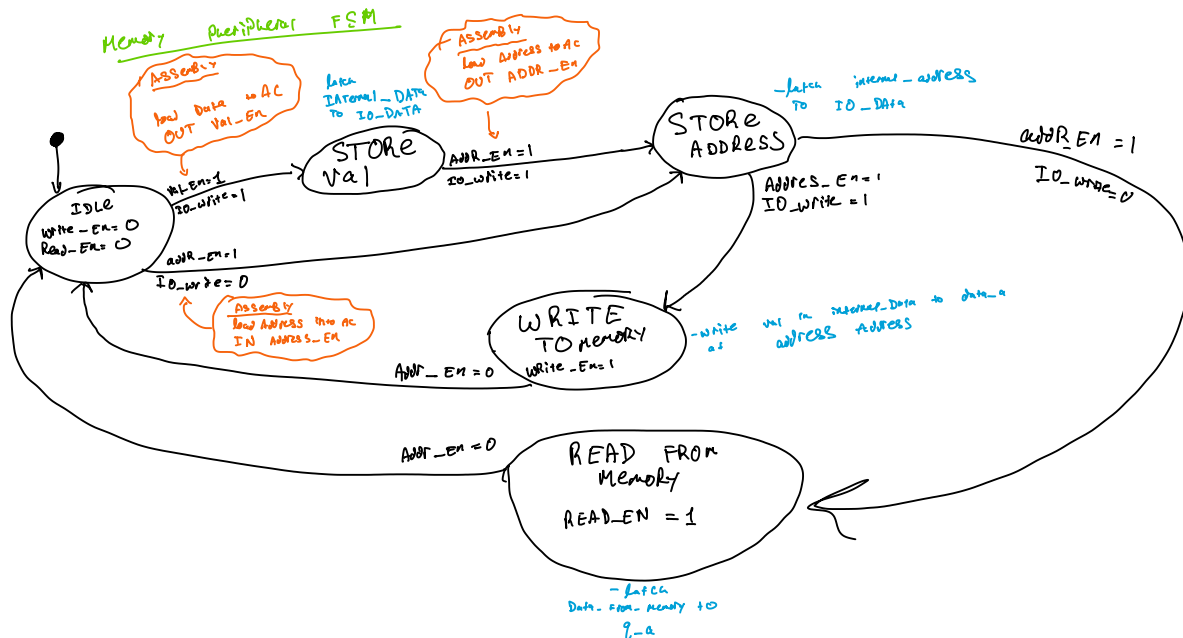


Steps to Store Data

- ① Load val into AC
 - ② OUT MEM-EN
 - ③ Load address into AC
 - ④ OUT ADDR-EN
- IO-Write will be 1
DATA-EN will be 1 ⇒ Latch Internal-Data onto IO-Data
- IO-Write will be 1
ADDR-EN will be 1 ⇒ Latch Internal-ADDR onto IO-Data

Steps to Read Data

- ① Load address into AC
 - ② IN ADDR-Val
- ⇒ Write to memory
- IO-Write will be 0
ADDR-EN will be 1 ⇒ Latch IO-Data to Q-a
- ⇒ Read operation



TriState Buffers in
Memory Peripherals

