Frquency increased by a factor of x, but the total power consumption of the chip and the chip area is still the same across generations, which means the chip power density remains the same throughout generations.

Protal,
$$n = P_{Total}$$
, $u+1$

Protal, $n = P_{Total}$, $u+1$

The frequency is scaled by a factor of 1/x

There will always be a small current flowing from the drain to source, regardless of the input voltage. Therefore, the inverter will always dissipate power even if the input voltage is 0V.

4)
$$t_1 = 20 \text{ ns}, P_1 = 1.3 \text{ W}$$

$$t_{zo} = \frac{t_1}{x^{20-1}} = \frac{20 \text{ ns}}{z^{\frac{1}{9}}} = 3.815 \times 10^{-14} \text{ s}$$

$$P_{zo} = \frac{P_1}{x^{\frac{1}{2}(20-1)}} = \frac{1.3 \text{ W}}{z^{\frac{38}{9}}} = 4.729 \times 10^{-12} \text{ W}$$