## ECE 3030: Physical Foundations of Computer Engineering

Spring 2024

Homework 6—Total points 100

Due on Thursday 4/10/2024 at 11.59am. In case of a late submission, you will be penalized by 50 points for each day after the submission deadline has passed. You will receive no score if you submit after the solution has been posted.

Q1 In class, we derived the following equations (square law model with correction for subthreshold current) relating the drain current  $I_D$ , the drain voltage  $V_D$  and the gate voltage  $V_G$  in a long-channel MOSFET.

$$\frac{I_D}{W} = \begin{cases} I_{sub-V_t} e^{\frac{q(V_G - V_t)}{mkT}} (1 - e^{\frac{-qV_D}{kT}}); \text{ when } V_G < V_t \text{ (sub-threshold)} \\ I_{sub-V_t} (1 - e^{\frac{-qV_D}{kT}}) + \mu C_{ox} \frac{1}{L} ((V_G - V_t)V_D - \frac{1}{2}V_D^2); \text{ when } V_G - V_t > V_D \text{ (linear)} \\ I_{sub-V_t} (1 - e^{\frac{-qV_D}{kT}}) + \mu C_{ox} \frac{1}{2L} (V_G - V_t)^2; \text{ when } V_G - V_t < V_D \text{ (saturation)} \end{cases}$$

We have also defined the on-current  $I_{ON}$  and the off-current  $I_{OFF}$  as follows.

$$I_{ON} = I_D(V_G = V_D = V_{DD})$$
  
 $I_{OFF} = I_D(V_G = 0, V_D = V_{DD})$ 

Here,  $V_{DD}$  is the power supply voltage, and  $qV_{DD} >> kT$ .

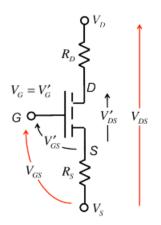
Q1.1 Based on these relations, find expressions for  $I_{ON}$  and  $I_{OFF}$  in terms of  $V_{DD}$ ,  $V_t$ ,  $I_{sub-V_t}$ ,  $\mu$ ,  $C_{ox}$ , W and L. Note that when  $V_G = V_D = V_{DD}$ , the MOSFET operates in the saturation region. [5 pts]

Q1.2 Based on the derived expressions, find how  $I_{ON}$  and  $I_{OFF}$  would change if  $V_t$  is increased. [5 pts]

## Solution to Q1:

- Q2 Consider an n-type MOSFET with  $N_A=7\times 10^{18}$  m³. The gate length of the MOSFET  $L=2~\mu\mathrm{m}$ , width  $W=12~\mu\mathrm{m}$  and the oxide thickness  $t_{ox}=8$  nm. Take  $N_C=N_V=10^{25}$  m³,  $E_G=1.12$  eV,  $n_i=1.5\times 10^{16}$  m³, kT=0.026 eV, vacuum permittivity  $\varepsilon_\circ=8.854\times 10^{-12}$  F/m, dielectric constant of oxide  $\varepsilon_{ox}=4$ , dielectric constant of silicon  $\varepsilon_{Si}=12$ , electron mobility  $\mu_n=230\times 10^{-4}$  m²/Vs, hole mobility  $\mu_p=83\times 10^{-4}$  m²/Vs.
  - (Q3.1) Calculate  $\phi_B = |E_F E_i|$ , the oxide capacitance  $C_{ox}$ , the maximum depletion width  $W_{max}$  and the threshold voltage  $V_t$ . [5 pts]
  - (Q3.2) Calculate the drain current  $I_D$  for the following six cases. [30 pts]
    - 1.  $V_G=3 \text{ V}, V_D=2 \text{ V}.$
    - 2.  $V_G$ =0.2 V,  $V_D$ =1 V.
    - 3.  $V_G$ =2.2 V,  $V_D$ =2 V.
    - 4.  $V_G=2 \text{ V}, V_D=1 \text{ V}.$
    - 5.  $V_G$ =0.5 V,  $V_D$ =0.5 V.
    - 6.  $V_G=1.5 \text{ V}, V_D=2 \text{ V}.$
- Q3 Real transistors have parasitic series resistances at the source and drain. As shown in the figure below, the result is that the voltages applied to the terminals of the device are not the voltages on the terminals of the intrinsic device. Modify the square law MOSFET equations to include the

effects of source and drain series resistances.



Prob. 3 Figure.

## Solution to Q3:

The voltages in the square law expressions are the intrinsic voltages – not the voltages applied to the terminals. Using prime to denote the intrinsic voltages:

$$I_{D} = \frac{W \overline{\mu}_{n} C_{ox}}{L} \left[ \left( V'_{GS} - V_{T} \right) V'_{DS} - \frac{{V'_{DS}}^{2}}{2} \right] \qquad 0 \le V'_{DS} < V_{Dsat} \ V'_{GS} \ge V_{T}$$

$$I_{D} = \frac{W \overline{\mu}_{n} C_{ox}}{2L} \left( V'_{GS} - V_{T} \right)^{2} \qquad V'_{DS} \ge V_{Dsat} \qquad V'_{GS} \ge V_{T}$$

Note that we use 
$$V_{GS}'$$
 instead of  $V_{GS}'$  and  $V_{DS}'$  instead of  $V_{GS}'$  because with a series

resistance, the intrinsic source is not at ground potential.

Straightforward circuit analysis gives the intrinsic voltages as

$$\begin{split} V_{GS}' &= V_G - I_D R_S \\ V_{DS}' &= V_{DS} - I_D \Big( R_S + R_D \Big) \end{split}$$

Inserting these voltages in the square law theory gives the answer. Note that  $I_D$  ends up on both sides of the equations, and it is not completely trivial to plot the IV with series resistance.

## Solution to Q2:

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$$\begin{array}{l}
\text{To}(V_0 = 3V_7) V_0 = 2V_5 - (V_0 - V_7) = 2.614V_7 V_0 - \frac{1}{2}V_0^2) \\
\text{To}(V_0 = 3V_7 V_0 = 2V) = \mathcal{U}_u Cox \frac{\mathcal{U}}{L} \left( V_0 - V_7 \right) V_0 - \frac{1}{2}V_0^2 \right) \\
= 610926 \times 10^{-4} \left[ 2.614 \times 2 - \frac{1}{3} \cdot 2^2 \right] \\
= 1.972 \text{ m } \theta
\end{array}$$

$$\emptyset V_{6}=0.2V, V_{0}=VV, V_{6} \leq V_{1}$$
 and  $(a+a+1)$ 

$$J_{0}(V_{6}=0.2V, V_{0}=V) = \sim 0 \quad (a+a+1)$$

$$= 1.025 \text{ mA}$$

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$$= 1.025 \text{ mA}$$

$$= 2 \text{ V}, \text{ V}_D = 1 \text{ V}, \text{ V}_D - \text{V}_4 = 1614 \text{ V} > \text{VD} \text{ and cond}^{\text{m}} \text{ (1)}$$

$$= 1.025 \text{ mA}$$

$$V_{t} = \sqrt{4202589NAPB} + 34B$$

$$= \sqrt{4 \times 8.854 \times 15^{12} \times 12} \times 1.6 \times 10^{-19} \times 7 \times 15^{8} \times 0.102$$

$$= \sqrt{4.827 \times 10^{-3}} + 2 \times 0.102$$

$$= 0.386 V$$

$$Q1.2.$$

$$I_{D} = \begin{cases} 0 & \text{if } V_{0} < V_{0} \\ V_{0} < V_{0} \end{cases} = \sqrt{6} < V_{0} \end{cases}$$

$$|V_{0} < V_{0} | V_{0}$$

$$\begin{array}{l} (5) \ V_{0} = 0.5 \, V, \ V_{D} = 0.5 \, V, \ (V_{0} - V_{+}) = 0.114 \, V \, \angle V_{D} \ \text{and} \ \text{condition} \\ \hline I_{1} \left( V_{0} = 0.5 \, V, V_{D} = 0.5 \, V \right) = \mathcal{M}_{1} \left( S_{1} \times \frac{W}{2L} \left( V_{0} - V_{+} \right)^{2} \\ &= \frac{1}{2} \times G \left( -8076 \times 16^{4} \left( -9.114 \right)^{2} \\ &= 3.077 \, \text{M} \, \text{A} \end{array}$$

$$\begin{array}{l} (5) \ V_{0} = 1.5 \, V, \ V_{D} = 2 \, V, \ (V_{0} - V_{+}) = 1.114 \, V \, \text{M} \, \text{C} \, V_{D} \, \text{and} \, \text{Cond} \, \text{C} \end{array}$$

 $I_{D}(V_{0}=1.5V), V_{D}=2512V) = M_{D}Cox \frac{W}{2L}(V_{0}-V_{4})^{2}$ =  $\frac{1}{2} \times 6 \cdot 10926 \times 10^{4} (1.114)^{2}$ = 0.379 m.A