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0:
%pgocount3 = load i64, ptr getelementptr inbounds ([3 x i64], ptr
... @__profc_main.2, i32 0, i32 2), align 8
%1 = add i64 %pgocount3, 1
store i64 %1, ptr getelementptr inbounds ([3 x i64], ptr @__profc_main.2,
... i32 0, i32 2), align 8
%pgocount = load i64, ptr getelementptr inbounds ([3 x i64], ptr
... @__profc_main, i32 0, i32 2), align 8
%2 = add i64 %pgocount, 1
store i64 %2, ptr getelementptr inbounds ([3 x i64], ptr @__profc_main, i32
... 0, i32 2), align 8
%3 = alloca i32, align 4
%4 = alloca [10 x i32], align 16
%5 = alloca [10 x i32], align 16
%6 = alloca i32, align 4
%7 = alloca i32, align 4
store i32 0, ptr %3, align 4
call void @llvm.memcpy.p0.p0.i64(ptr align 16 %4, ptr align 16
... @__const.main.A, i64 40, i1 false)
call void @llvm.memset.p0.i64(ptr align 16 %5, i8 0, i64 40, i1 false)
store i32 0, ptr %7, align 4
store i32 0, ptr %6, align 4
%newreg_load_src = alloca i32, align 4
%8 = load i32, ptr %7, align 4
store i32 %8, ptr %newreg_load_src, align 4
br label %9
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9:
%10 = load i32, ptr %6, align 4
%11 = icmp slt i32 %10, 10
br i1 %11, label %12, label %41, !prof !36
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12:
%newreg_load_dst = load i32, ptr %newreg_load_src, align 4
%13 = sext i32 %newreg_load_dst to i64
%14 = getelementptr inbounds [10 x i32], ptr %4, i64 0, i64 %13
%15 = load i32, ptr %14, align 4
%16 = mul nsw i32 %15, 13
%17 = add nsw i32 %16, 4
%18 = load i32, ptr %6, align 4
%19 = add nsw i32 %17, %18
%20 = load i32, ptr %6, align 4
%21 = sext i32 %20 to i64
%22 = getelementptr inbounds [10 x i32], ptr %5, i64 0, i64 %21
store i32 %19, ptr %22, align 4
%23 = load i32, ptr %6, align 4
%24 = srem i32 %23, 8
%25 = icmp eq i32 %24, 0
br i1 %25, label %26, label %30, !prof !37
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41:
ret i32 0
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26:
%pgocount4 = load i64, ptr getelementptr inbounds ([3 x i64], ptr
... @__profc_main.2, i32 0, i32 1), align 8
%27 = add i64 %pgocount4, 1
store i64 %27, ptr getelementptr inbounds ([3 x i64], ptr @__profc_main.2,
... i32 0, i32 1), align 8
%pgocount1 = load i64, ptr getelementptr inbounds ([3 x i64], ptr
... @__profc_main, i32 0, i32 1), align 8
%28 = add i64 %pgocount1, 1
store i64 %28, ptr getelementptr inbounds ([3 x i64], ptr @__profc_main, i32
... 0, i32 1), align 8
%29 = load i32, ptr %6, align 4
store i32 %29, ptr %newreg_load_src, align 4
br label %30
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30:
%31 = load i32, ptr %6, align 4
%32 = sext i32 %31 to i64
%33 = getelementptr inbounds [10 x i32], ptr %5, i64 0, i64 %32
%34 = load i32, ptr %33, align 4
%35 = call i32 (ptr, ...) @printf(ptr noundef @.str, i32 noundef %34)
br label %36
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36:
%pgocount5 = load i64, ptr @__profc_main.2, align 8
%37 = add i64 %pgocount5, 1
store i64 %37, ptr @__profc_main.2, align 8
%pgocount2 = load i64, ptr @__profc_main, align 8
%38 = add i64 %pgocount2, 1
store i64 %38, ptr @__profc_main, align 8
%39 = load i32, ptr %6, align 4
%40 = add nsw i32 %39, 1
store i32 %40, ptr %6, align 4
br label %9, !llvm.loop !38
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CFG for 'main' function