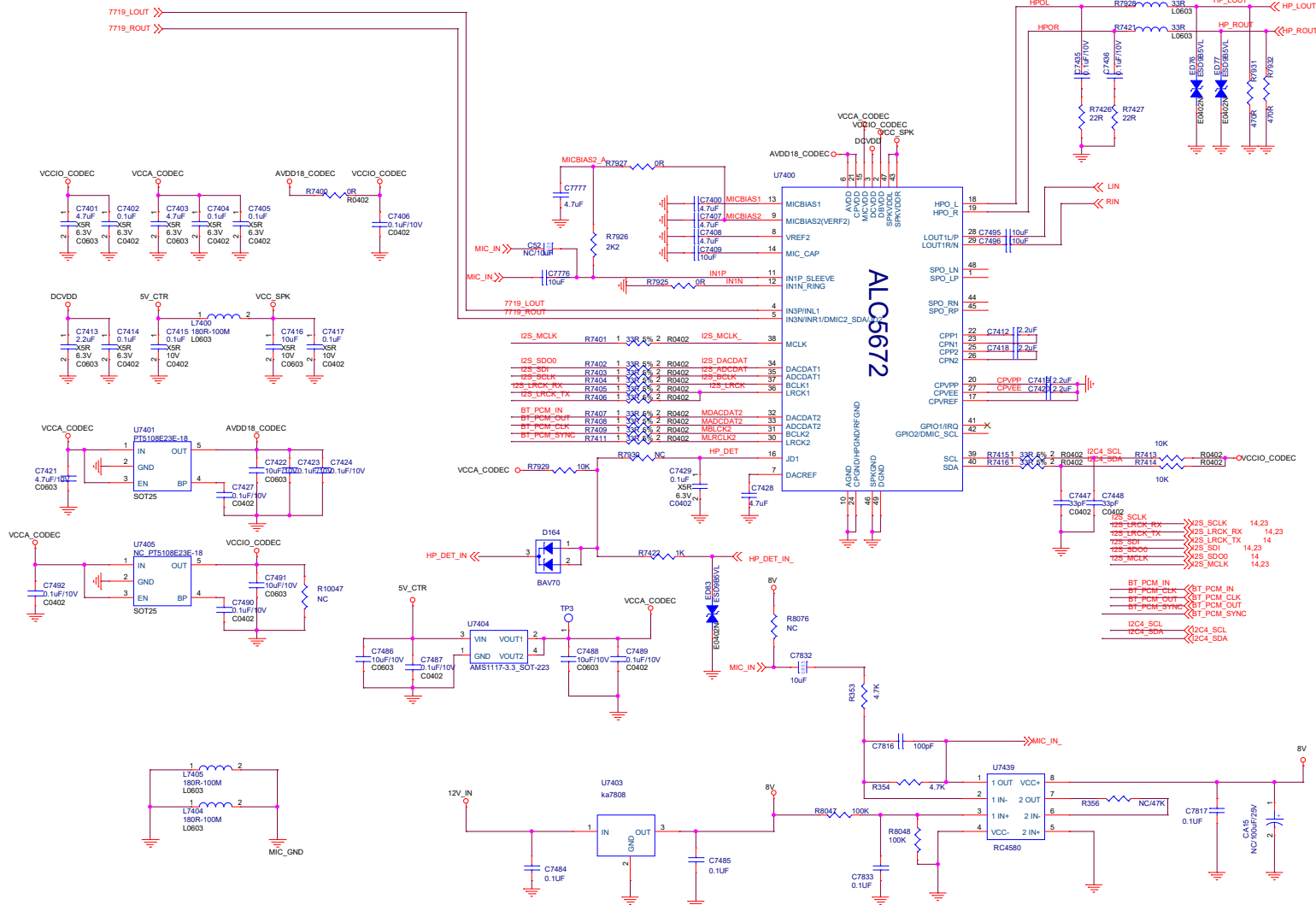
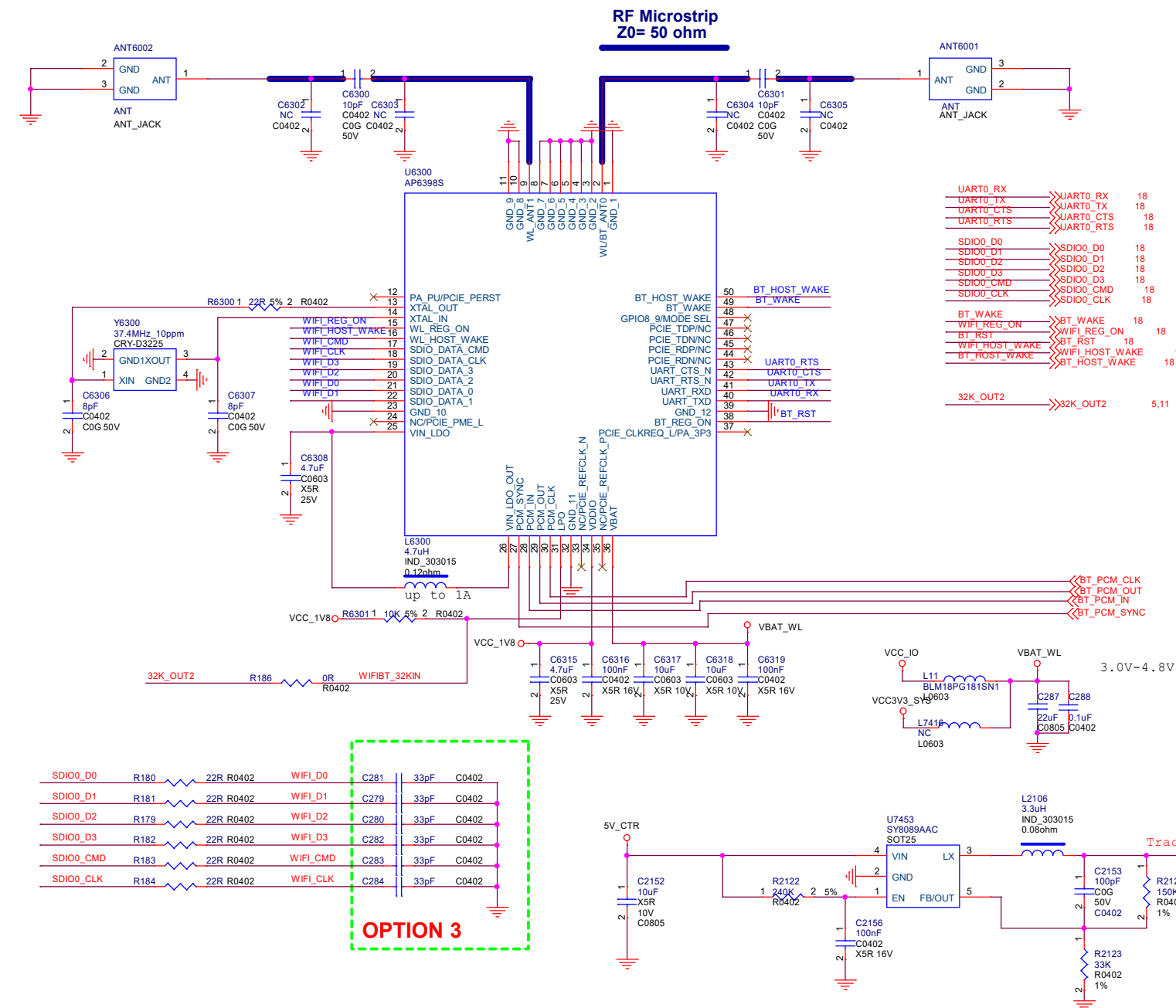
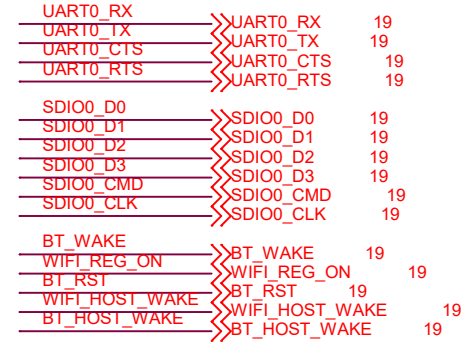
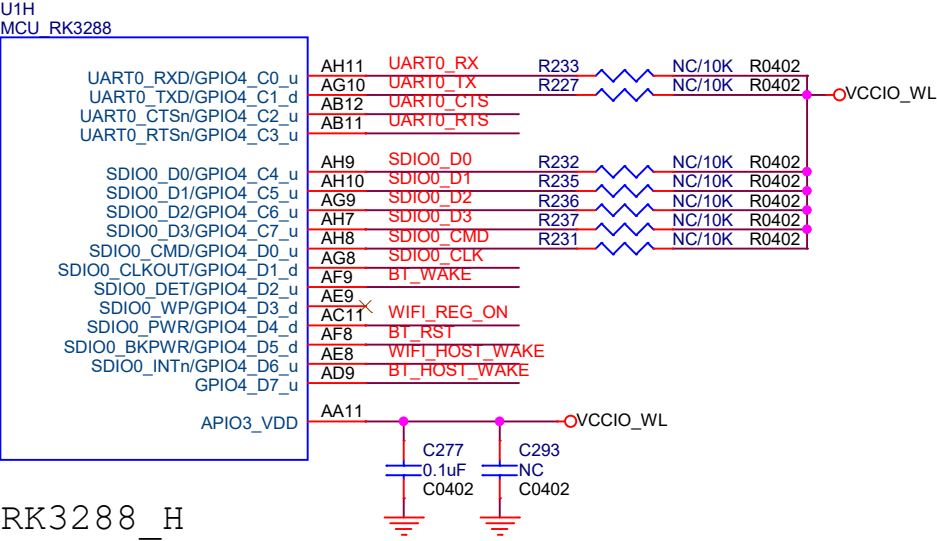


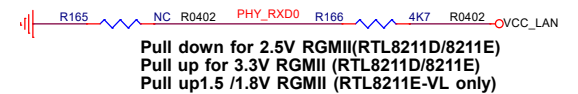
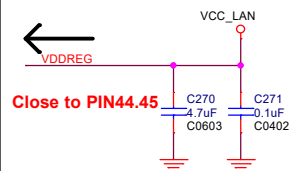
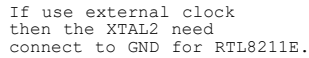
CODEC



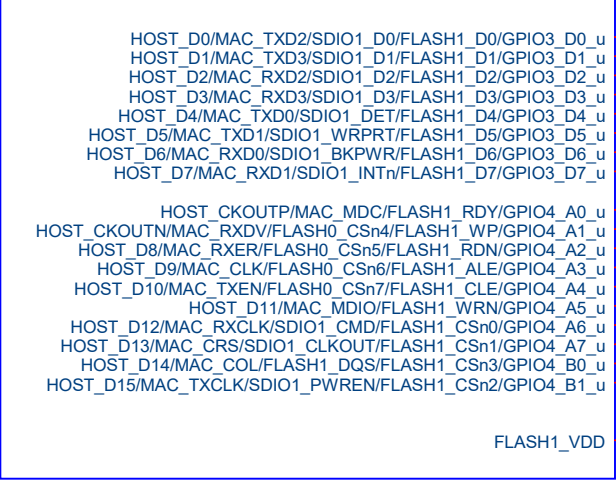
Note:VBAT voltage range is 3.0V~4.8V,
and peak-current is at least 400mA.



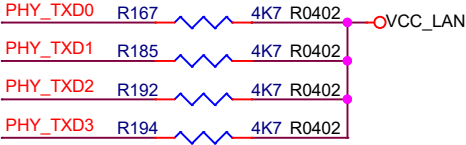
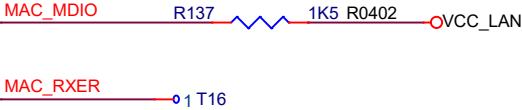
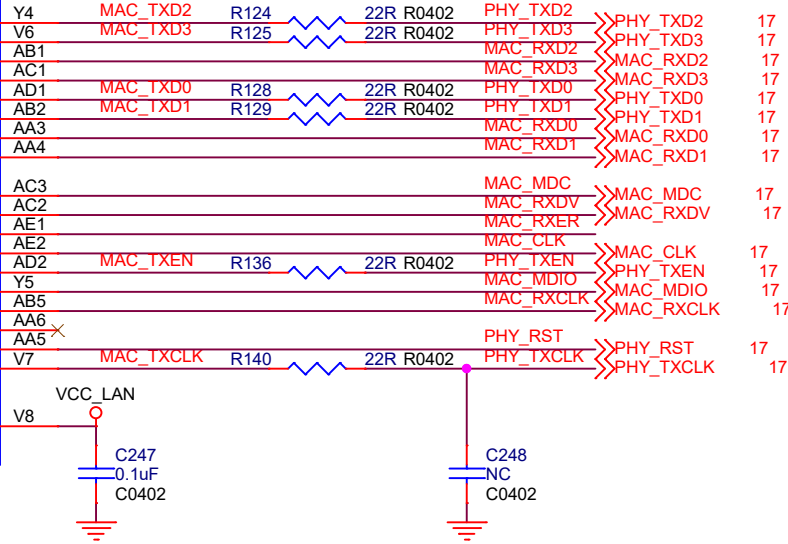




U1Q
MCU_RK3288



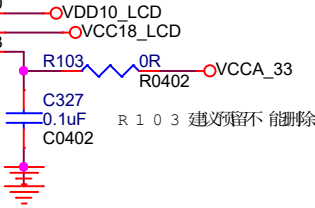
RK3288_Q



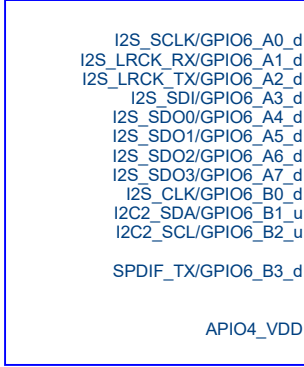
U1A
MCU_RK3288



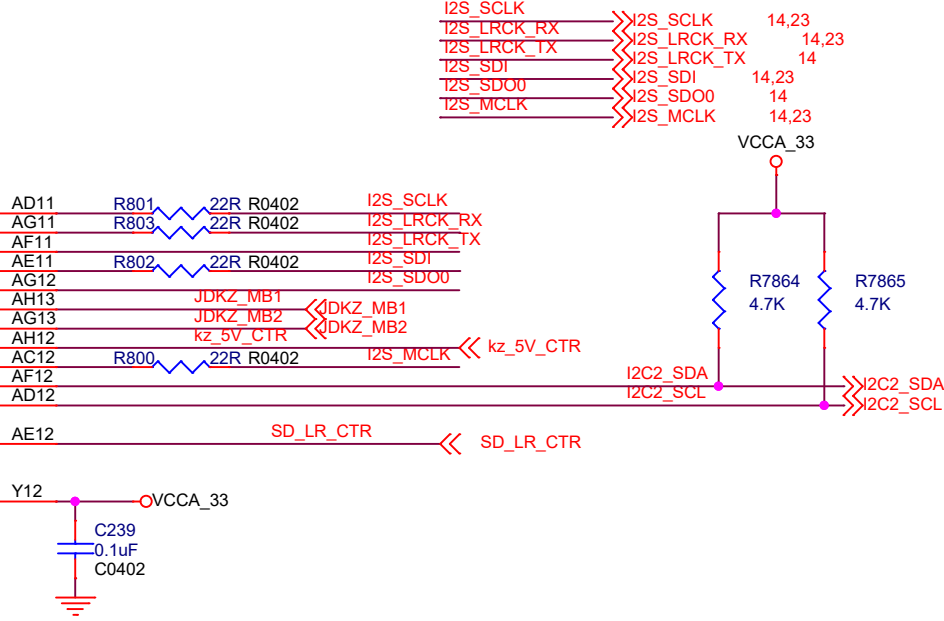
RK3288_A

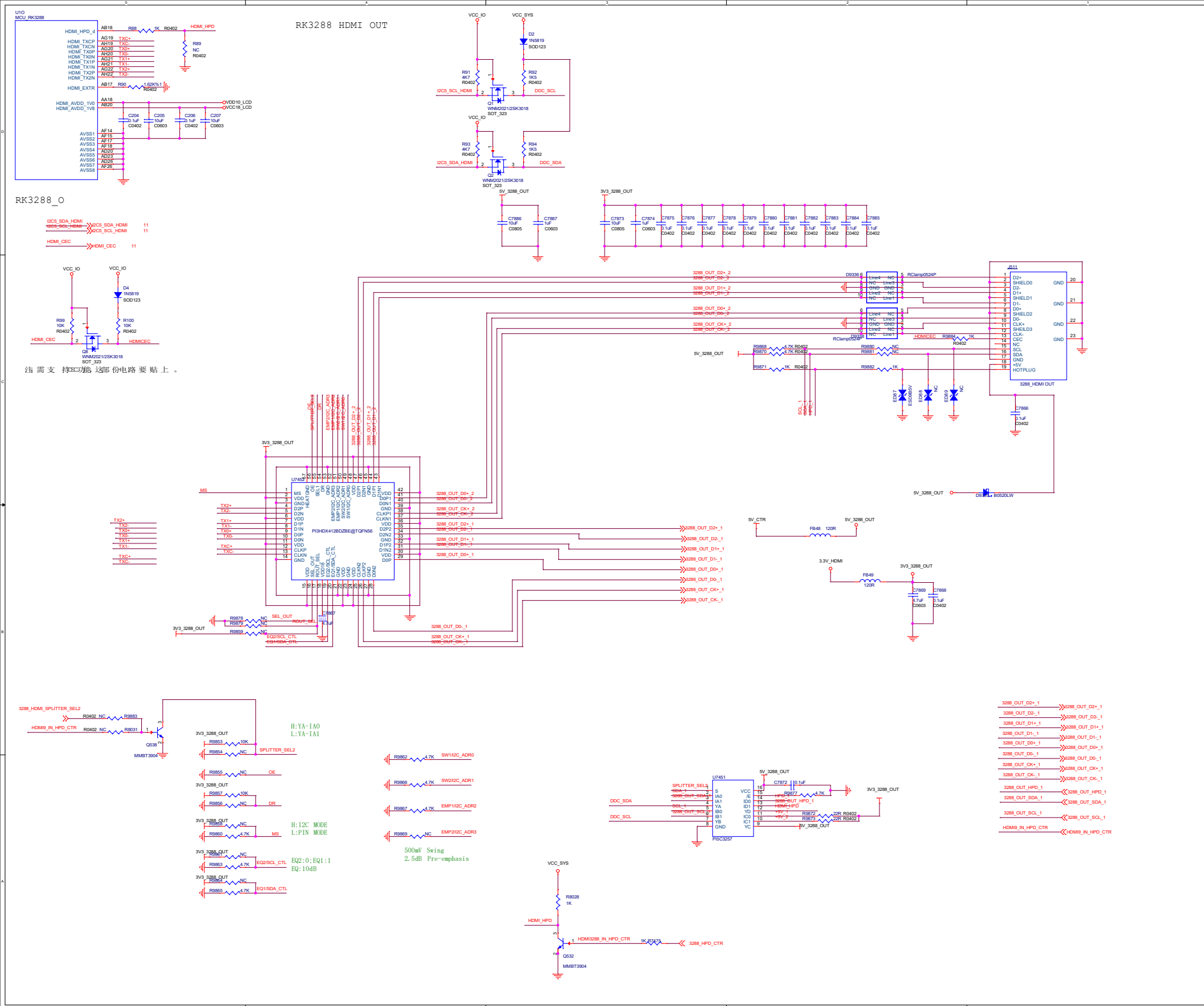


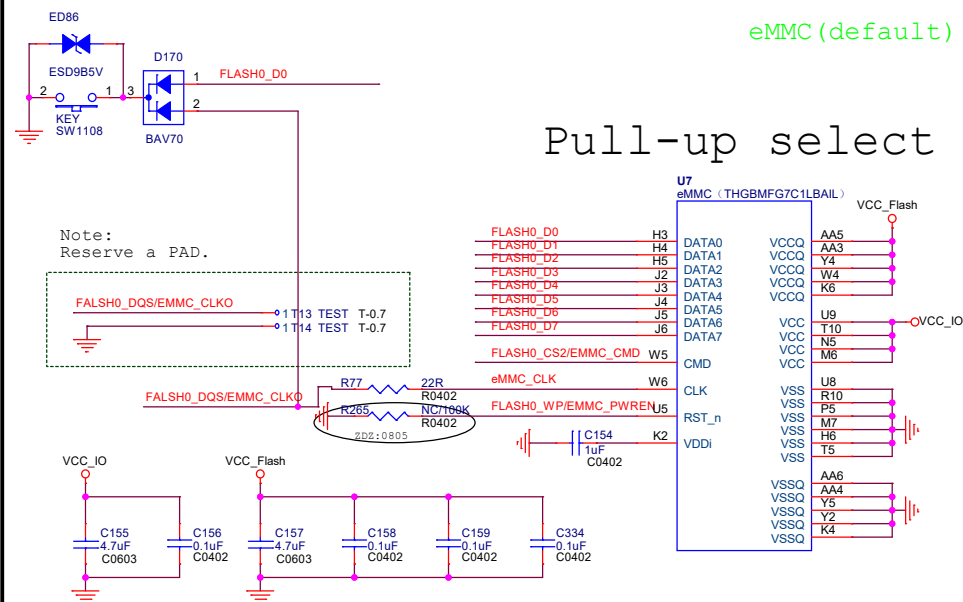
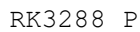
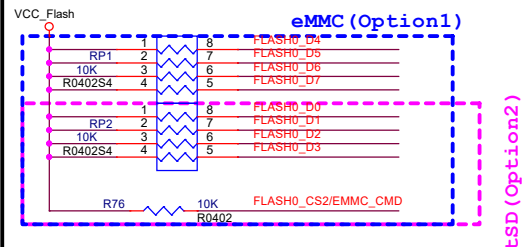
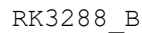
U1I
MCU_RK3288



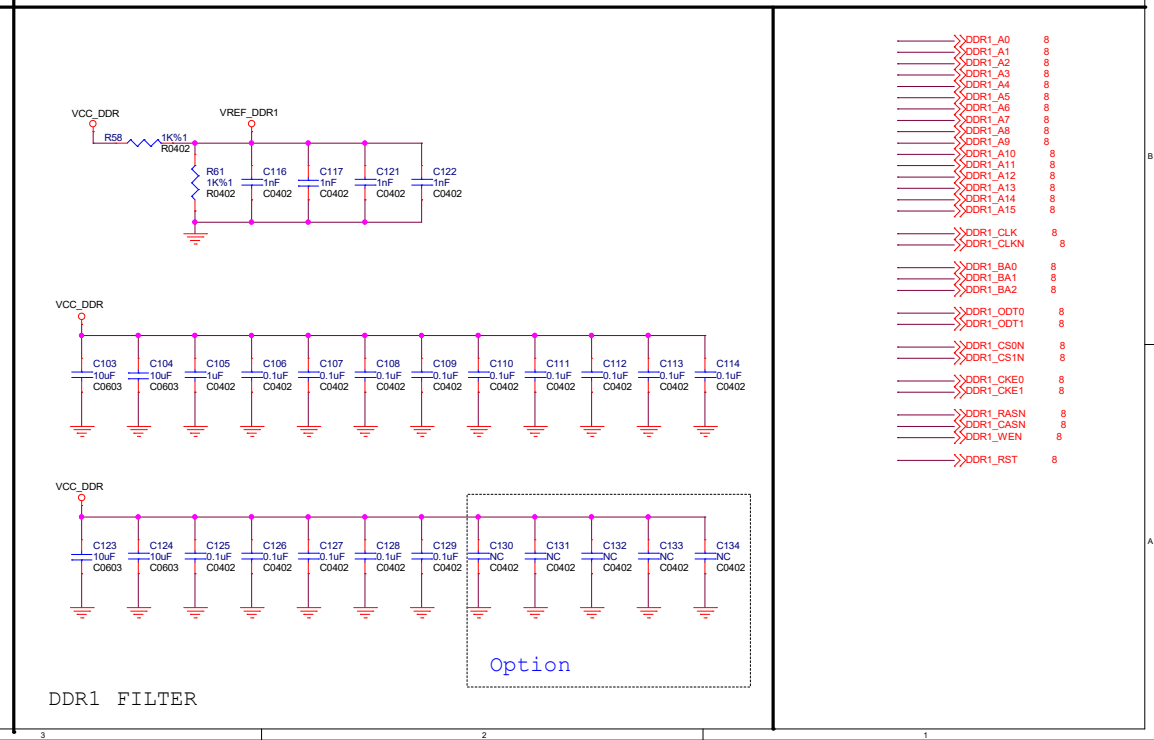
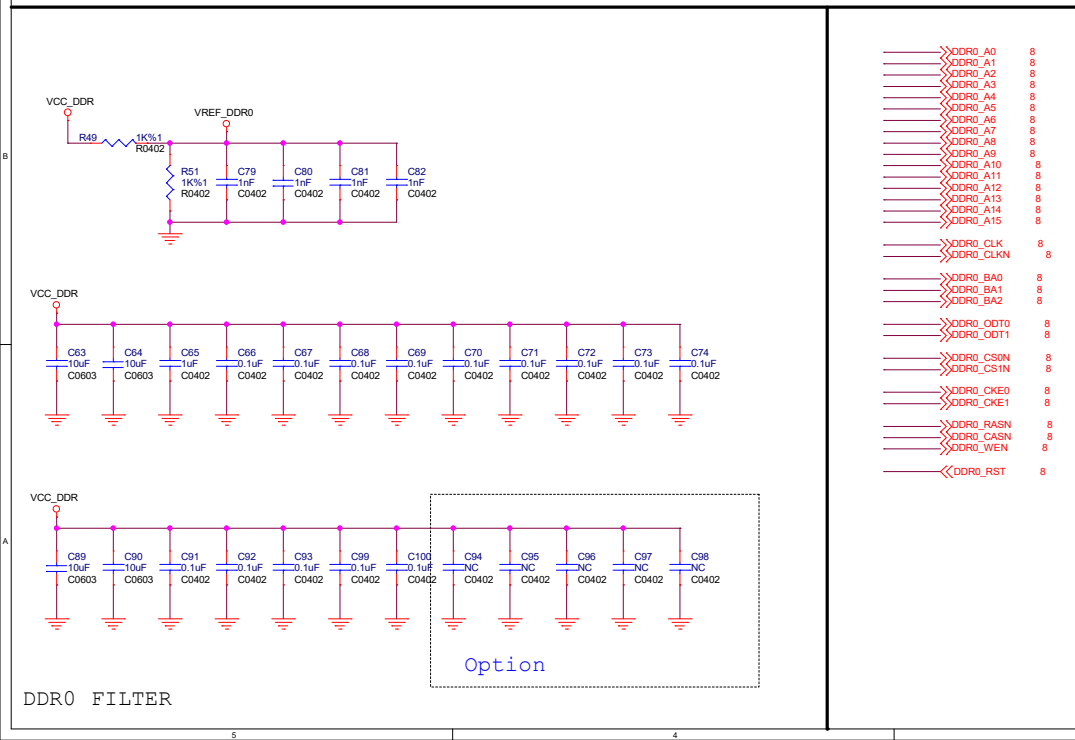
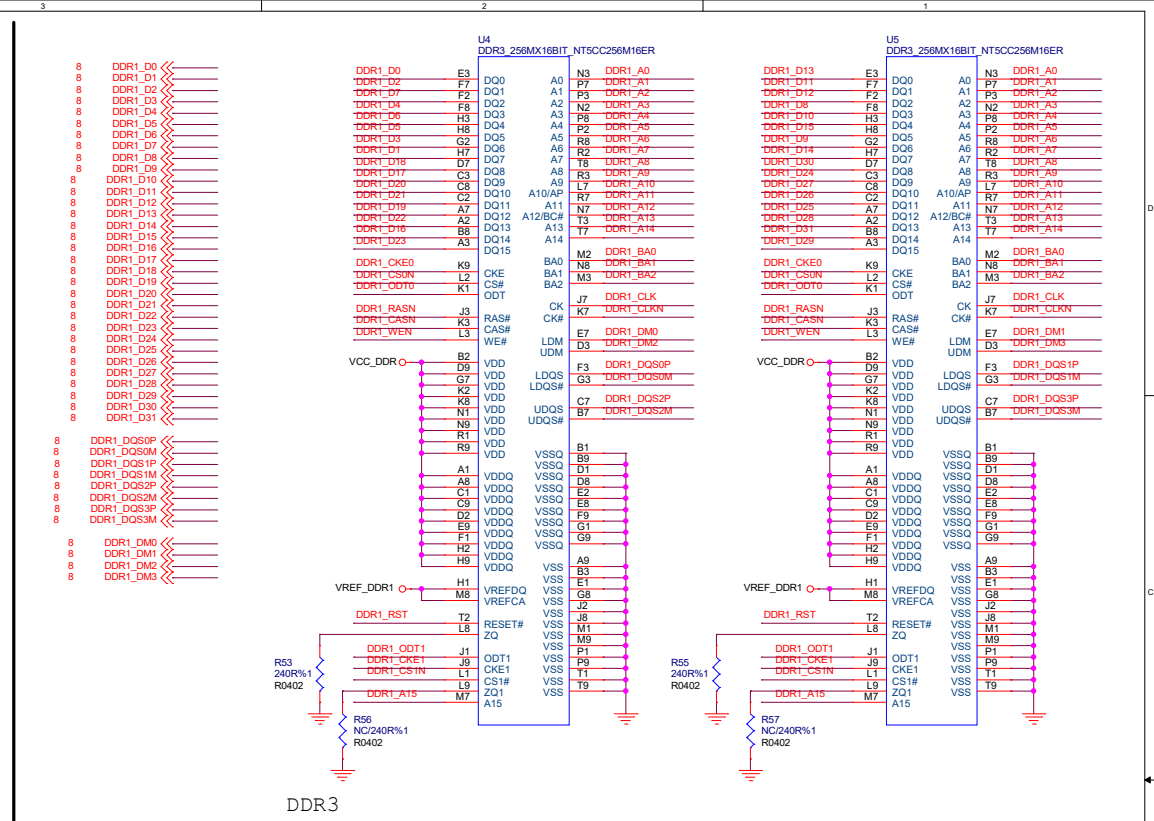
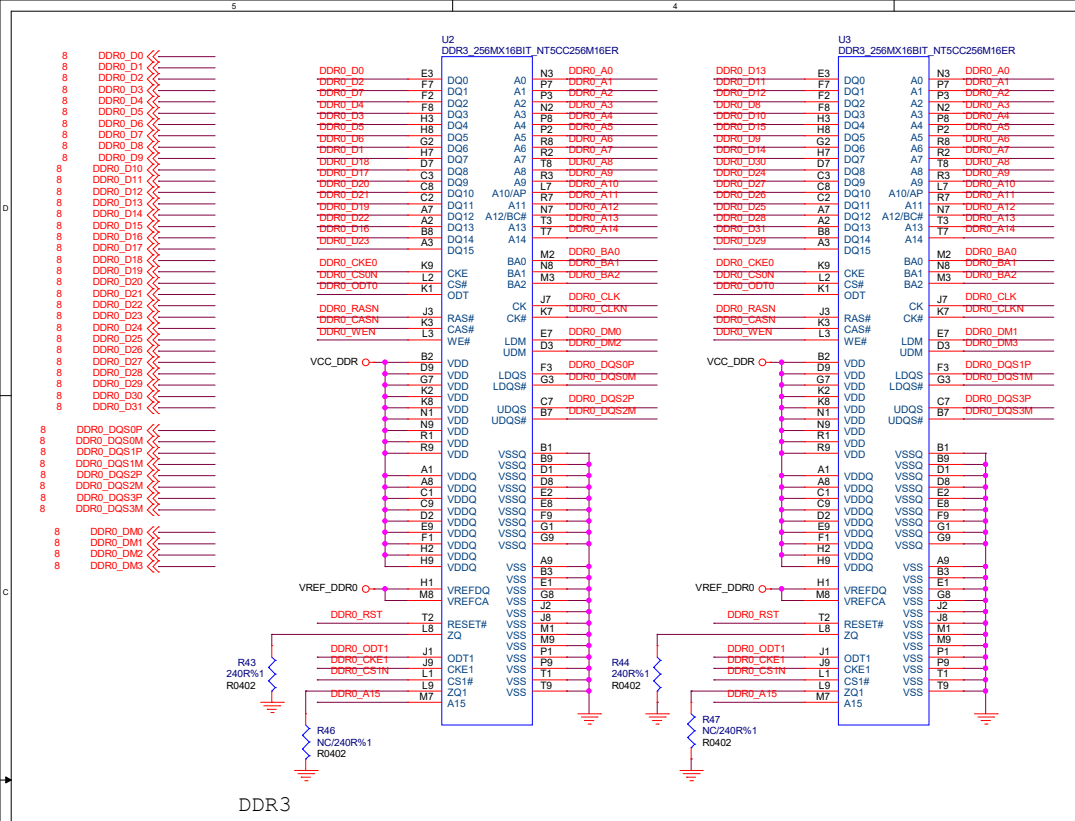
RK3288_I

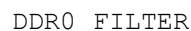
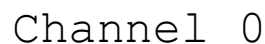




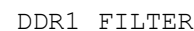


	Flash IO voltage		
eMMC (Default)	1.8V (VCCQ<150mA)	R74:DNP U15:DNP R532:0R	Default
	1.8V (VCCQ>150mA)	R74:DNP U15:PT5108E23E-18 (500mA) R532:DNP	请确认所使用的 eMMC 模块的 VCCQ 峰值电流。
	3.3V	R74:0R U15:DNP R532:DNP	
Nand Flash	1.8V (VCCQ<150mA)	R74:DNP U15:DNP R532:0R	
	1.8V (VCCQ>150mA)	R74:DNP U15:PT5108E23E-18 (500mA) R532:DNP	请确认所使用的 Nand 模块的 VCCQ 峰值电流。
	3.3V	R74:0R U15:DNP R532:DNP	





Note:
These termination resistors must be placed in the middle of trace, and the termination resistor of CLK must be placed in the bifurcation point.

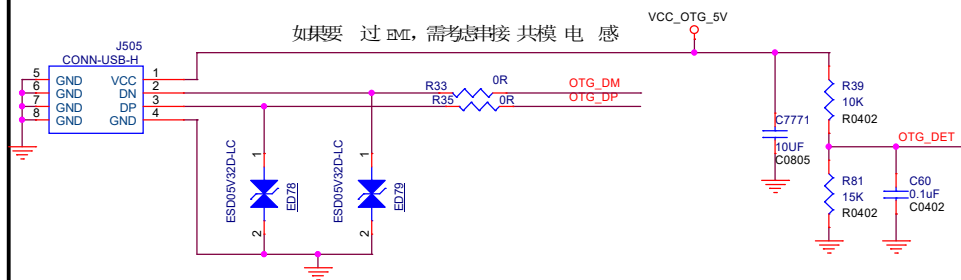
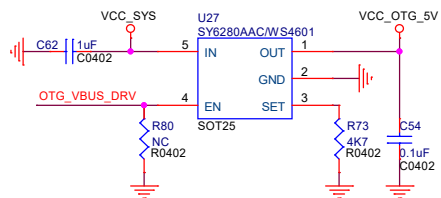


Note:
These termination resistors must be placed in the middle of trace, and the termination resistor of CLK must be placed in the bifurcation point.

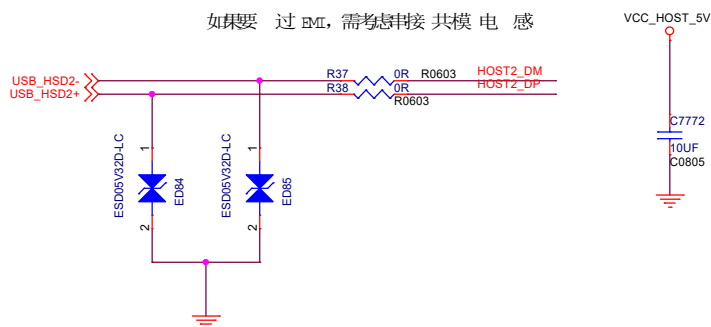
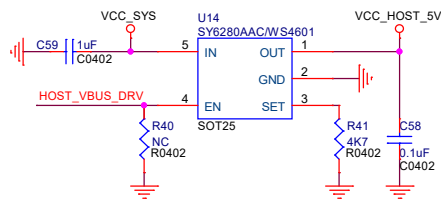
OTG_DM >> OTG_DM 6
OTG_DP >> OTG_DP 6
OTG_DET >> OTG_DET 6

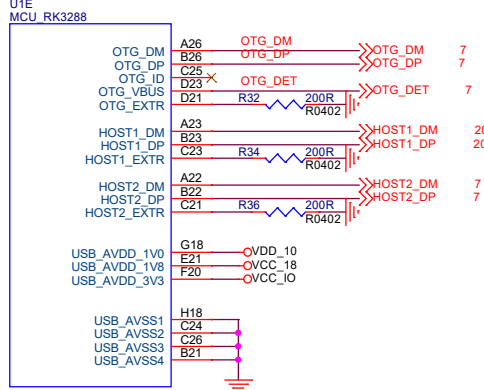
HOST2_DM >> HOST2_DM 6
HOST2_DP >> HOST2_DP 6

HOST_VBUS_DRV >> HOST_VBUS_DRV 11
OTG_VBUS_DRV >> OTG_VBUS_DRV 11

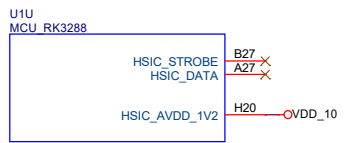
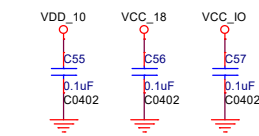


注默认 HOST接口 (烧写 固侖 这个接口)
如果要 当 OTG电路 需更改。

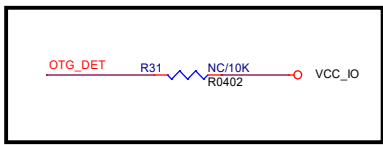
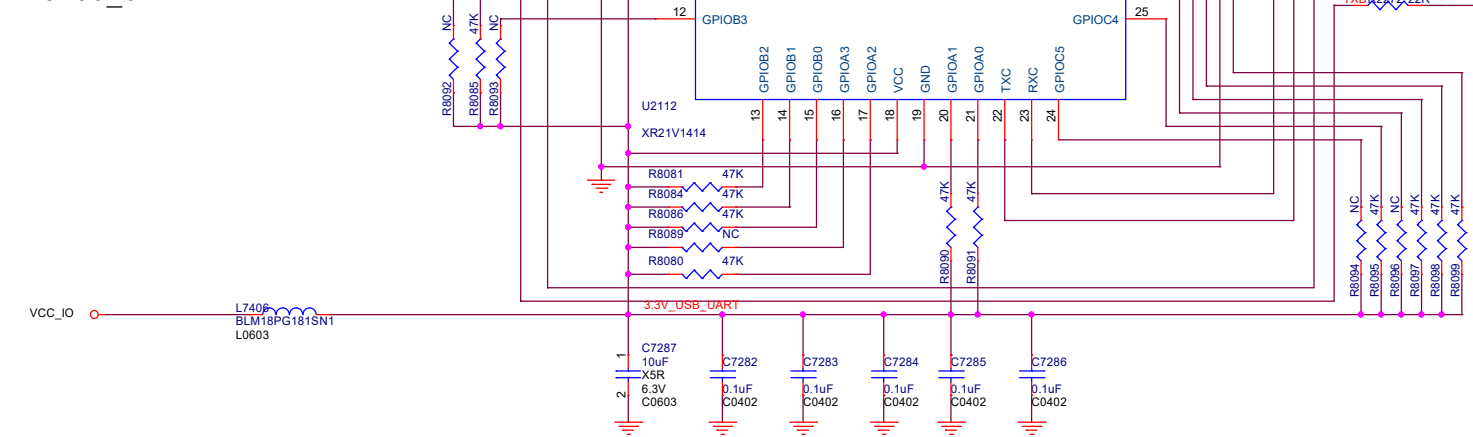




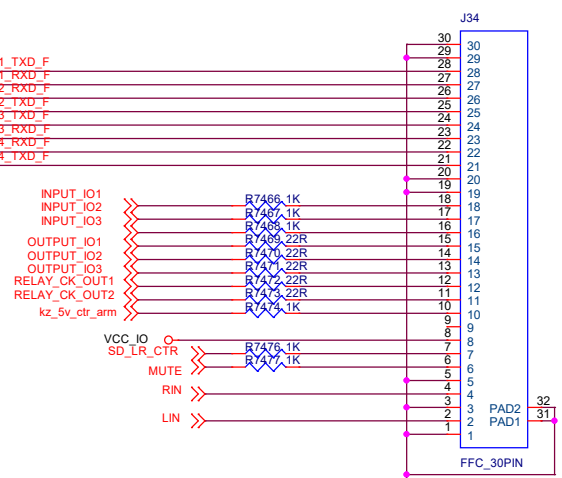
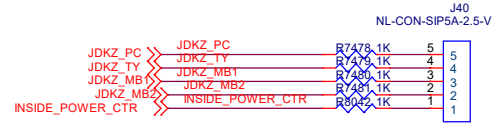
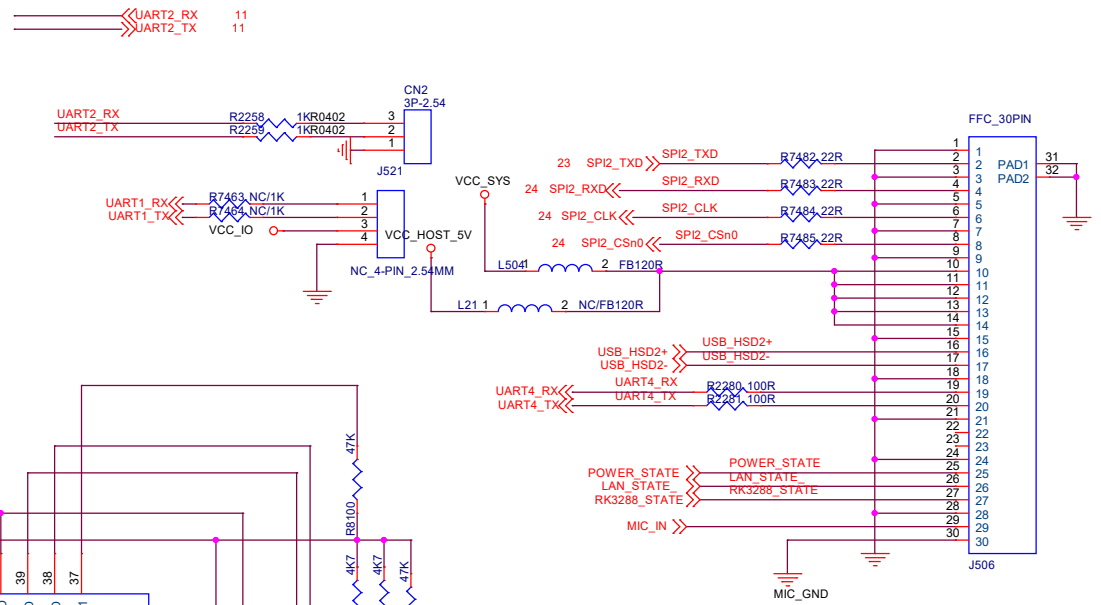
RK3288_E

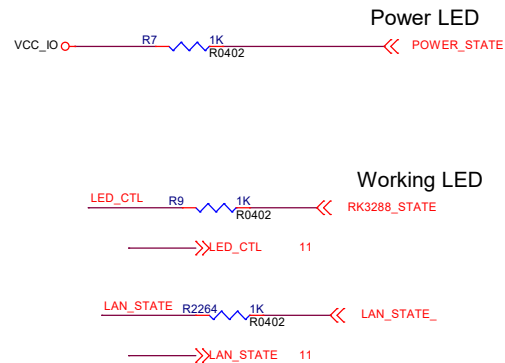


RK3288_U

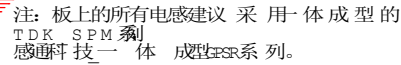


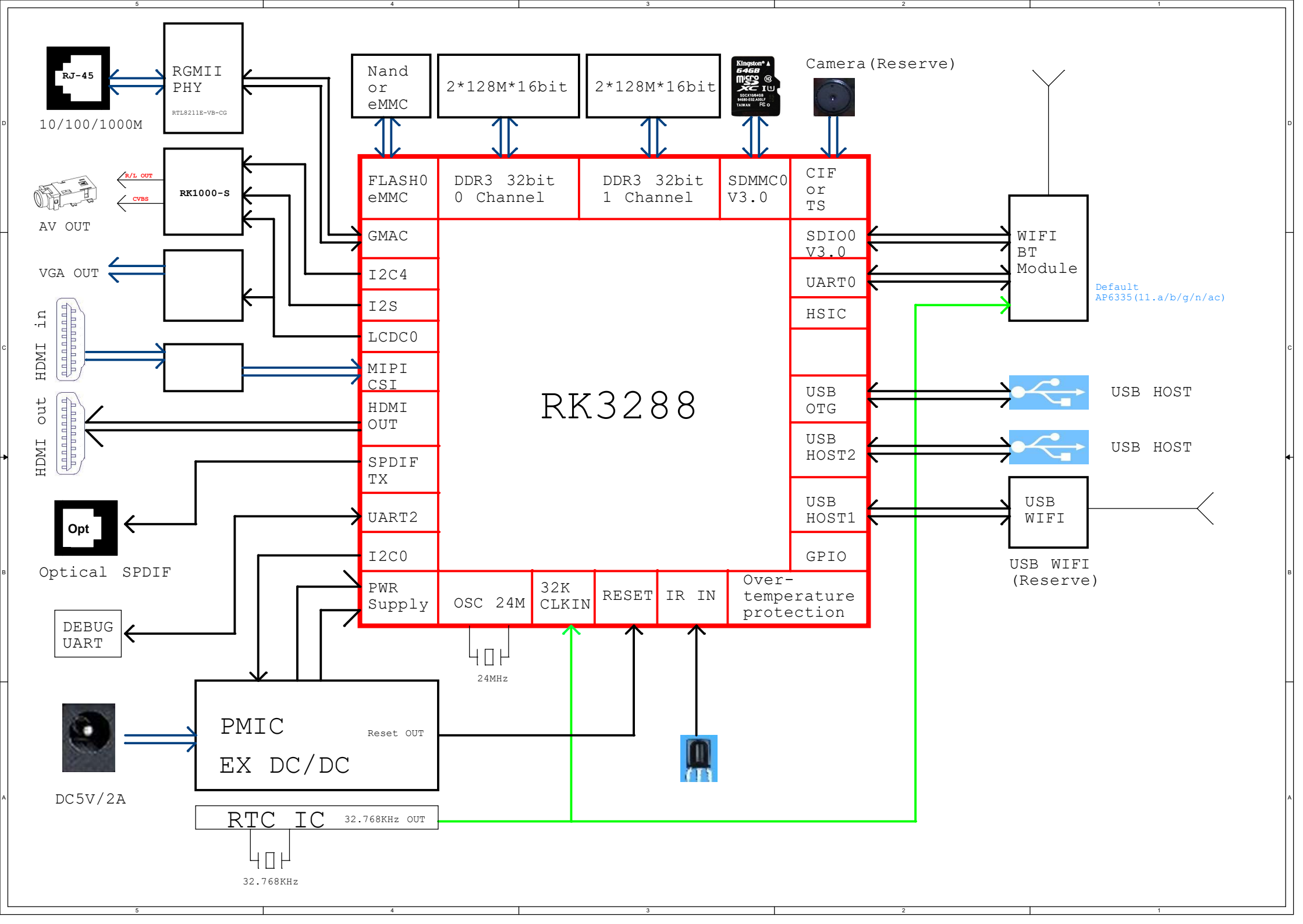
HOST1 仅支持 USB2.0 模式时 需注意!



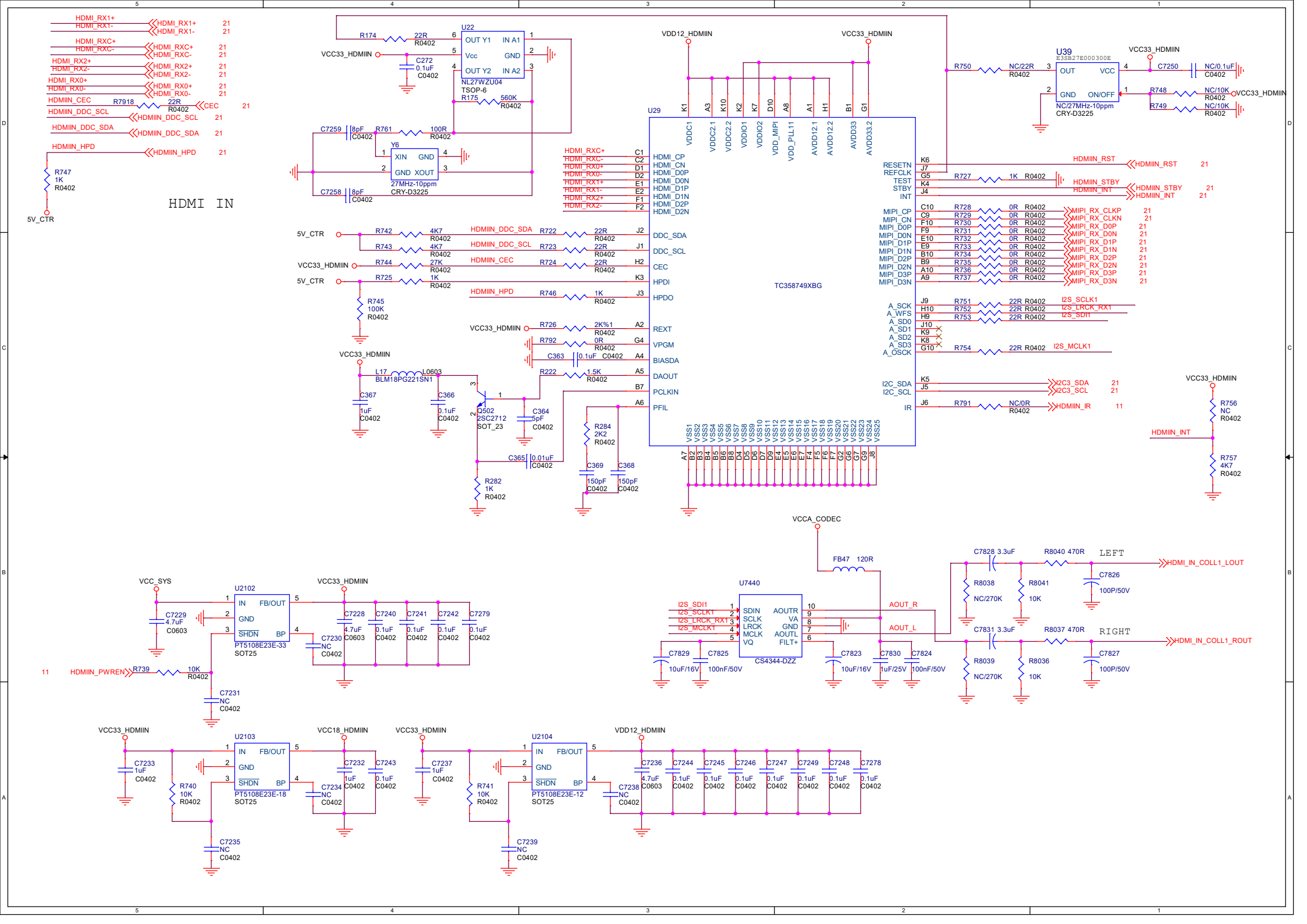


DDR Type	OutPut voltage	R2320	R2323
DDR3	1.5V	100K	200K
DDR3L	1.358V	43K	120K
LPDDR2/3	1.25V	49.9K	200K

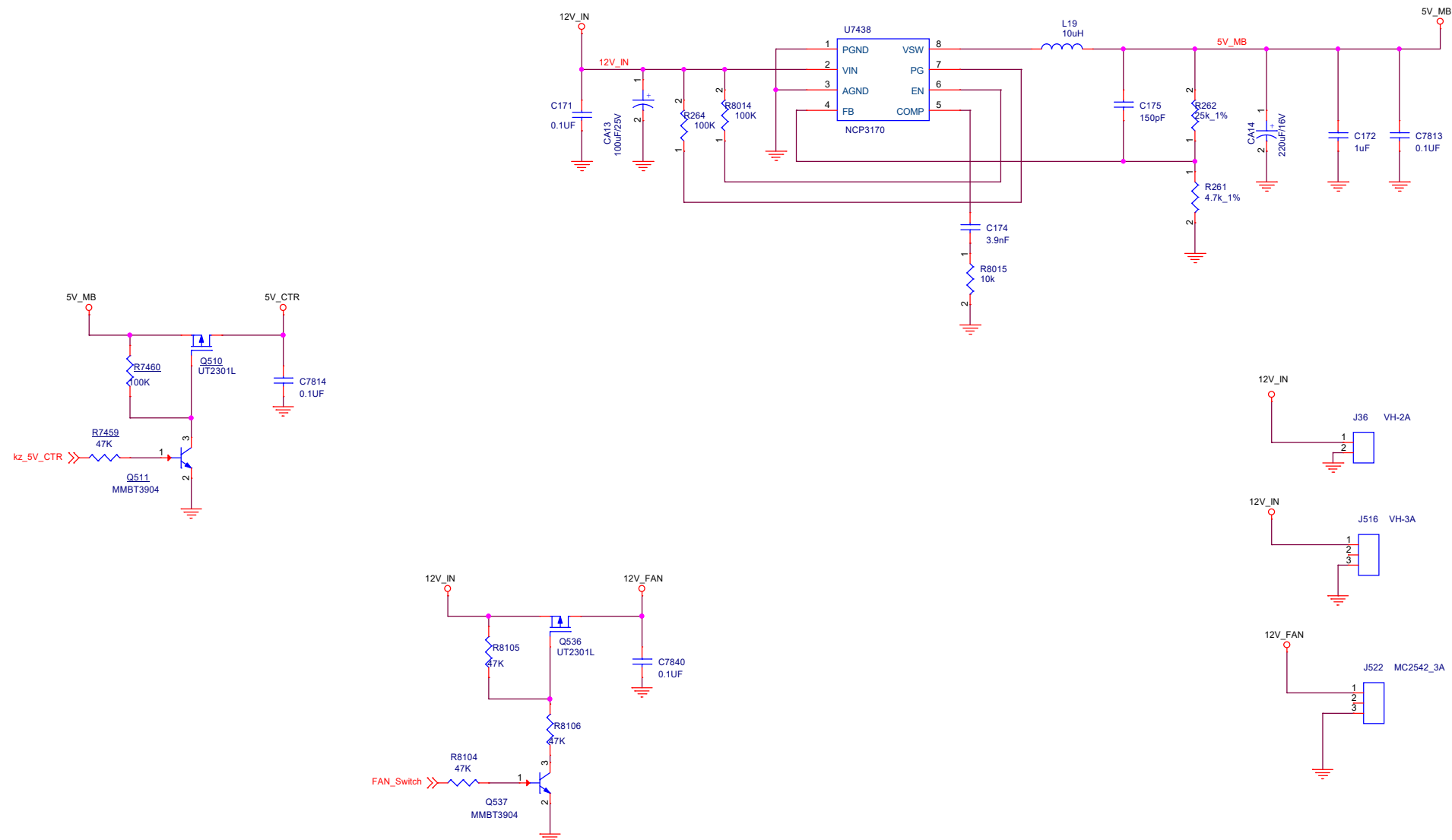




[illegible]



Power Supply

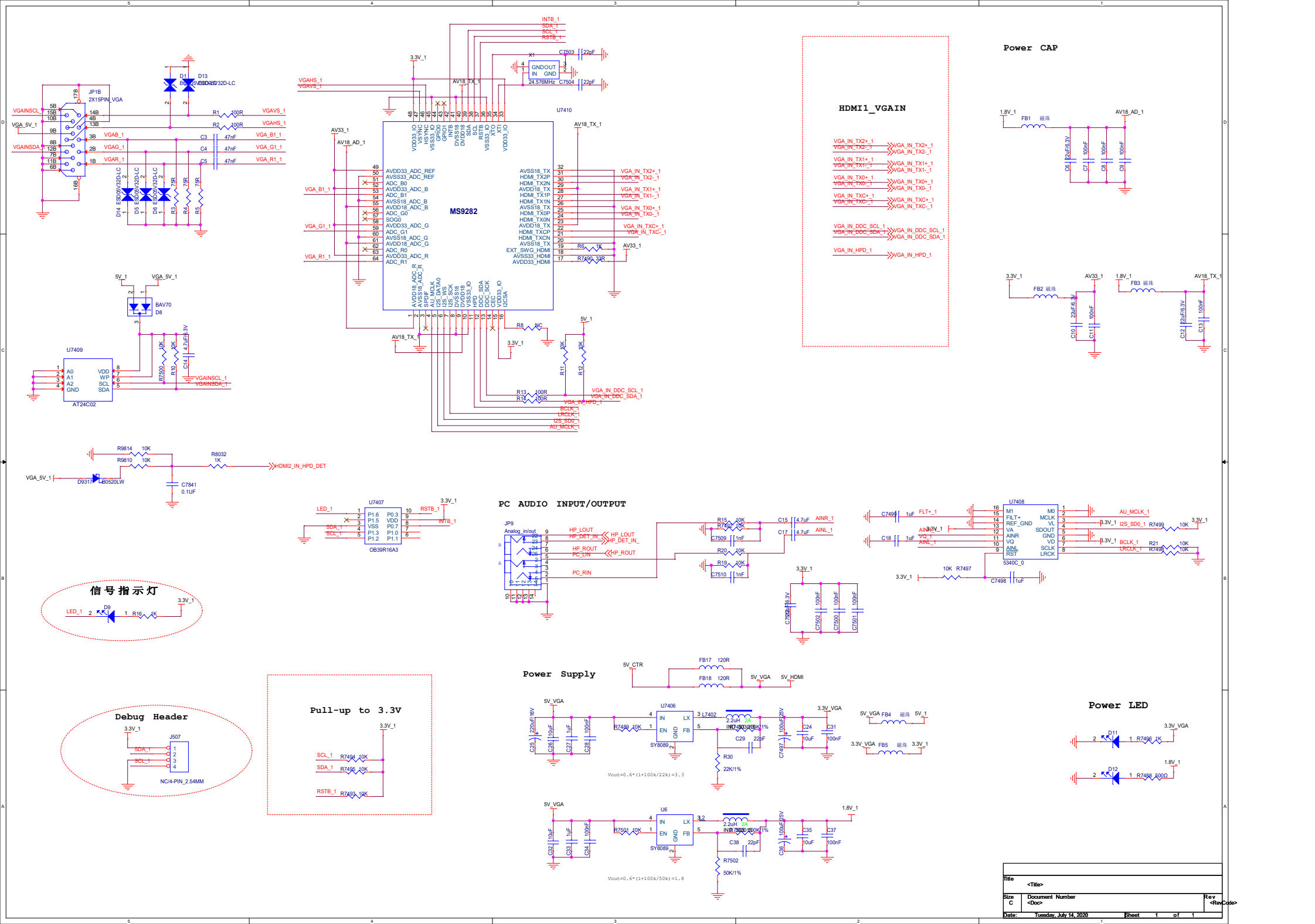


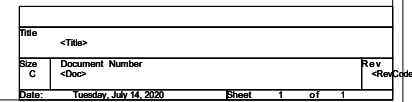
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- 09.RAM-DDR3-4X16bit
- 10.Nand FLASH/eMMC/TF Card
- 11.RK3288 GPIO/POWER
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- 19.AP6XXX-WIFI/BT
- 20.USB WIFI-(option)
- 21.MIPI Interface
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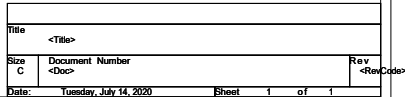
6 LAYERS PCB STACK

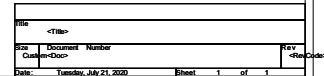
TOP(Signal1)		Cu,thickness:0.7mil,Plating to 1oz
	FR4,thickness:3.8mil,Dielectric Constant:4.3	
GND1		Cu,thickness:1.5mil, 1oz
	FR4,thickness:8mil,Dielectric Constant:4.3	
POWER		Cu,thickness:1.5mil, 1oz
FR4,thickness:adjust thickness according to the thickness of board ,Dielectric Constant:4.3		
Signal2		Cu,thickness:1.5mil, 1oz
	FR4,thickness:8mil,Dielectric Constant:4.3	
GND2		Cu,thickness:1.5mil, 1oz
	FR4,thickness:3.8mil,Dielectric Constant:4.3	
BOTTOM(Signal3)		Cu,thickness:0.7mil,Plating to 1oz

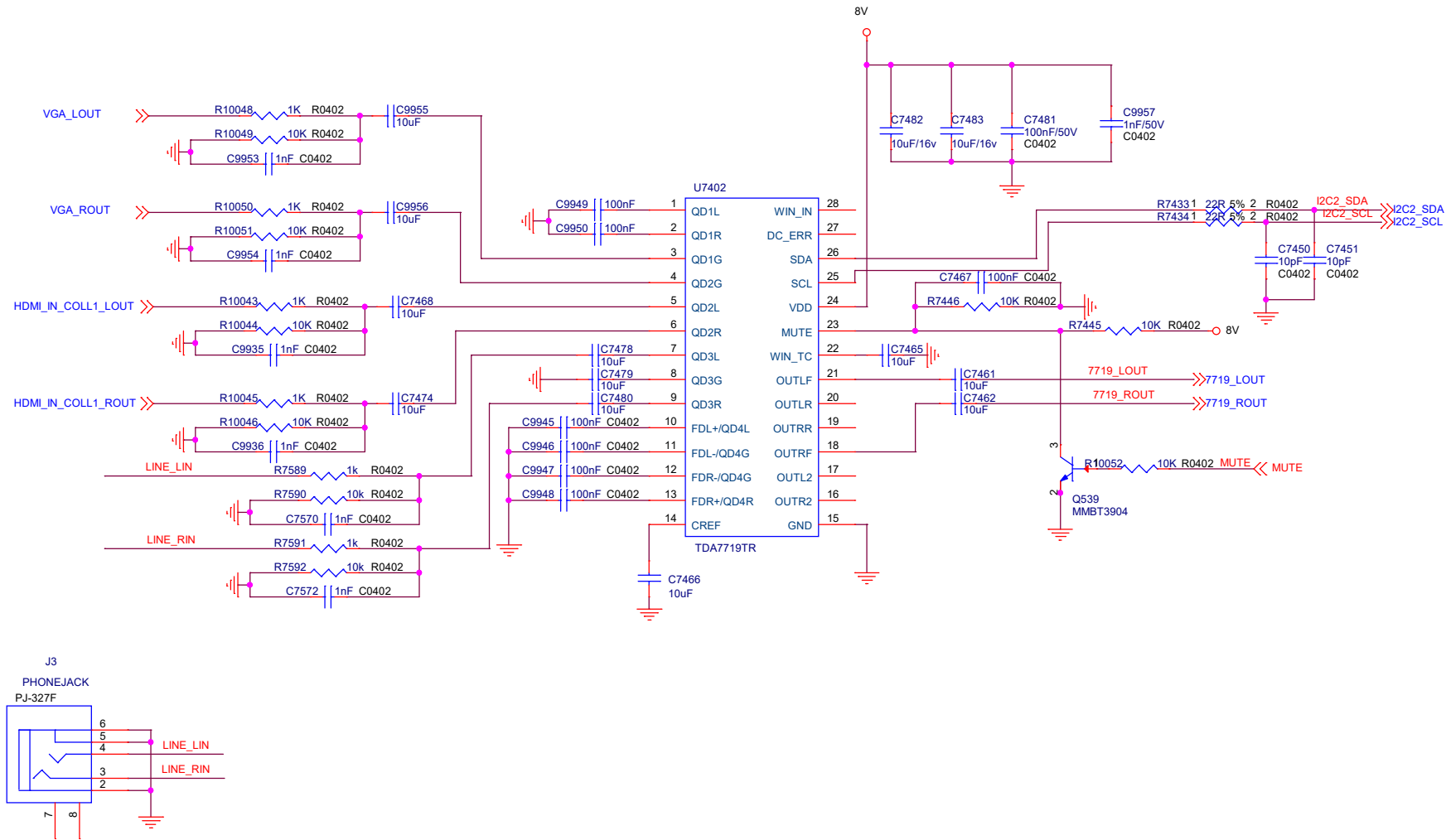




WP_SET	1	WP_SET = 0 (Default, Set Bit) = INT Flag WP_SET = 1, (OC, WPACT is programmable by Bit)
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B	<Doc>		
Date:	Tuesday, July 14, 2020	Sheet	1 of 1

