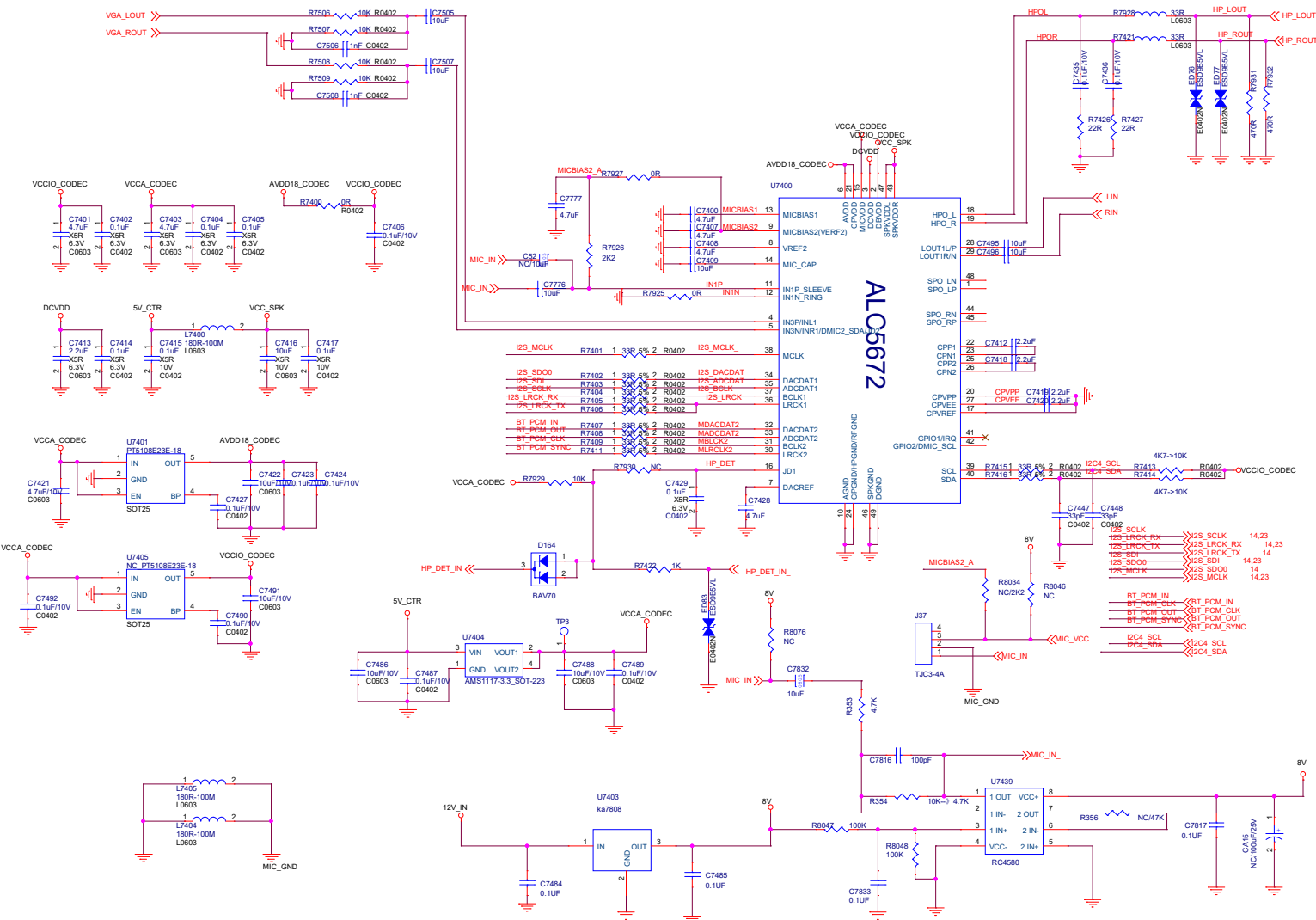
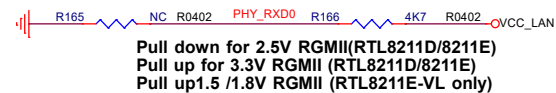
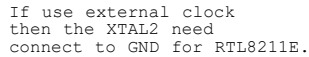
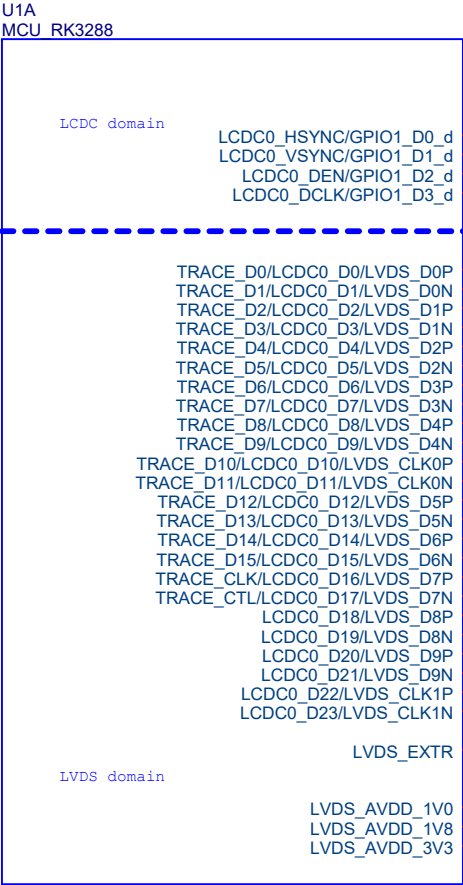


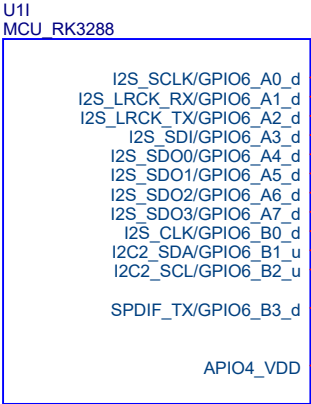
CODEC



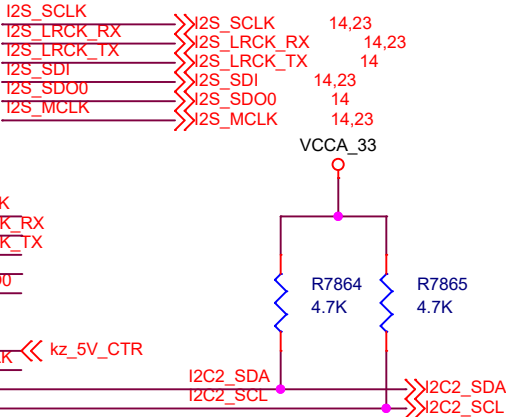


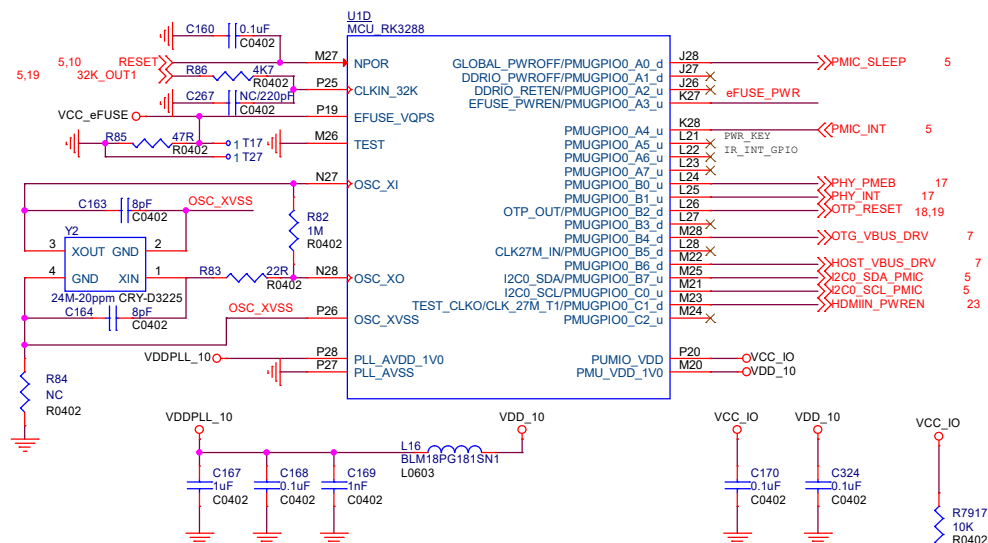


RK3288_A

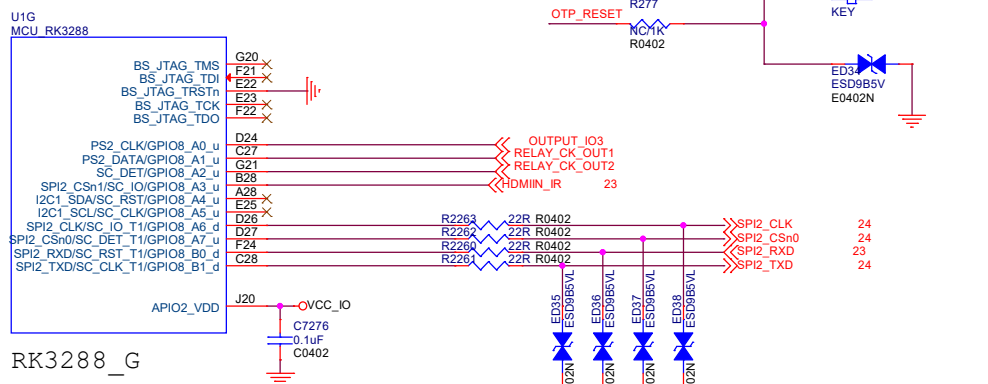


RK3288_I

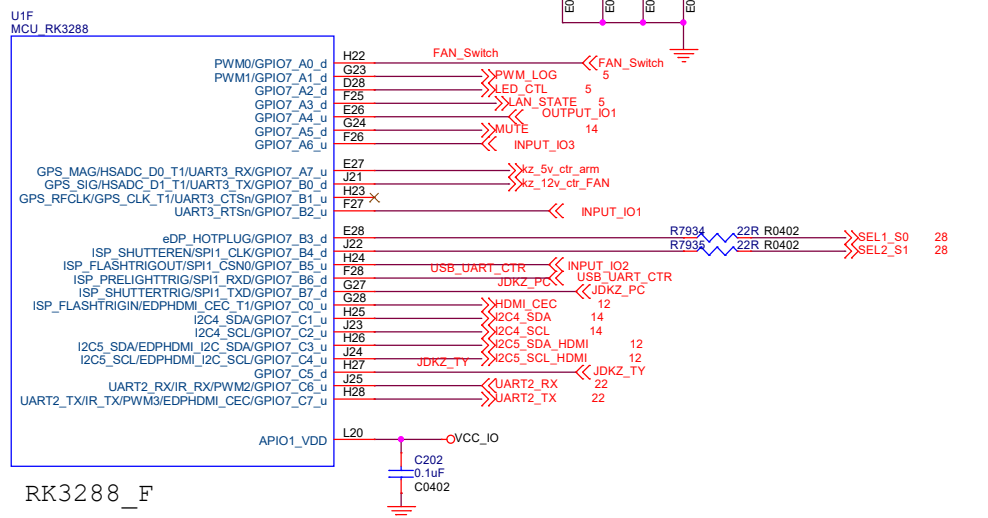




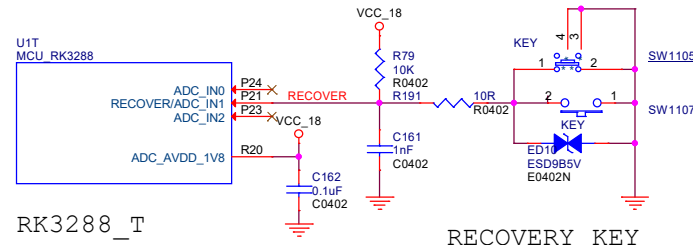
RK3288 D



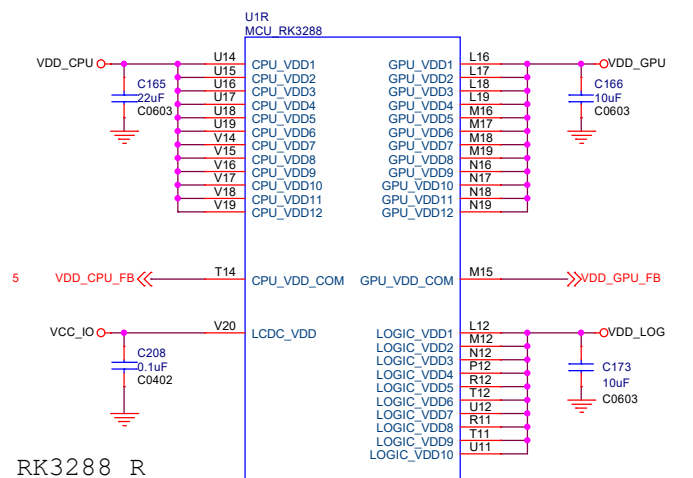
RK3288 G



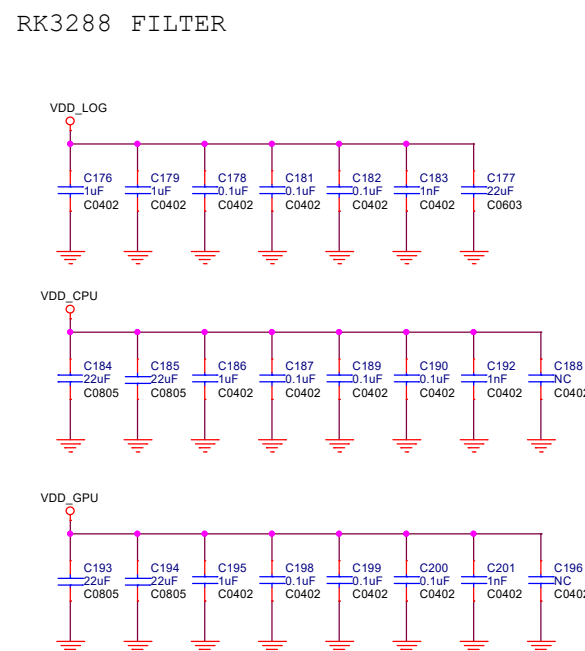
RK3288 F



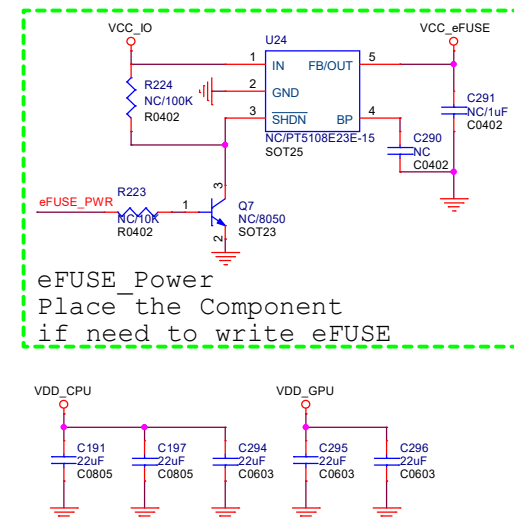
RK3288 T



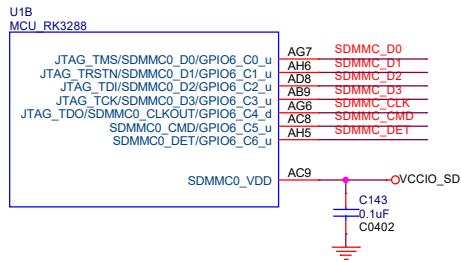
RK3288 R



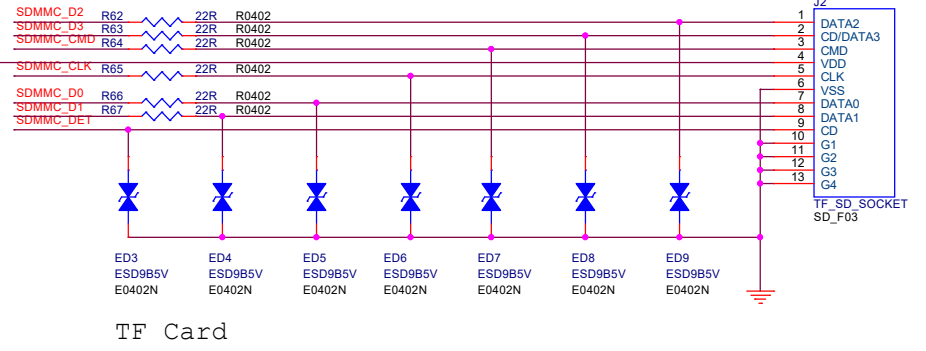
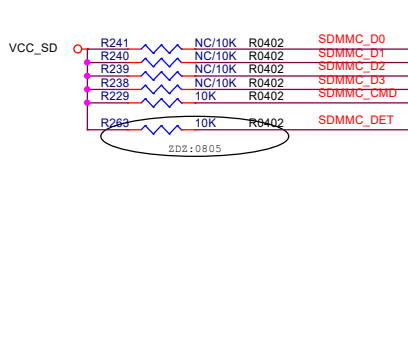
RK3288 FILTER



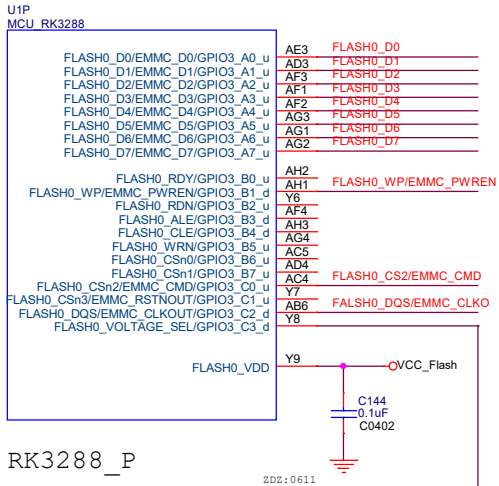
RK3288 M



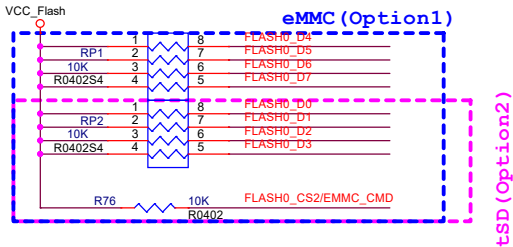
RK3288_B



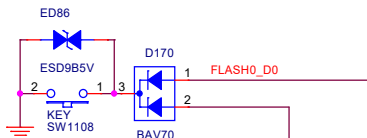
TF Card



RK3288_P



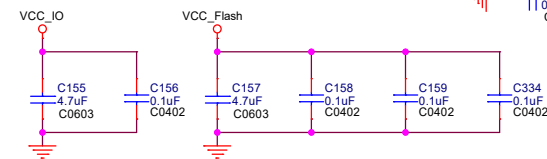
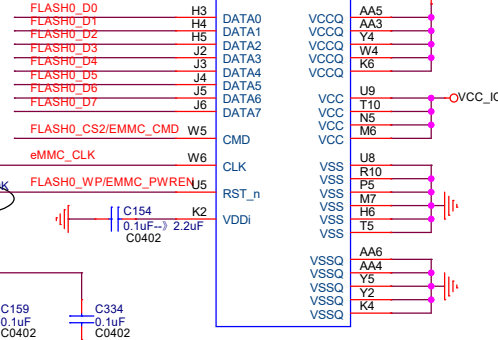
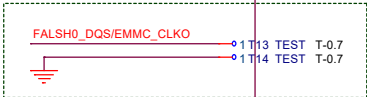
Option2



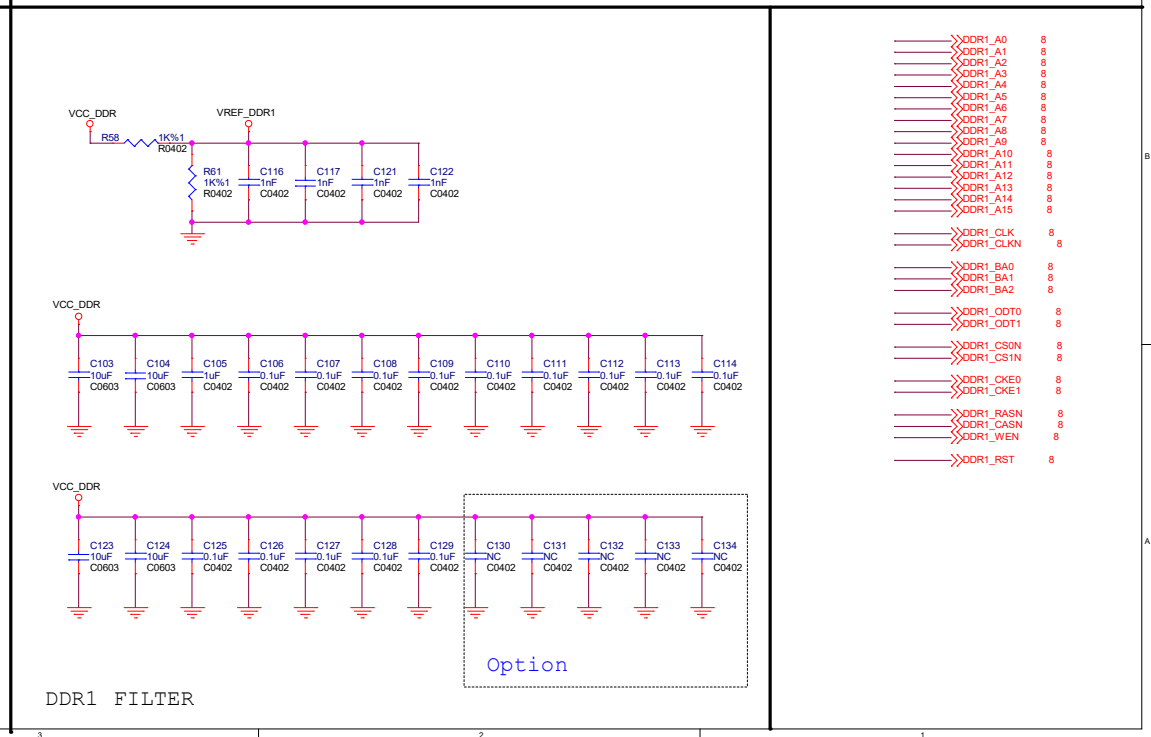
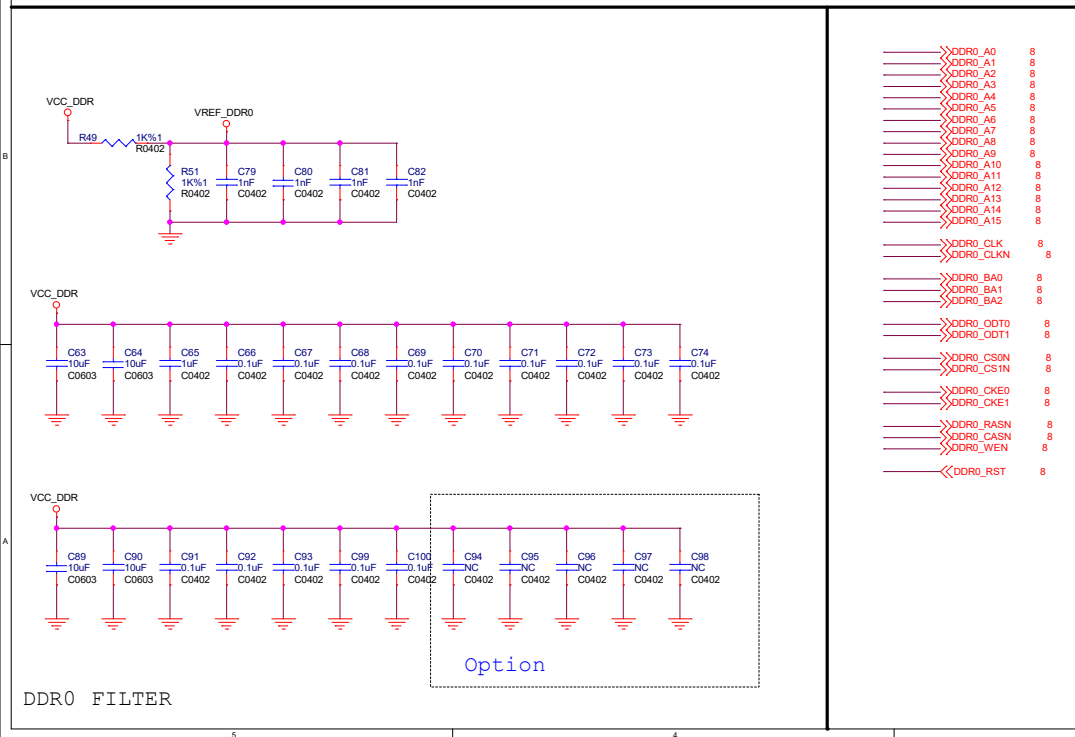
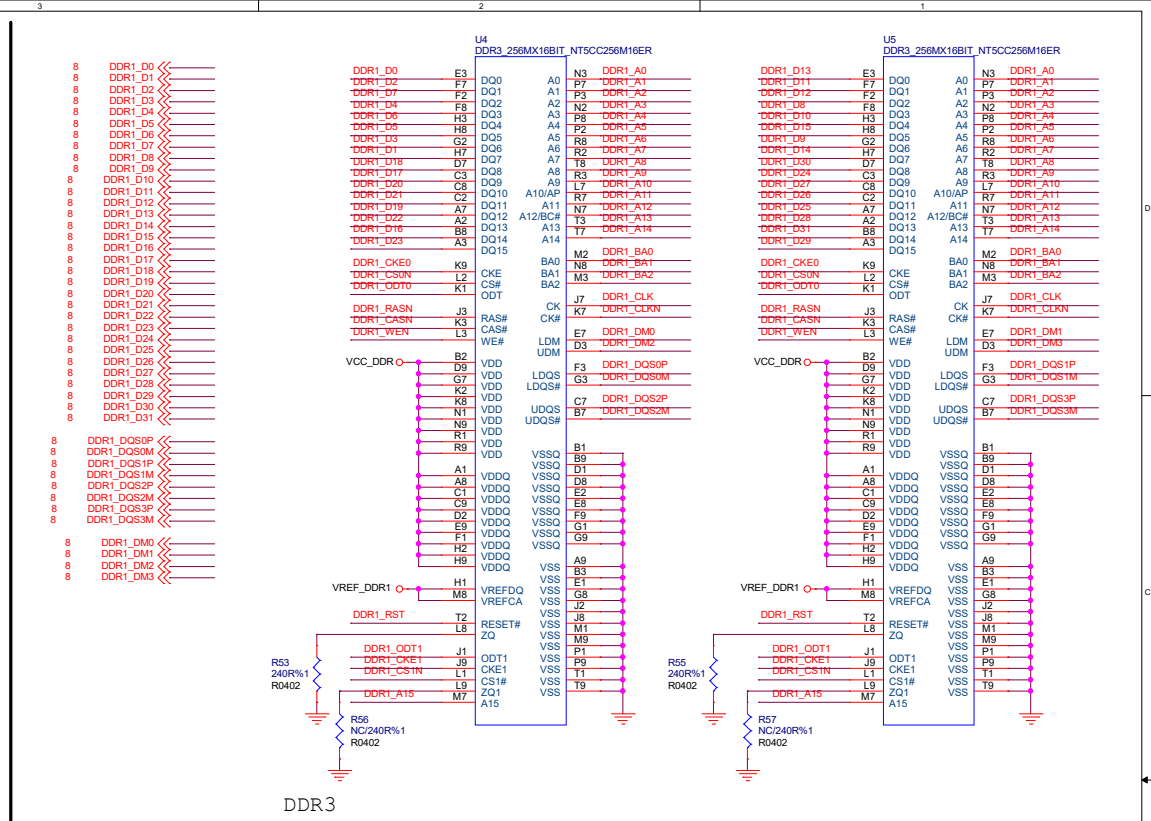
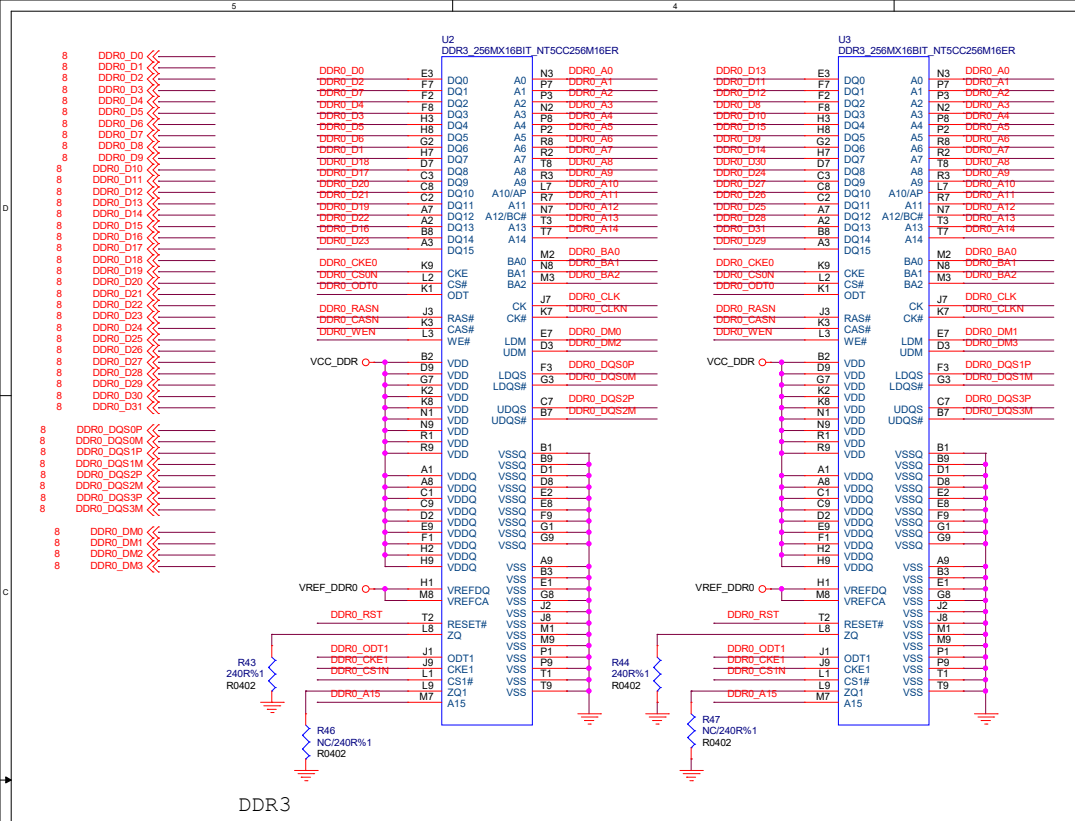
eMMC (default)

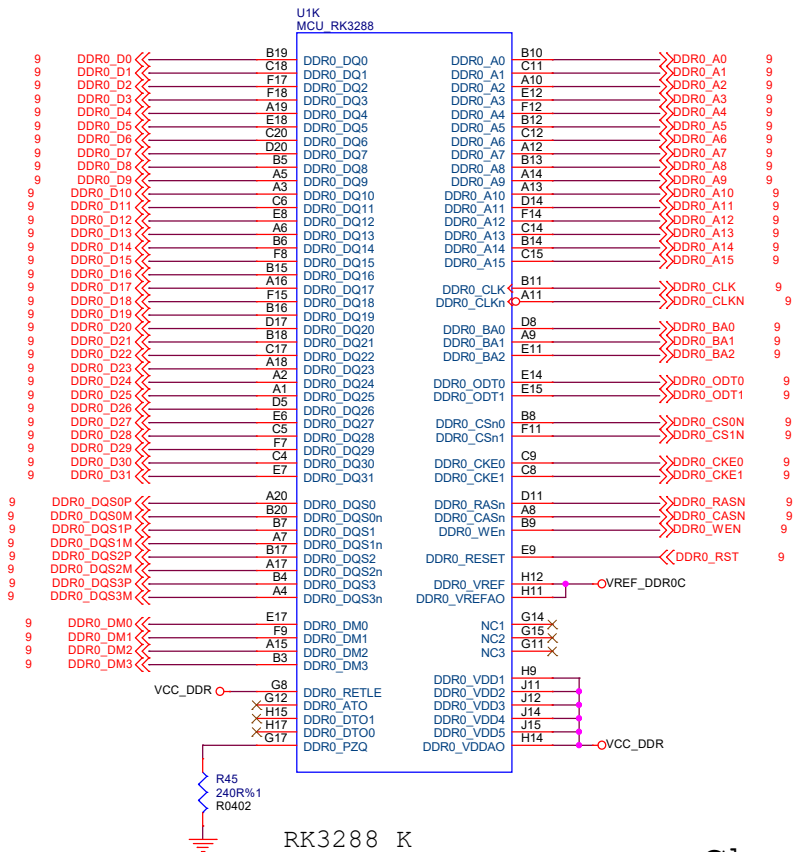
Pull-up select

Note:
Reserve a PAD.



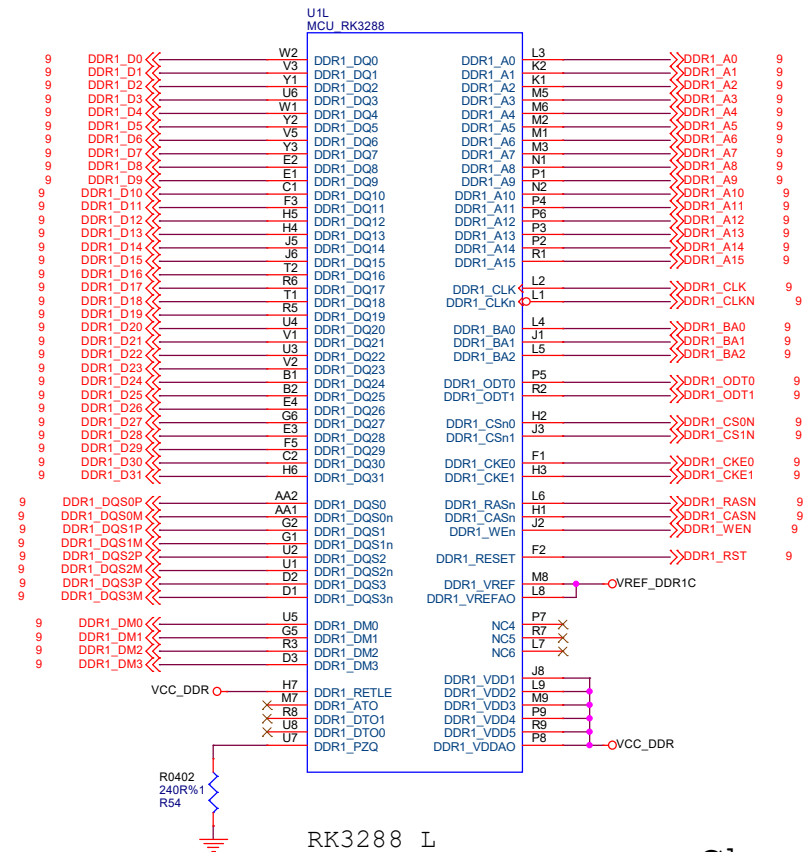
	Flash IO voltage		
eMMC (Default)	1.8V (VCCQ<150mA)	R74:DNP U15:DNP R532:0R	Default
	1.8V (VCCQ>150mA)	R74:DNP U15:PT5108E23E-18 (500mA) R532:DNP	请确认所使用的 eMMC 颗粒的 VCCQ 峰值电流。
	3.3V	R74:0R U15:DNP R532:DNP	
Nand Flash	1.8V (VCCQ<150mA)	R74:DNP U15:DNP R532:0R	
	1.8V (VCCQ>150mA)	R74:DNP U15:PT5108E23E-18 (500mA) R532:DNP	请确认所使用的 Nand 颗粒的 VCCQ 峰值电流。
	3.3V	R74:0R U15:DNP R532:DNP	





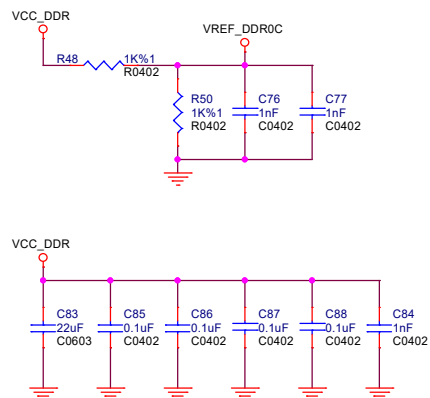
RK3288_K

Channel 0



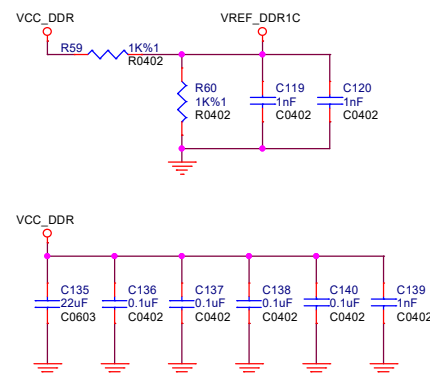
RK3288_L

Channel 1



DDR0 FILTER

Note:
These termination resistors must be placed in the middle of trace, and the termination resistor of CLK must be placed in the bifurcation point.



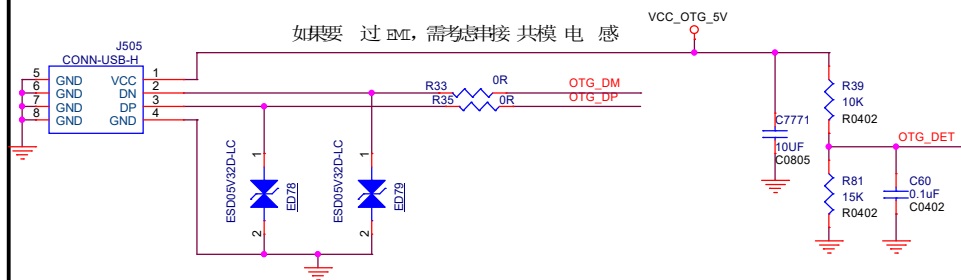
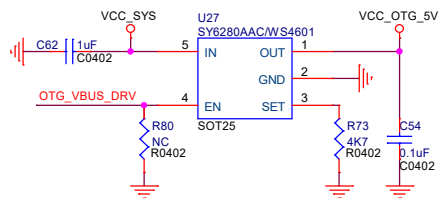
DDR1 FILTER

Note:
These termination resistors must be placed in the middle of trace, and the termination resistor of CLK must be placed in the bifurcation point.

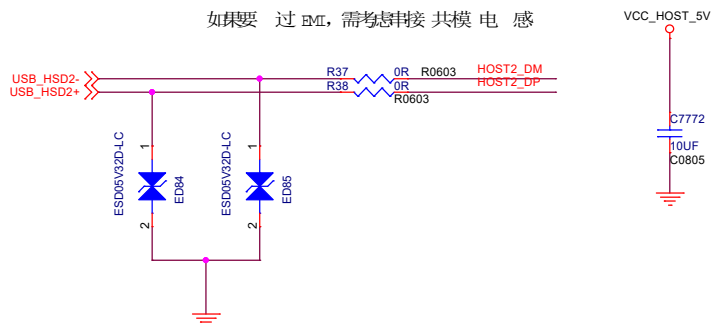
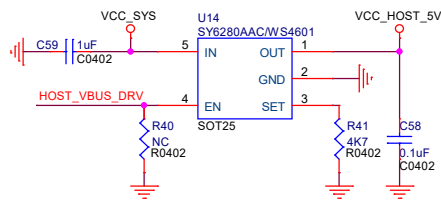
OTG_DM >> OTG_DM 6
OTG_DP >> OTG_DP 6
OTG_DET >> OTG_DET 6

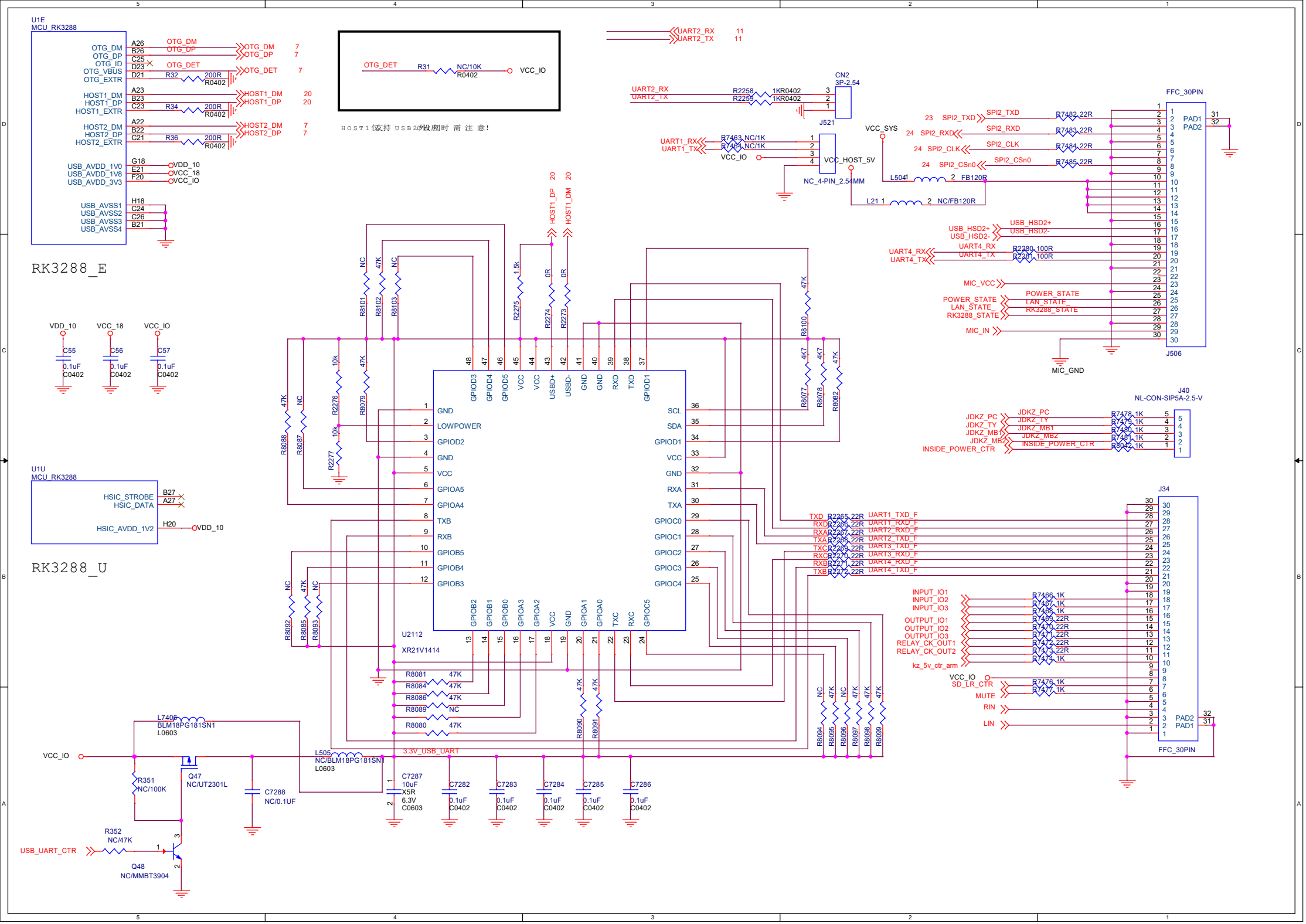
HOST2_DM >> HOST2_DM 6
HOST2_DP >> HOST2_DP 6

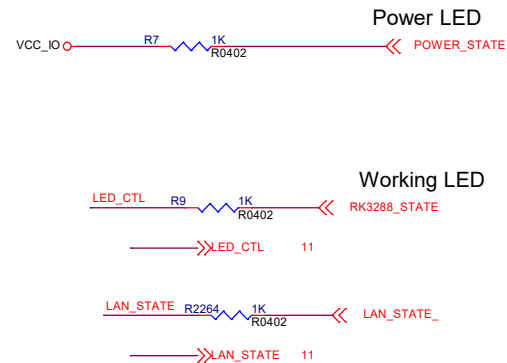
HOST_VBUS_DRV >> HOST_VBUS_DRV 11
OTG_VBUS_DRV >> OTG_VBUS_DRV 11



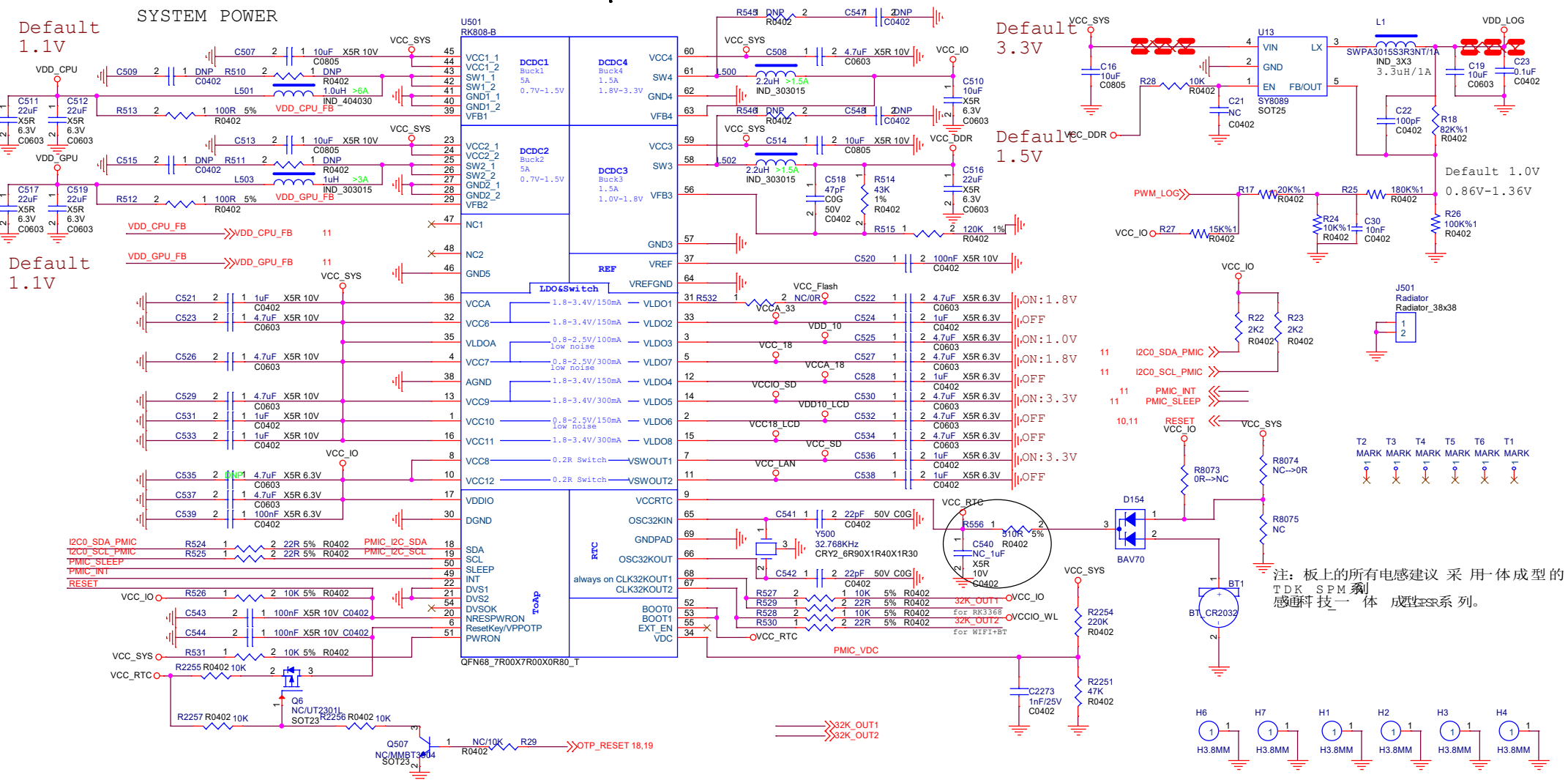
注默认 HOST 接口 (烧写 固侖 这个 接口)
如果要 当 OTG 电路 需更改。

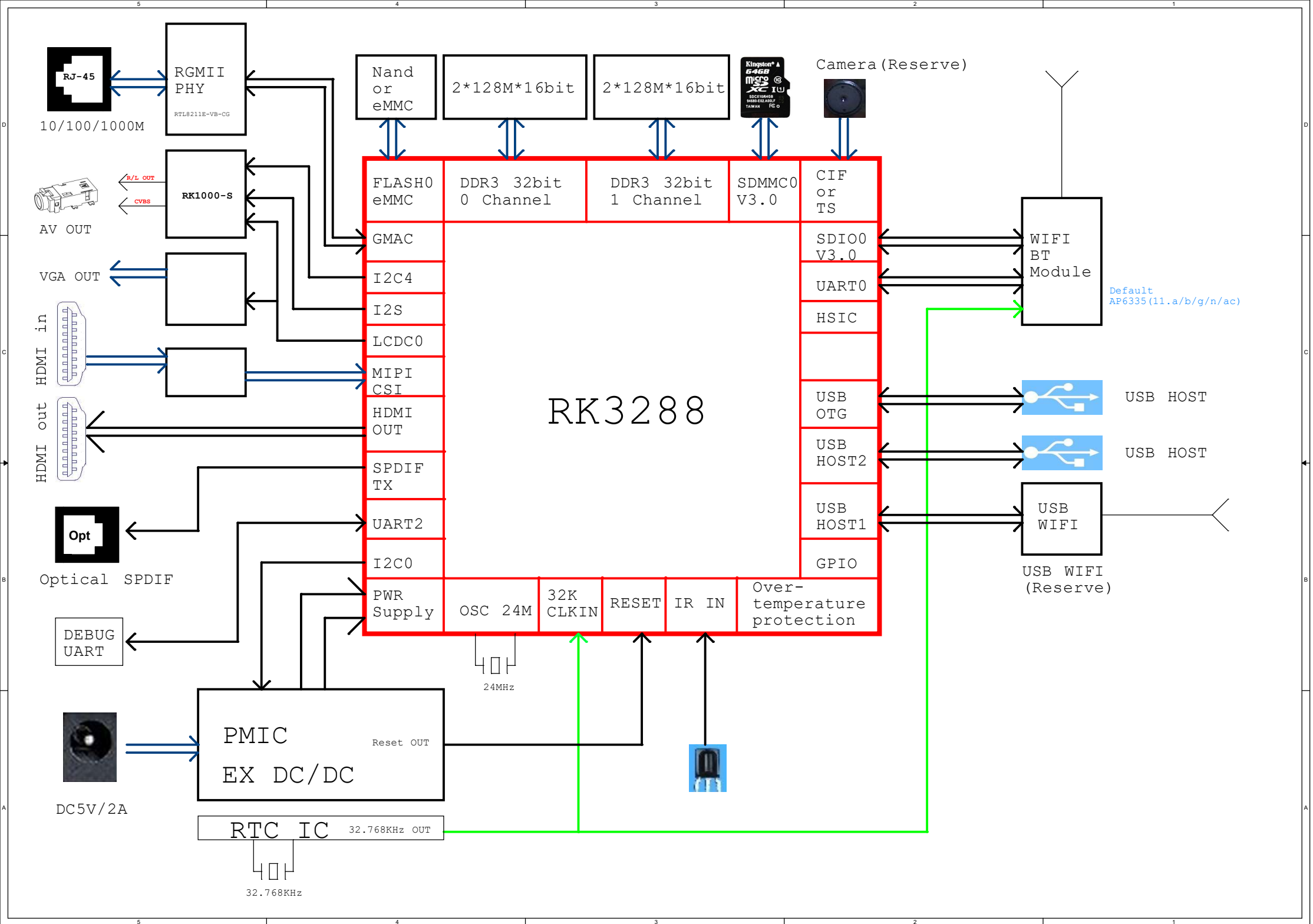






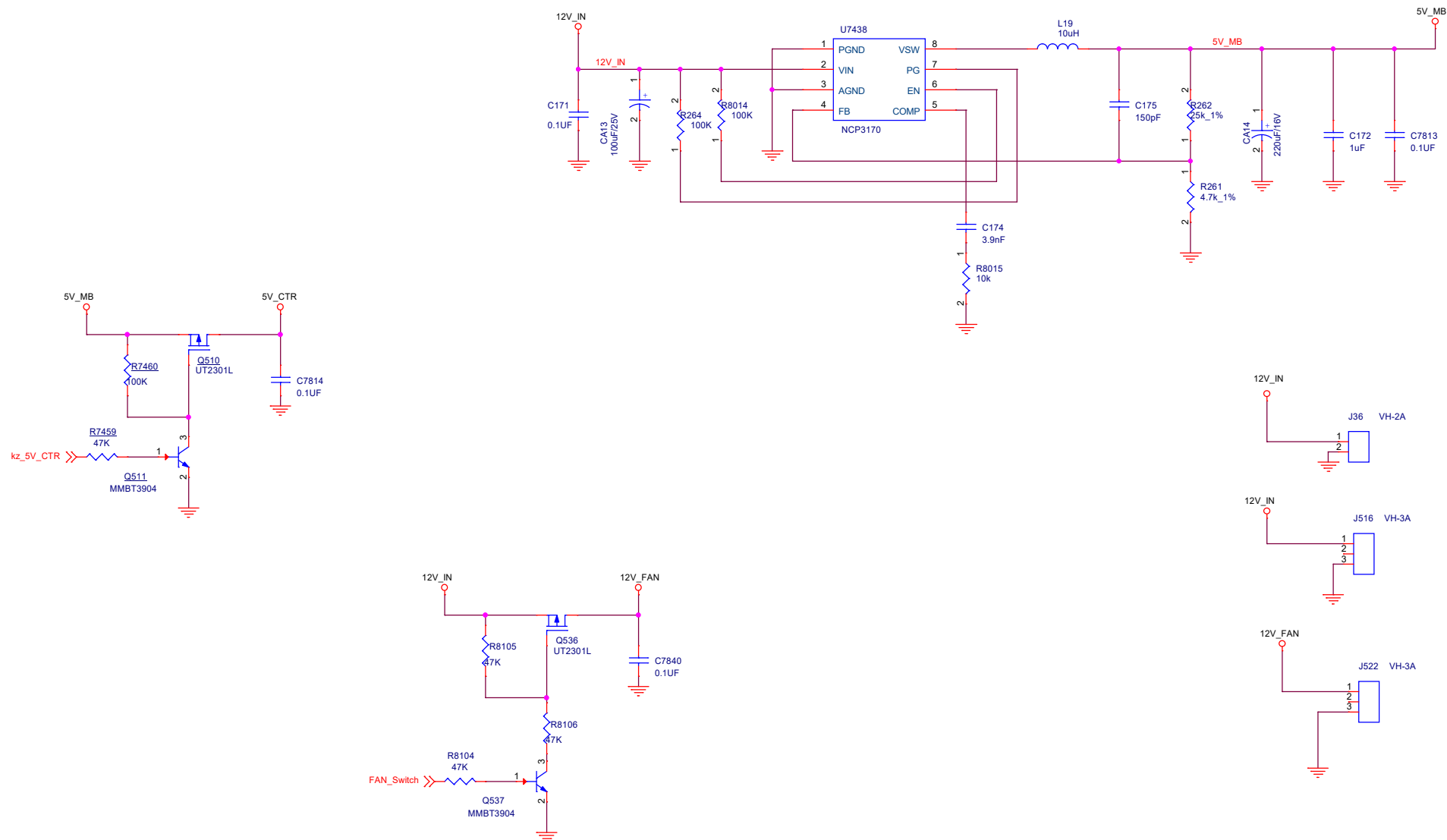
DDR Type	OutPut voltage	R2320	R2323
DDR3	1.5V	100K	200K
DDR3L	1.358V	43K	120K
LPDDR2/3	1.25V	49.9K	200K





[illegible]

Power Supply

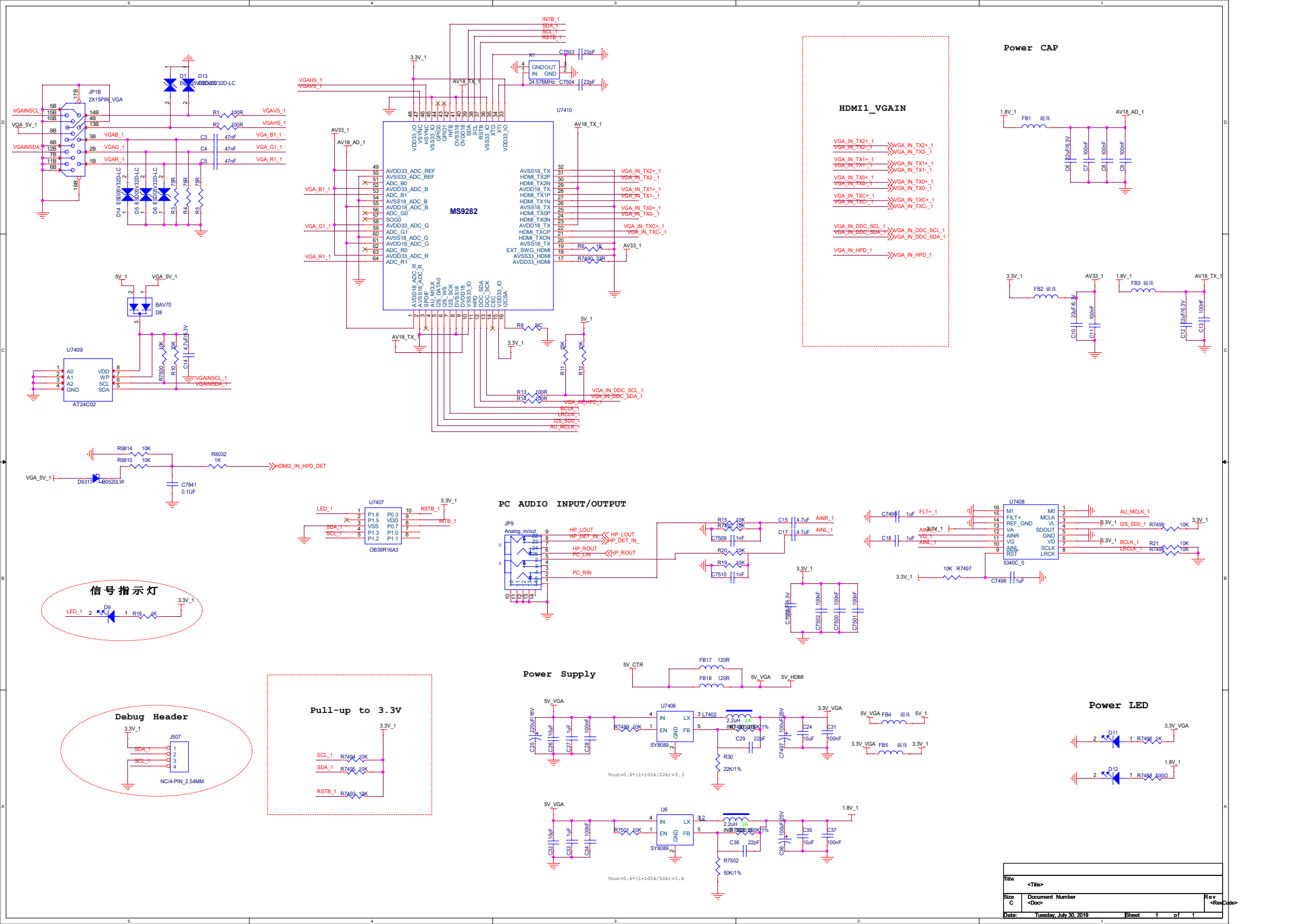


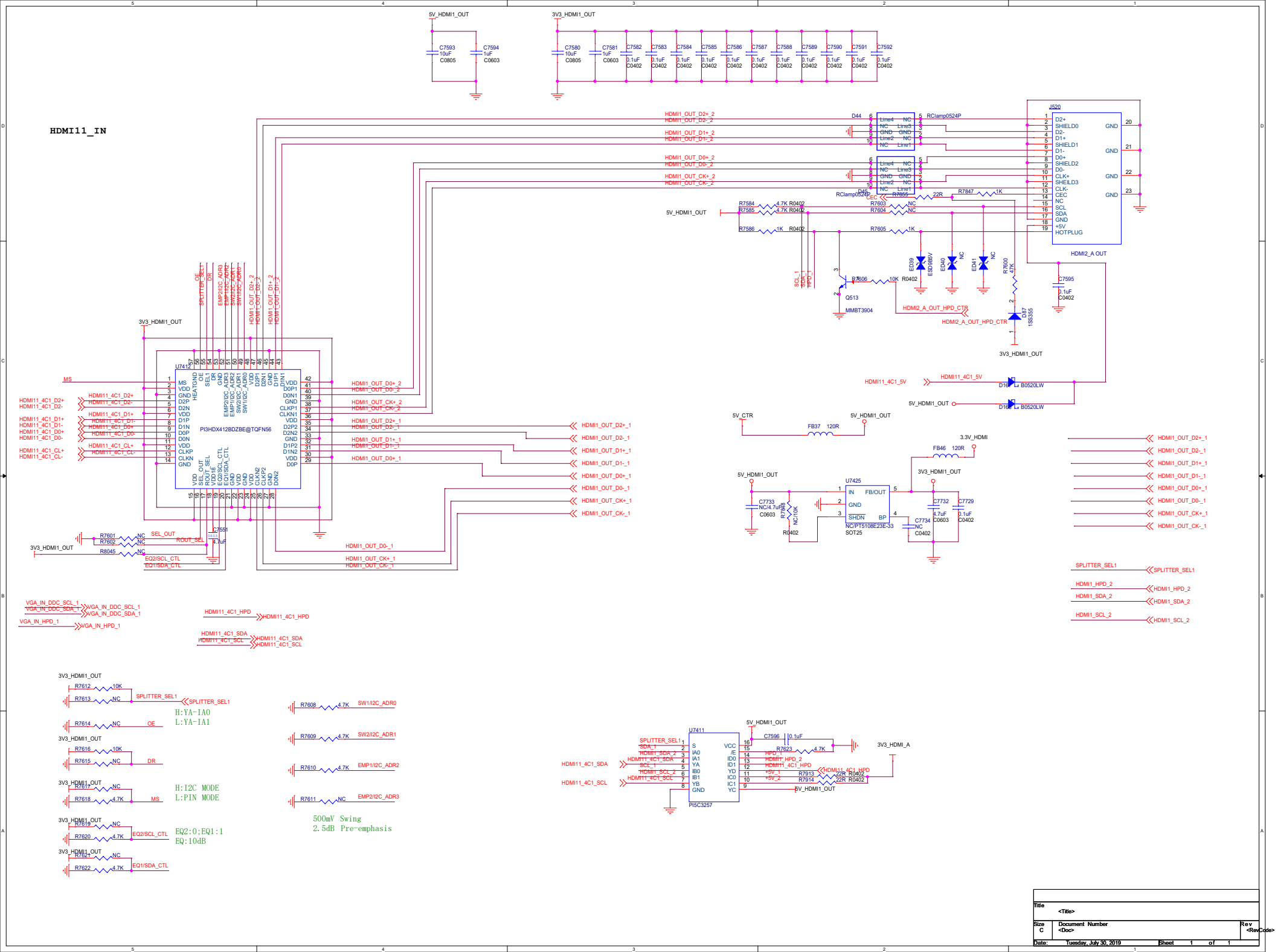
Content Indexing

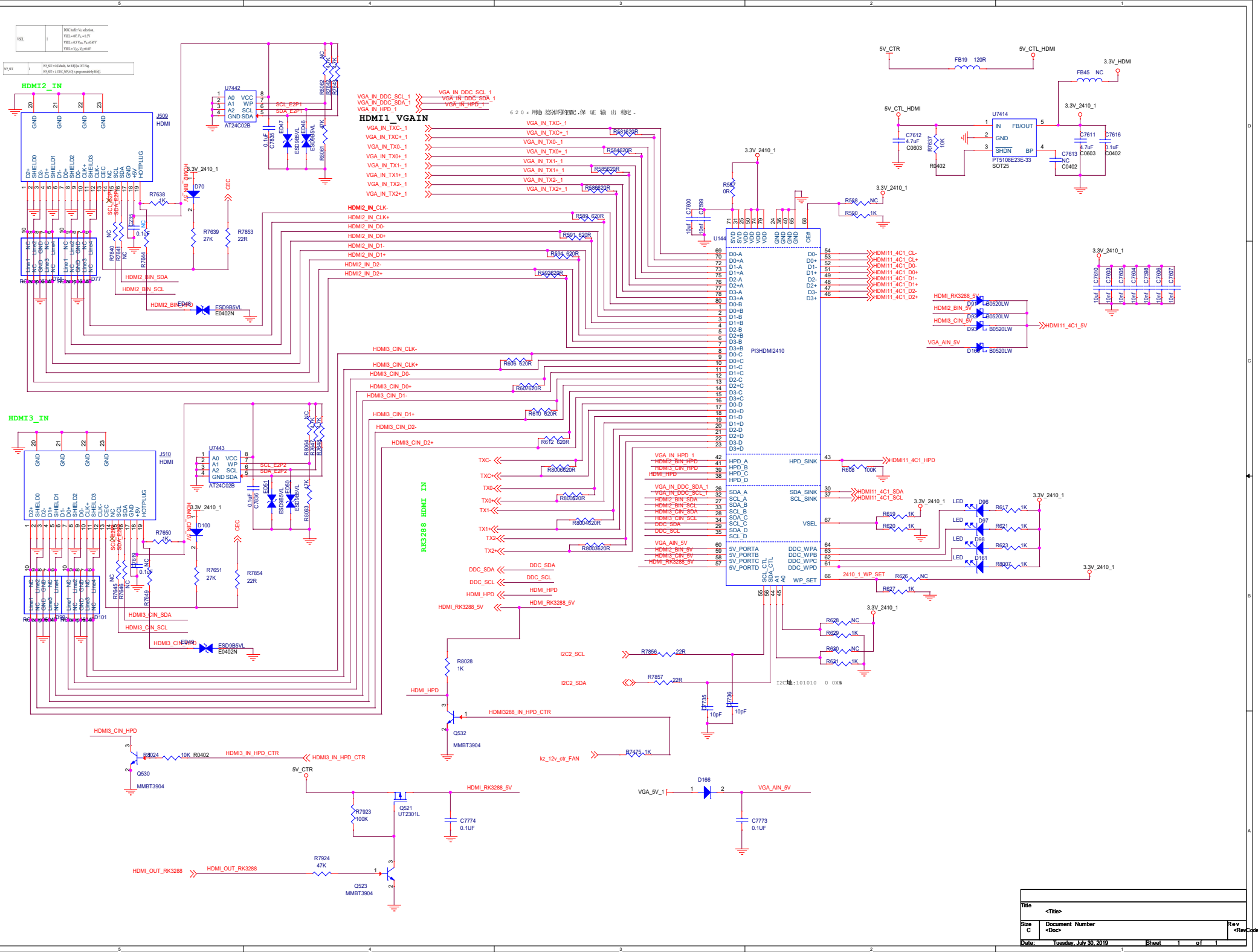
- 01.Index
- 02.Modify note
- 03.Block Diagram
- 04.Power Tree
- 05.System Power
- 06.RK3288 USB/HSIC Controler
- 07.USB HOST Port
- 08.RK3288 RAM Controler
- 09.RAM-DDR3-4X16bit
- 10.Nand FLASH/eMMC/TF Card
- 11.RK3288 GPIO/POWER
- 12.HDMI OUT
- 13.RK3288 LCDC/I2S Controler
- 14.RK1000-S-AV OUT
- 15.S/PDIF OUT
- 16.RK3288 Ethernet MAC Controler
- 17.10/100/1000M-PHY
- 18.RK3288 SDIO0 Controler
- 19.AP6XXX-WIFI/BT
- 20.USB WIFI-(option)
- 21.MIPI Interface
- 22.DTV-(Reserve)
- 23.HDMI IN
- 24.VGA OUT

6 LAYERS PCB STACK

TOP(Signal1)		Cu,thickness:0.7mil,Plating to 1oz
	FR4,thickness:3.8mil,Dielectric Constant:4.3	
GND1		Cu,thickness:1.5mil, 1oz
	FR4,thickness:8mil,Dielectric Constant:4.3	
POWER		Cu,thickness:1.5mil, 1oz
	FR4,thickness:adjust thickness according to the thickness of board ,Dielectric Constant:4.3	
Signal2		Cu,thickness:1.5mil, 1oz
	FR4,thickness:8mil,Dielectric Constant:4.3	
GND2		Cu,thickness:1.5mil, 1oz
	FR4,thickness:3.8mil,Dielectric Constant:4.3	
BOTTOM(Signal3)		Cu,thickness:0.7mil,Plating to 1oz

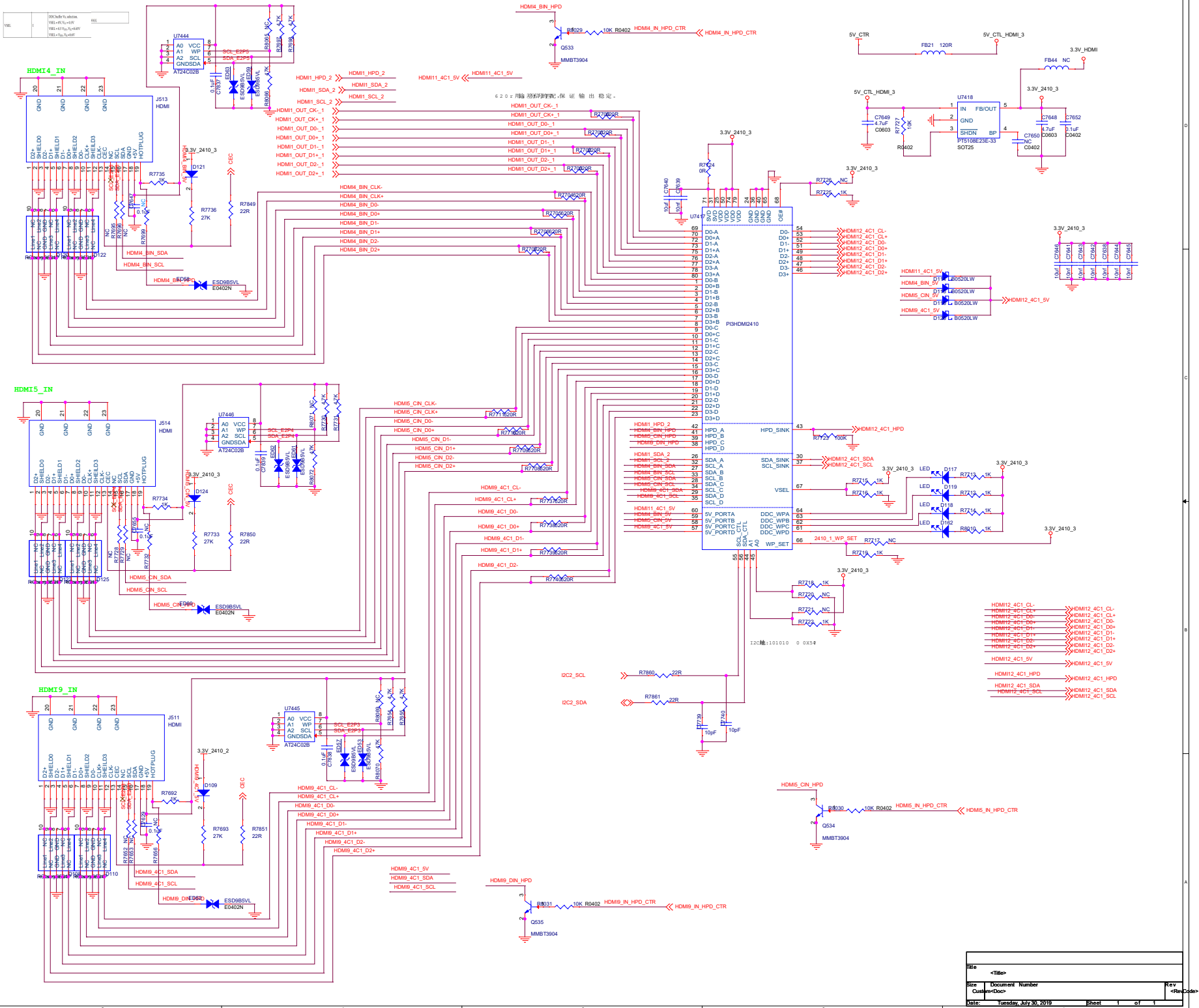




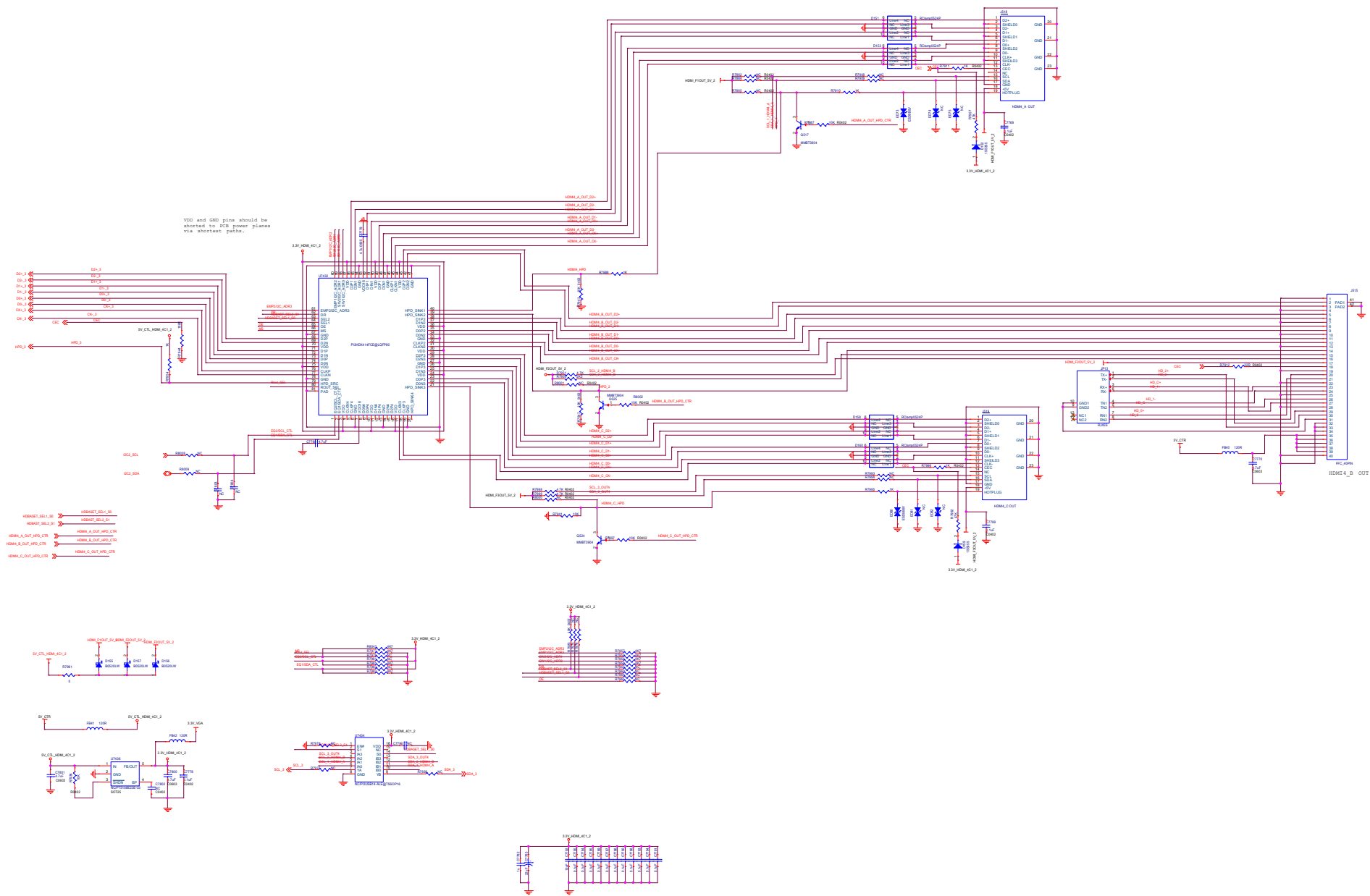


Title <Title>			
Size C	Document Number <Doc>		Rev <Rev Code>
Date:	Tuesday, July 30, 2019	Sheet 1 of 1	

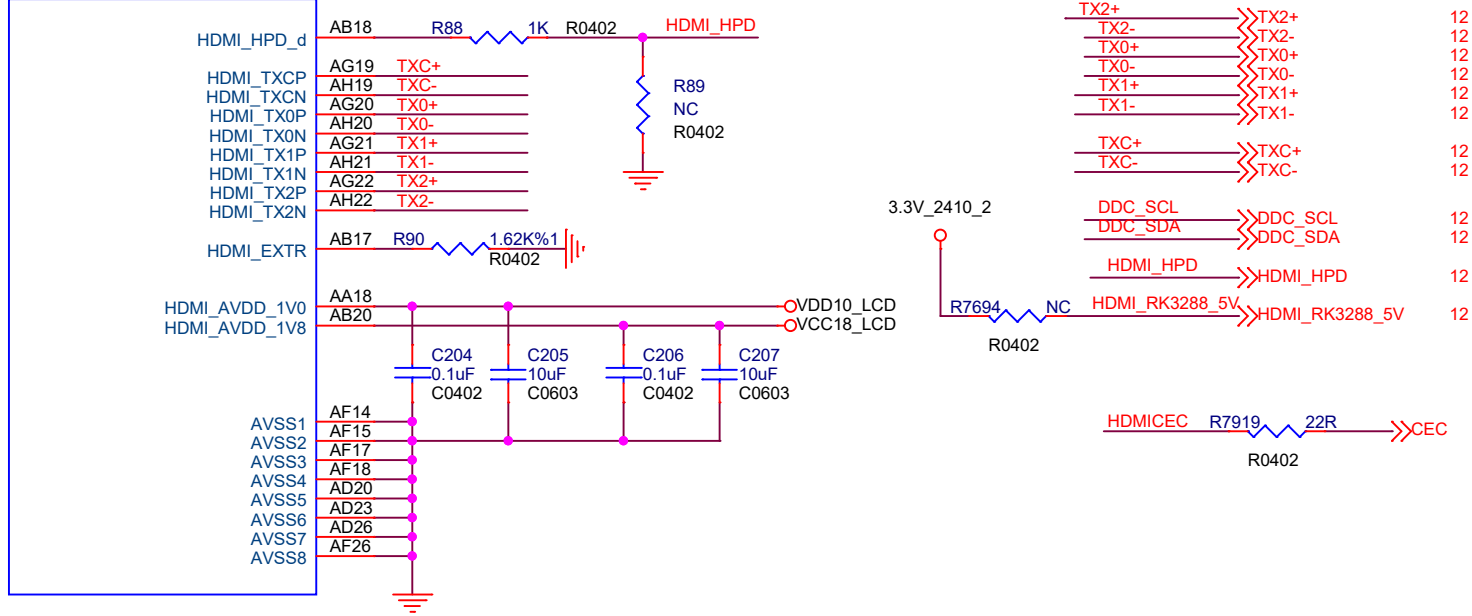
VSEL	1	IOC buffer V_{OL} selection. $VSEL = 0V, V_{OL} = 0.9V$ $VSEL = 0.5V_{DD}, V_{OL} = 0.49V$ $VSEL = V_{DD}, V_{OL} = 0V$	48L
------	---	--	-----



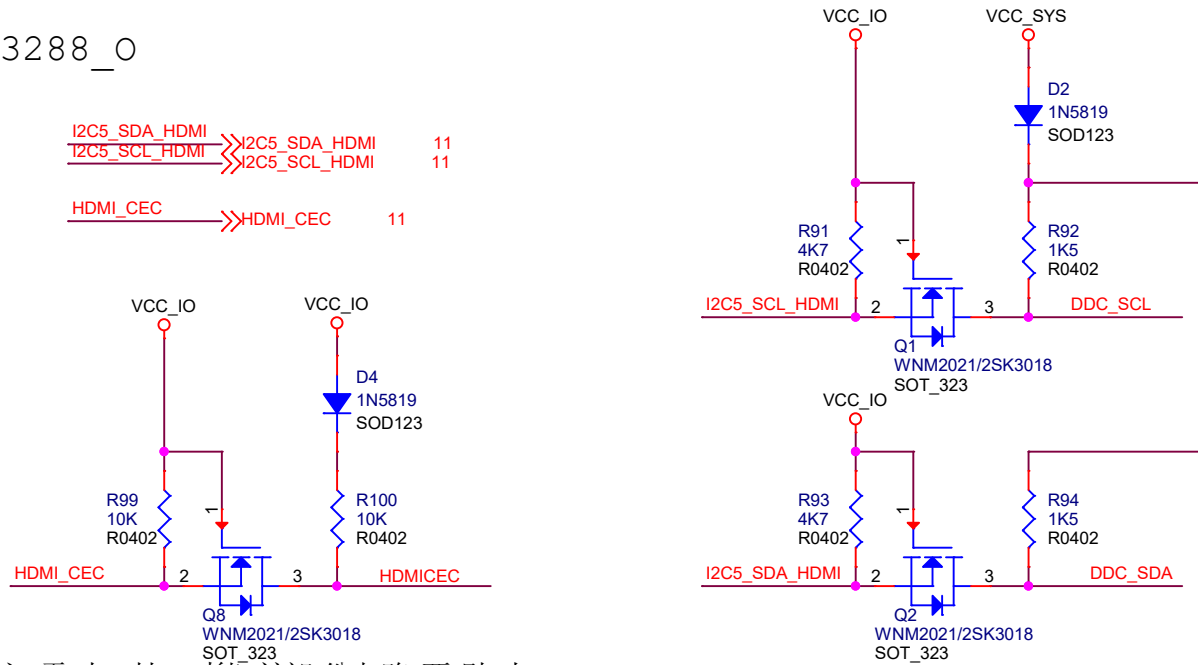
Title			
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Size	Document Number		Rev
Custom	<Doc>		<Rev Code>
Date:	Tuesday, July 30, 2019	Sheet 1 of 1	



U10
MCU RK3288

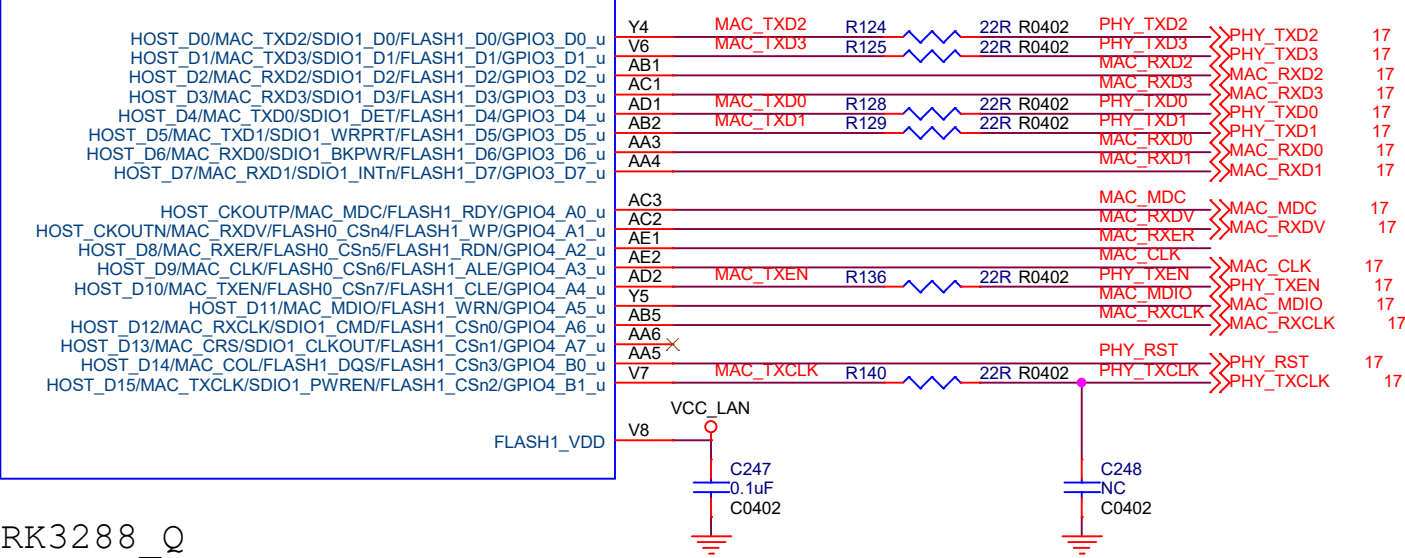


RK3288_O

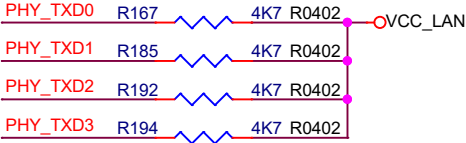
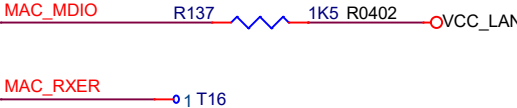


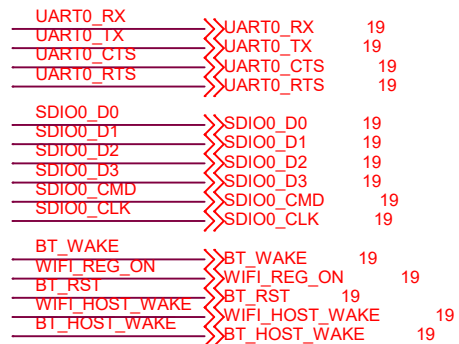
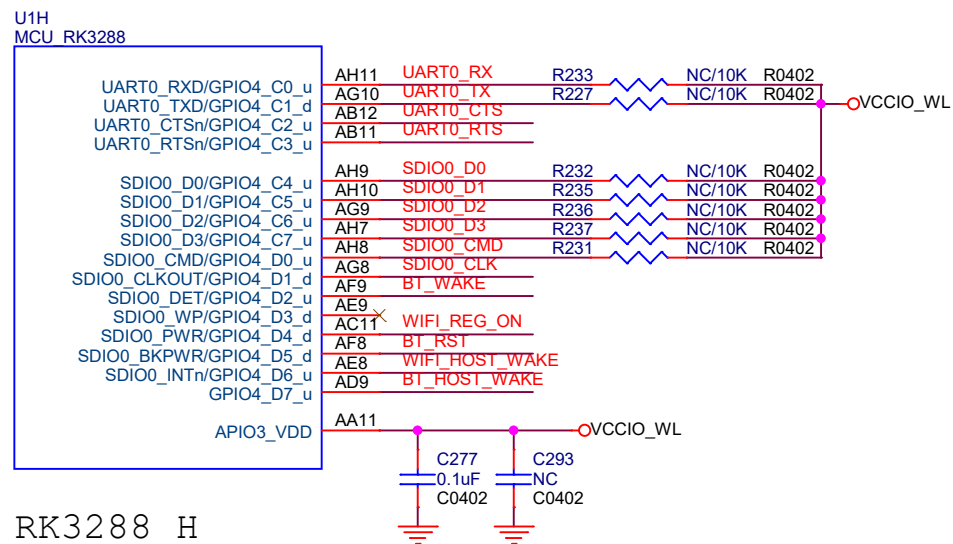
注 需 支 持 EC 功能 这 部 份 电 路 要 贴 上 。

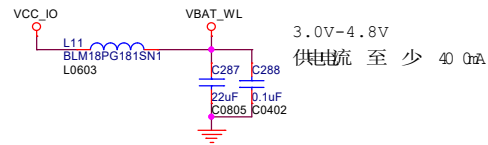
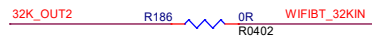
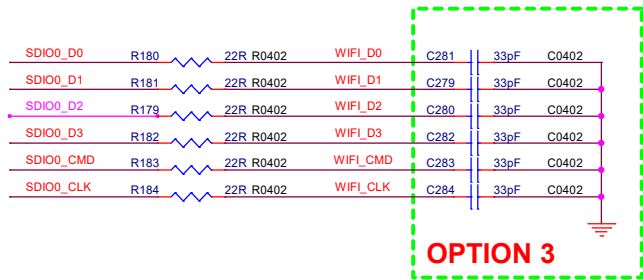
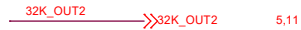
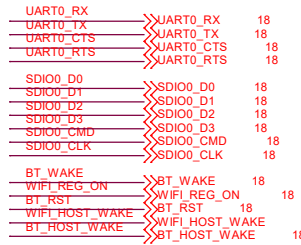
U1Q
MCU_RK3288



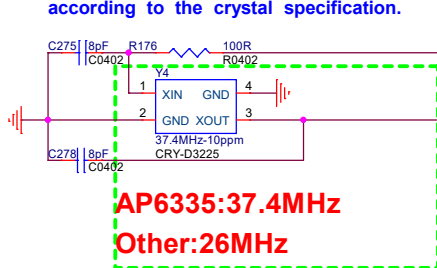
RK3288_Q



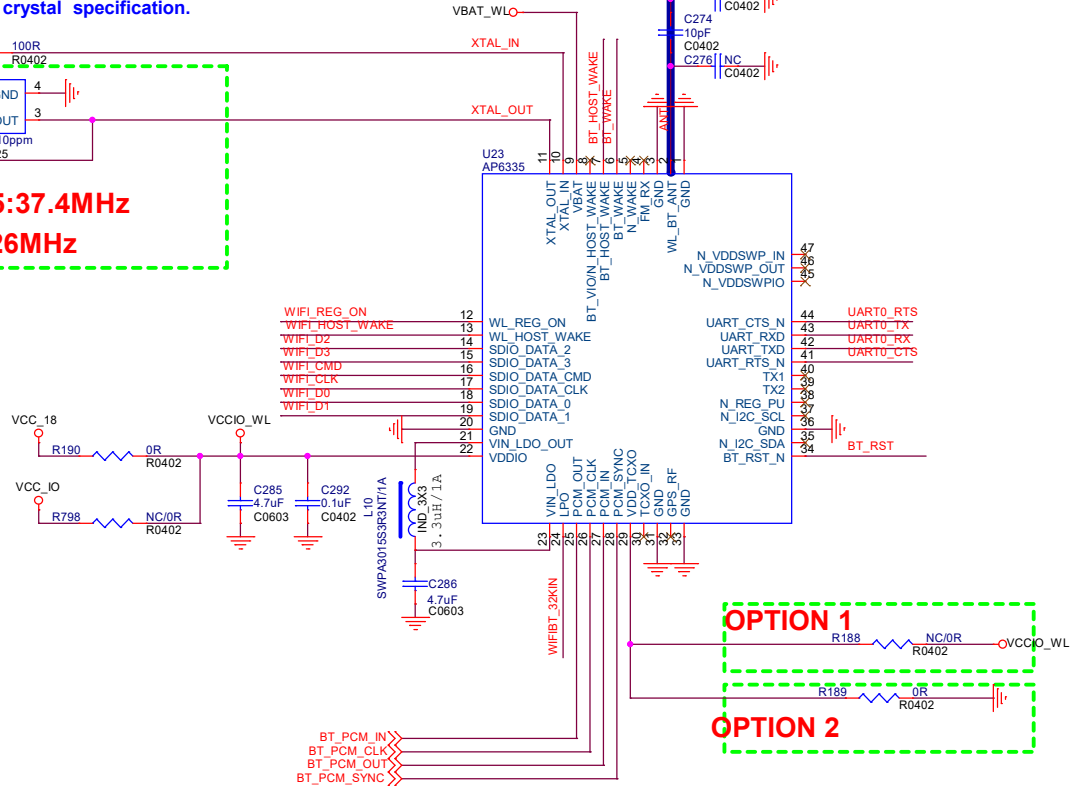




Note:
Adjusted the load capacitance
according to the crystal specification.



AP6335:37.4MHz
Other:26MHz



OPTION 1

OPTION 2

OPTION	WIFI				BT4.0	Crystals	VDDIO
	a	b/g/n	ac	5GHz			
AP6181		Yes				26MHz	1.71-3.6V
AP6212		Yes			Yes	26MHz	1.71-3.6V
XZ3538		Yes			Yes	26MHz	1.71-3.6V
XZ3660	Yes	Yes		Yes	Yes	26MHz	1.2-2.9V
AP6330	Yes	Yes		Yes	Yes	26MHz	1.2-2.9V
AP6335 (Default)	Yes	Yes	Yes	Yes	Yes	37.4MHz	1.71-3.63V

OPTION	1	2	3
AP6181	No	No	No
AP6212	No	No	No
XZ3538	No	No	No
XZ3660	No	No	No
AP6330	No	No	No
AP6335 (Default)	No	Yes	Yes

Note:
Yes: 框要 贴
No: 框内不贴

