Hey everyone,

Quiz 2 will be administered this upcoming Wednesday (September 12th) in recitation. The following topics may appear:

* Logic gates (NOT, AND, NAND, OR, NOR, XOR, and XNOR) and their truth table values
* Bitmasking and bitwise operators (please reference HW02)
* Deriving truth tables from circuits
* Deriving circuits from boolean expressions
* Multiplexers and decoders (inputs and outputs, inner-workings, etc.)
* Full adders
* Subtraction using adders

We will review some of these concepts at the beginning of tomorrow's lecitation. As you prepare, please feel free to ask questions on Piazza and in office hours.

Preston